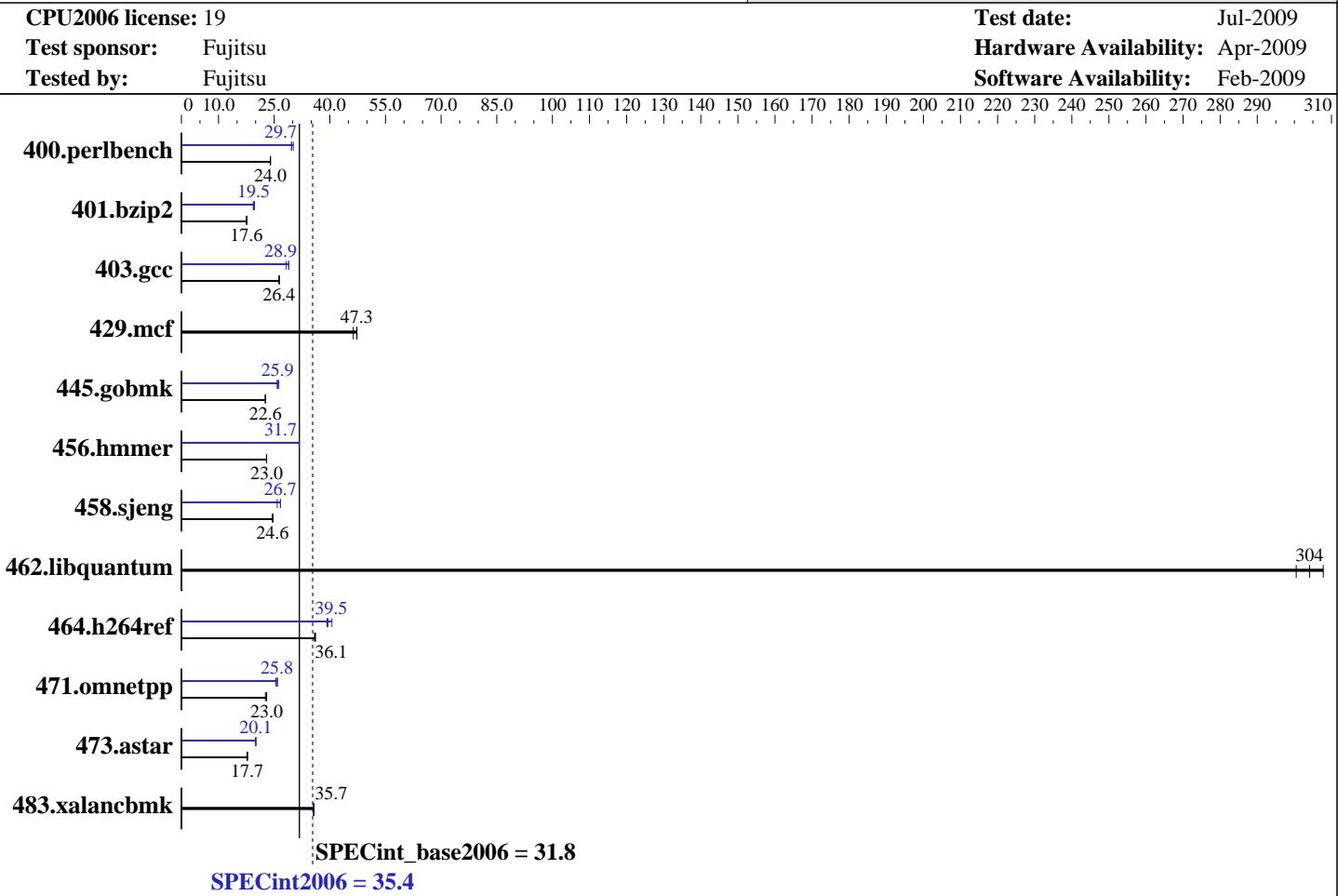# SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX200 S5, Intel Xeon X5570, 2.93 GHz

$SPECint®2006 =$ **35.4**

$SPECint\_base2006 =$ **31.8**

CPU2006 license: 19	**Test date:** Jul-2009
Test sponsor: Fujitsu	**Hardware Availability:** Apr-2009
Tested by: Fujitsu	**Software Availability:** Feb-2009

Benchmark results graph (scale 0 to 310):

- **400.perlbench** — 29.7 / 24.0
- **401.bzip2** — 19.5 / 17.6
- **403.gcc** — 28.9 / 26.4
- **429.mcf** — 47.3
- **445.gobmk** — 25.9 / 22.6
- **456.hmmer** — 31.7 / 23.0
- **458.sjeng** — 26.7 / 24.6
- **462.libquantum** — 304
- **464.h264ref** — 39.5 / 36.1
- **471.omnetpp** — 25.8 / 23.0
- **473.astar** — 20.1 / 17.7
- **483.xalancbmk** — 35.7

SPECint_base2006 = 31.8
SPECint2006 = 35.4

Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**408**	**24.0**	407	24.0	408	24.0	324	30.2	329	29.7	**329**	**29.7**
401.bzip2	**549**	**17.6**	548	17.6	549	17.6	498	19.4	**496**	**19.5**	490	19.7
403.gcc	306	26.3	305	26.4	**305**	**26.4**	285	28.3	**278**	**28.9**	278	28.9
429.mcf	197	46.3	193	47.3	**193**	**47.3**	197	46.3	193	47.3	**193**	**47.3**
445.gobmk	463	22.7	464	22.6	**464**	**22.6**	407	25.8	400	26.2	**404**	**25.9**
456.hmmer	**406**	**23.0**	407	22.9	406	23.0	294	31.7	**294**	**31.7**	294	31.7
458.sjeng	493	24.6	**492**	**24.6**	491	24.6	469	25.8	**454**	**26.7**	453	26.7
462.libquantum	69.0	300	67.3	308	**68.1**	**304**	69.0	300	67.3	308	**68.1**	**304**
464.h264ref	**614**	**36.1**	617	35.9	613	36.1	564	39.3	545	40.6	**560**	**39.5**
471.omnetpp	276	22.6	**272**	**23.0**	272	23.0	246	25.4	241	25.9	**242**	**25.8**
473.astar	394	17.8	397	17.7	**396**	**17.7**	352	20.0	**350**	**20.1**	349	20.1
483.xalancbmk	**194**	**35.7**	193	35.7	194	35.6	**194**	**35.7**	193	35.7	194	35.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

> 401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

> 456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

> 458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
```
icpc
```

> 473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

> 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
> 401.bzip2: `-DSPEC_CPU_LP64`
> 456.hmmer: `-DSPEC_CPU_LP64`
> 458.sjeng: `-DSPEC_CPU_LP64`
> 462.libquantum: `-DSPEC_CPU_LINUX`
> 473.astar: `-DSPEC_CPU_LP64`
> 483.xalancbmk: `-DSPEC_CPU_LINUX`

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CPU2006 license: 19	**Test date:** Jul-2009
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Tested by: Fujitsu	**Software Availability:** Feb-2009

Peak Optimization Flags

C benchmarks:

    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

    403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
               -opt-malloc-options=3

    429.mcf: basepeak = yes

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

    462.libquantum: basepeak = yes

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/spec/cpu2006.1.1/lib -lsmartheap

    473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
               -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

    483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

      403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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