



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 130

### PRIMERGY RX300 S5, Intel Xeon X5570, 2.93 GHz

### SPECint\_rate\_base2006 = 122

CPU2006 license: 19

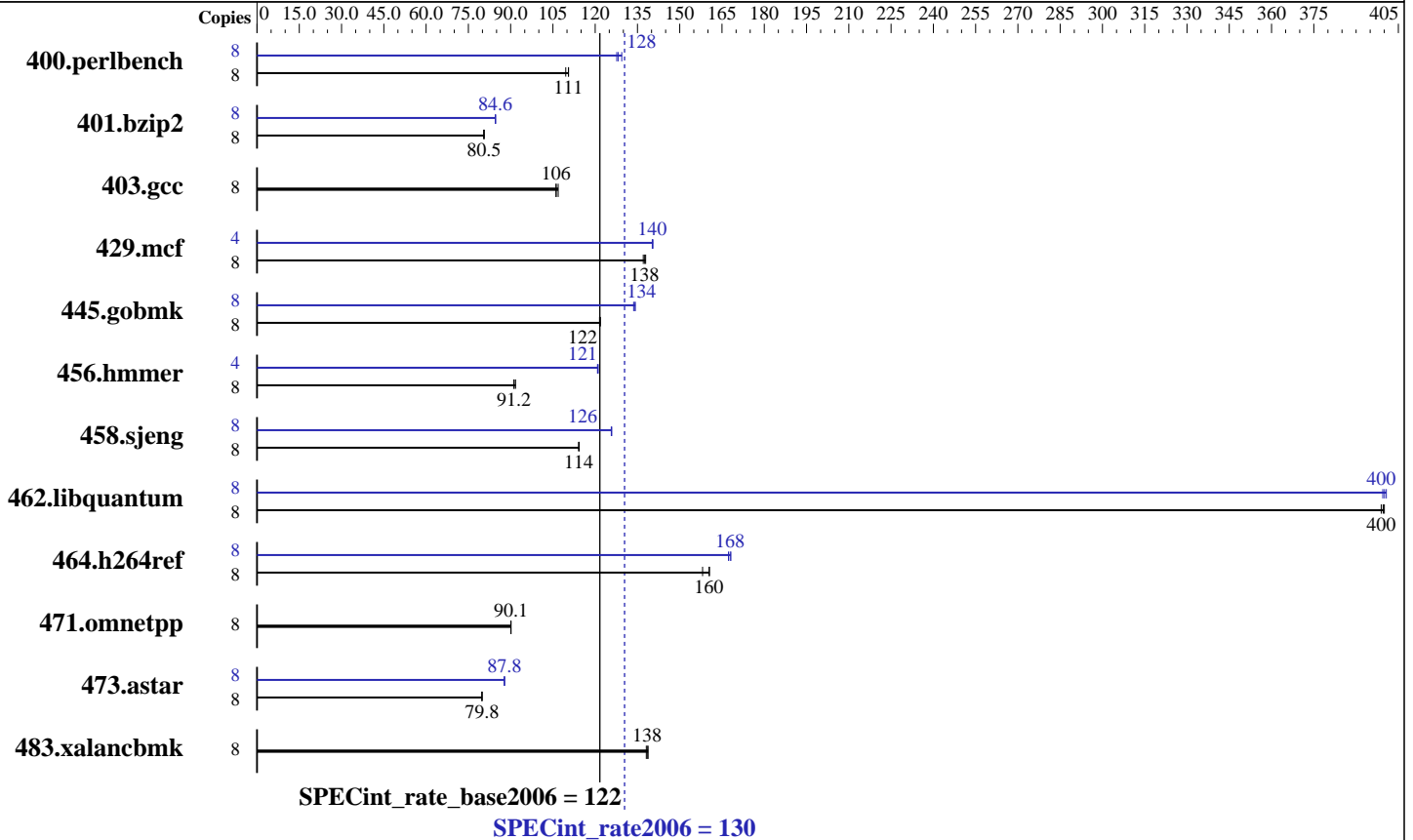
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 12 GB (3x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	707	111	<b>707</b>	<b>111</b>	713	110	8	604	129	<b>610</b>	<b>128</b>	612	128
401.bzip2	8	960	80.5	<b>959</b>	<b>80.5</b>	957	80.6	8	913	84.5	912	84.7	<b>913</b>	<b>84.6</b>
403.gcc	8	<b>607</b>	<b>106</b>	603	107	607	106	8	<b>607</b>	<b>106</b>	603	107	607	106
429.mcf	8	532	137	<b>530</b>	<b>138</b>	529	138	4	260	140	260	140	<b>260</b>	<b>140</b>
445.gobmk	8	689	122	690	122	<b>690</b>	<b>122</b>	8	<b>627</b>	<b>134</b>	628	134	625	134
456.hammer	8	814	91.7	818	91.2	<b>818</b>	<b>91.2</b>	4	309	121	308	121	<b>308</b>	<b>121</b>
458.sjeng	8	848	114	847	114	<b>848</b>	<b>114</b>	8	769	126	770	126	<b>770</b>	<b>126</b>
462.libquantum	8	<b>415</b>	<b>400</b>	416	399	414	400	8	415	399	<b>414</b>	<b>400</b>	414	401
464.h264ref	8	1120	158	<b>1104</b>	<b>160</b>	1103	161	8	<b>1055</b>	<b>168</b>	1053	168	1058	167
471.omnetpp	8	<b>555</b>	<b>90.1</b>	555	90.1	555	90.1	8	<b>555</b>	<b>90.1</b>	555	90.1	555	90.1
473.astar	8	<b>704</b>	<b>79.8</b>	703	79.9	704	79.8	8	<b>640</b>	<b>87.8</b>	639	87.8	640	87.7
483.xalancbmk	8	398	139	<b>399</b>	<b>138</b>	400	138	8	398	139	<b>399</b>	<b>138</b>	400	138

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result was measured on the PRIMERGY RX300 S5. The PRIMERGY TX300 S5 and the PRIMERGY RX300 S5 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc`

`456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc`

`458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):

`icpc`

`473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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