



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = Not Run

PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECint\_base2006 = 16.6

CPU2006 license: 55

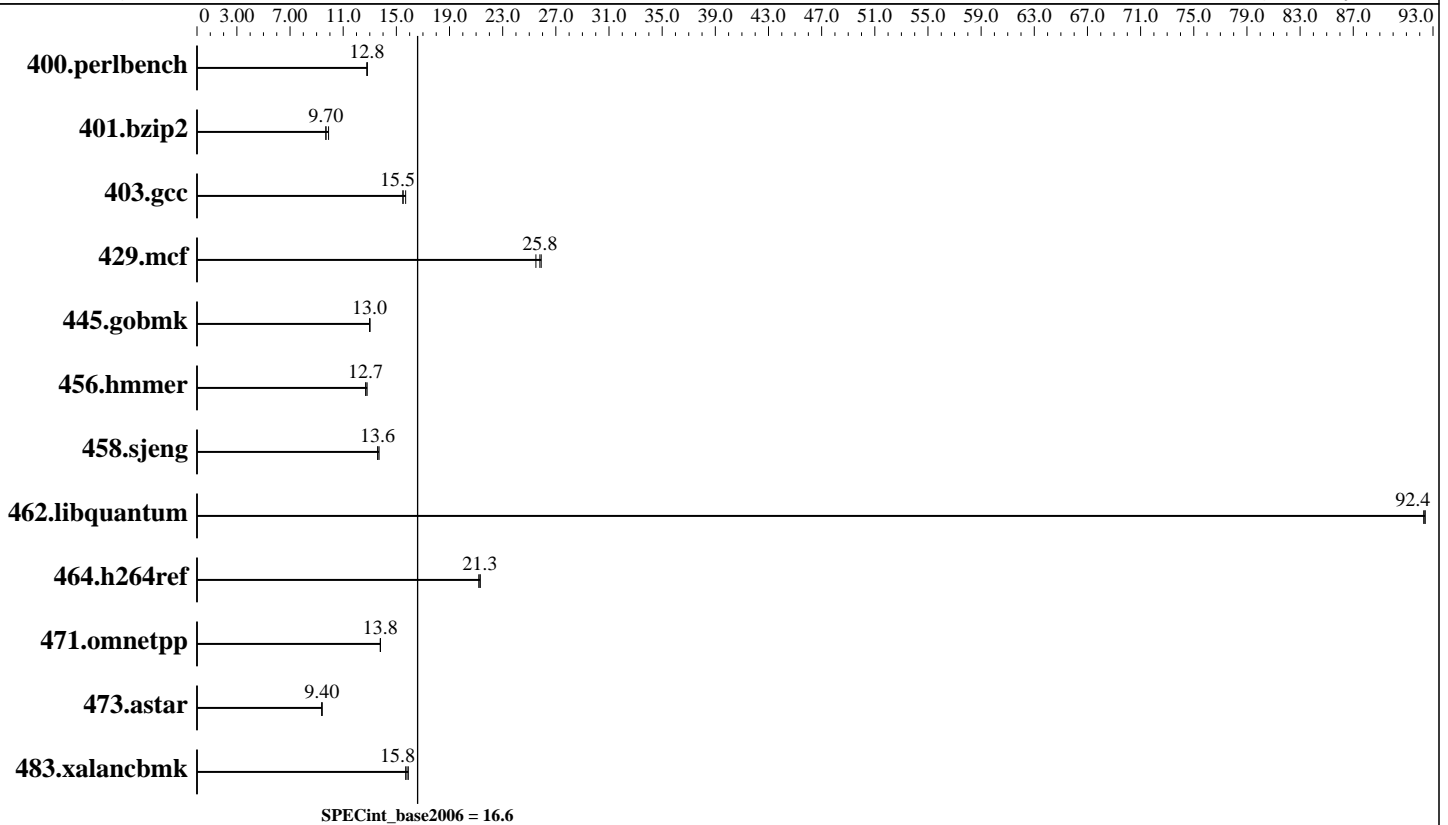
Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: May-2009



## Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 4 GB (2 x 2 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)  
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS  
 Other Hardware: None

## Software

Operating System: Microsoft Windows Server 2008 Enterprise x64 Edition SP2  
 Compiler: Intel C++ Compiler for IA-32, Version 11.0 Build 20090131 Package ID: w\_eproc\_p\_11.0.072 Microsoft Visual Studio 2008 SP1  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	766	12.8	762	12.8	<b>763</b>	<b>12.8</b>						
401.bzip2	974	9.90	<b>993</b>	<b>9.70</b>	996	9.70						
403.gcc	<b>519</b>	<b>15.5</b>	512	15.7	519	15.5						
429.mcf	358	25.5	352	25.9	<b>354</b>	<b>25.8</b>						
445.gobmk	808	13.0	809	13.0	<b>808</b>	<b>13.0</b>						
456.hammer	<b>735</b>	<b>12.7</b>	732	12.8	737	12.7						
458.sjeng	<b>887</b>	<b>13.6</b>	886	13.7	890	13.6						
462.libquantum	224	92.4	<b>224</b>	<b>92.4</b>	224	92.3						
464.h264ref	1041	21.3	<b>1041</b>	<b>21.3</b>	1043	21.2						
471.omnetpp	452	13.8	<b>453</b>	<b>13.8</b>	453	13.8						
473.astar	748	9.40	745	9.40	<b>748</b>	<b>9.40</b>						
483.xalancbmk	440	15.7	<b>437</b>	<b>15.8</b>	435	15.9						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries were built on Windows Vista Business (64-bit)

## Base Compiler Invocation

C benchmarks:  
icl -Qvc9 -Qstd=c99  
C++ benchmarks:  
icl -Qvc9

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword

## Base Optimization Flags

C benchmarks:  
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel  
-Qpar-runtime-control -Qvec-guard-write /F512000000

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## Base Optimization Flags (Continued)

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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