



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 38.5

CELSIUS R670, Intel Xeon W5590

SPECint_base2006 = 34.2

CPU2006 license: 19

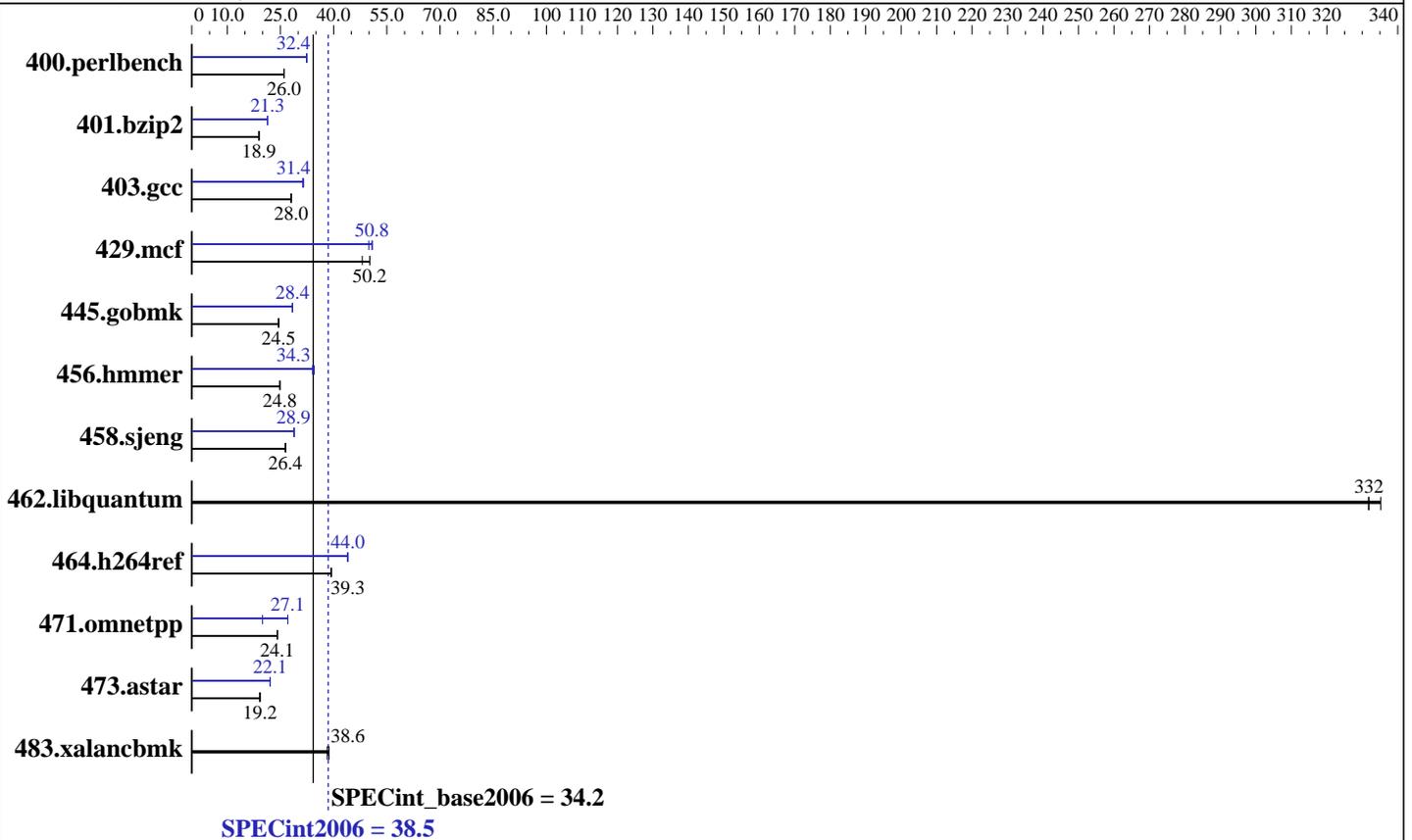
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2009

Hardware Availability: Aug-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon W5590
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB PC3 10600R, 2 rank, CL9-9-9, ECC)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	375	26.1	<u>376</u>	<u>26.0</u>	376	26.0	302	32.3	<u>301</u>	<u>32.4</u>	301	32.5
401.bzip2	508	19.0	<u>509</u>	<u>18.9</u>	510	18.9	<u>452</u>	<u>21.3</u>	453	21.3	450	21.4
403.gcc	287	28.1	<u>287</u>	<u>28.0</u>	287	28.0	255	31.5	257	31.3	<u>256</u>	<u>31.4</u>
429.mcf	190	48.1	182	50.2	<u>182</u>	<u>50.2</u>	183	49.9	<u>180</u>	<u>50.8</u>	179	50.9
445.gobmk	<u>428</u>	<u>24.5</u>	428	24.5	429	24.5	<u>369</u>	<u>28.4</u>	370	28.4	369	28.4
456.hammer	376	24.8	374	25.0	<u>376</u>	<u>24.8</u>	<u>272</u>	<u>34.3</u>	274	34.1	270	34.5
458.sjeng	456	26.5	<u>458</u>	<u>26.4</u>	459	26.4	419	28.9	420	28.8	<u>419</u>	<u>28.9</u>
462.libquantum	61.8	335	<u>62.4</u>	<u>332</u>	62.5	332	61.8	335	<u>62.4</u>	<u>332</u>	62.5	332
464.h264ref	<u>563</u>	<u>39.3</u>	564	39.2	562	39.4	504	43.9	503	44.0	<u>503</u>	<u>44.0</u>
471.omnetpp	<u>259</u>	<u>24.1</u>	258	24.2	259	24.1	<u>231</u>	<u>27.1</u>	313	20.0	231	27.1
473.astar	364	19.3	367	19.1	<u>365</u>	<u>19.2</u>	317	22.1	<u>317</u>	<u>22.1</u>	318	22.1
483.xalancbmk	178	38.7	<u>179</u>	<u>38.6</u>	181	38.2	178	38.7	<u>179</u>	<u>38.6</u>	181	38.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores.
KMP_AFFINITY set to granularity=fine,scatter

Platform Notes

BIOS configuration:
Hyper-Threading Technology = Disabled
Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

General Notes

For information about Fujitsu please visit:
<http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
C++ benchmarks (except as noted below):
icpc
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch
401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090818.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090818.00.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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