



SPEC[®] CINT2006 Result

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Lenovo Group Limited

SPECint[®]2006 = 23.0

Lenovo ThinkServer RD120(Intel Xeon E5430)

SPECint_base2006 = 22.2

CPU2006 license: 9017

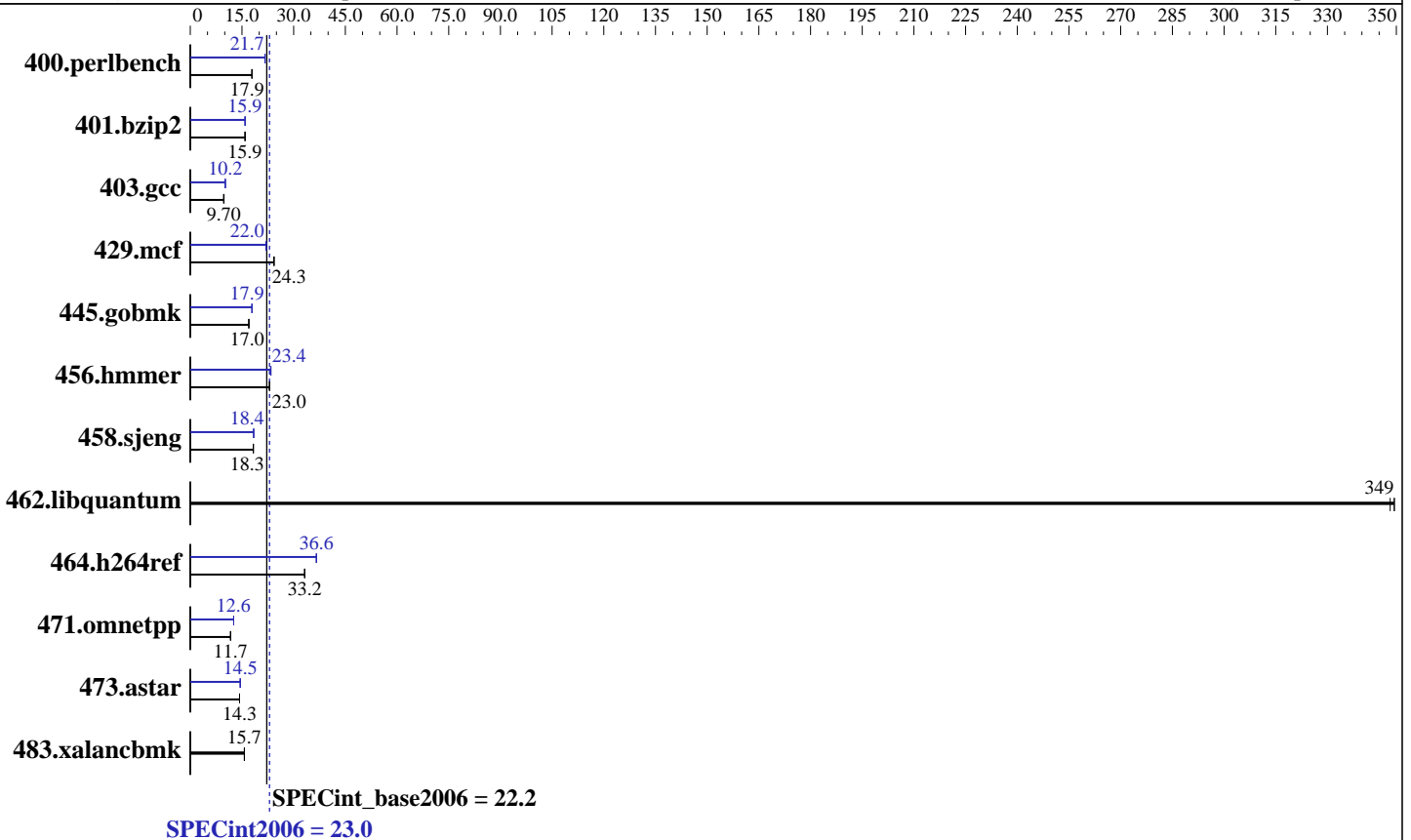
Test date: Apr-2009

Test sponsor: Lenovo Group Limited

Hardware Availability: Apr-2009

Tested by: Lenovo Group Limited

Software Availability: Apr-2009



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB Hynix 2Rx8 PC2 5300F)

Disk Subsystem: 1 x 160 GB, SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 R2, Standard x64 Edition
 Compiler: Intel C++ Compiler Professional 11.0 for Intel64
 Build 20080930 Package ID: w_cproc_p_11.0.072
 Microsoft Visual Studio 2008 (for libraries)

Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.0 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	545	17.9	545	17.9	<u>545</u>	<u>17.9</u>	449	21.7	<u>450</u>	<u>21.7</u>	450	21.7
401.bzip2	607	15.9	607	15.9	<u>607</u>	<u>15.9</u>	<u>606</u>	<u>15.9</u>	606	15.9	607	15.9
403.gcc	826	9.70	827	9.70	823	9.80	797	10.1	<u>793</u>	<u>10.2</u>	791	10.2
429.mcf	375	24.3	<u>375</u>	<u>24.3</u>	375	24.3	<u>414</u>	<u>22.0</u>	413	22.1	415	22.0
445.gobmk	616	17.0	<u>616</u>	<u>17.0</u>	616	17.0	<u>585</u>	<u>17.9</u>	585	17.9	586	17.9
456.hammer	405	23.0	405	23.0	<u>405</u>	<u>23.0</u>	399	23.4	401	23.3	<u>399</u>	<u>23.4</u>
458.sjeng	661	18.3	660	18.3	<u>660</u>	<u>18.3</u>	656	18.4	<u>656</u>	<u>18.4</u>	658	18.4
462.libquantum	59.3	349	59.5	348	<u>59.3</u>	<u>349</u>	59.3	349	59.5	348	<u>59.3</u>	<u>349</u>
464.h264ref	667	33.2	<u>667</u>	<u>33.2</u>	667	33.2	<u>604</u>	<u>36.6</u>	604	36.6	604	36.6
471.omnetpp	532	11.7	532	11.7	<u>532</u>	<u>11.7</u>	495	12.6	<u>496</u>	<u>12.6</u>	496	12.6
473.astar	492	14.3	492	14.3	<u>492</u>	<u>14.3</u>	485	14.5	485	14.5	<u>485</u>	<u>14.5</u>
483.xalancbmk	<u>439</u>	<u>15.7</u>	439	15.7	439	15.7	<u>439</u>	<u>15.7</u>	439	15.7	439	15.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
 -DSPEC_CPU_NO_NEED_VA_COPY
 401.bzip2: -DSPEC_CPU_P64
 403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
 429.mcf: -DSPEC_CPU_P64
 445.gobmk: -DSPEC_CPU_P64
 456.hammer: -DSPEC_CPU_P64
 458.sjeng: -DSPEC_CPU_P64
 462.libquantum: -DSPEC_CPU_P64
 464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
 471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
 473.astar: -DSPEC_CPU_P64
 483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write -Qauto-ilp32 /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64Mt.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW64Mt.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlW64Mt.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20090721.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20090721.xml>



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