



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 240

ACTINA SOLAR 820 S4 (Intel Xeon X5560)

SPECint_rate_base2006 = 223

CPU2006 license: 9008

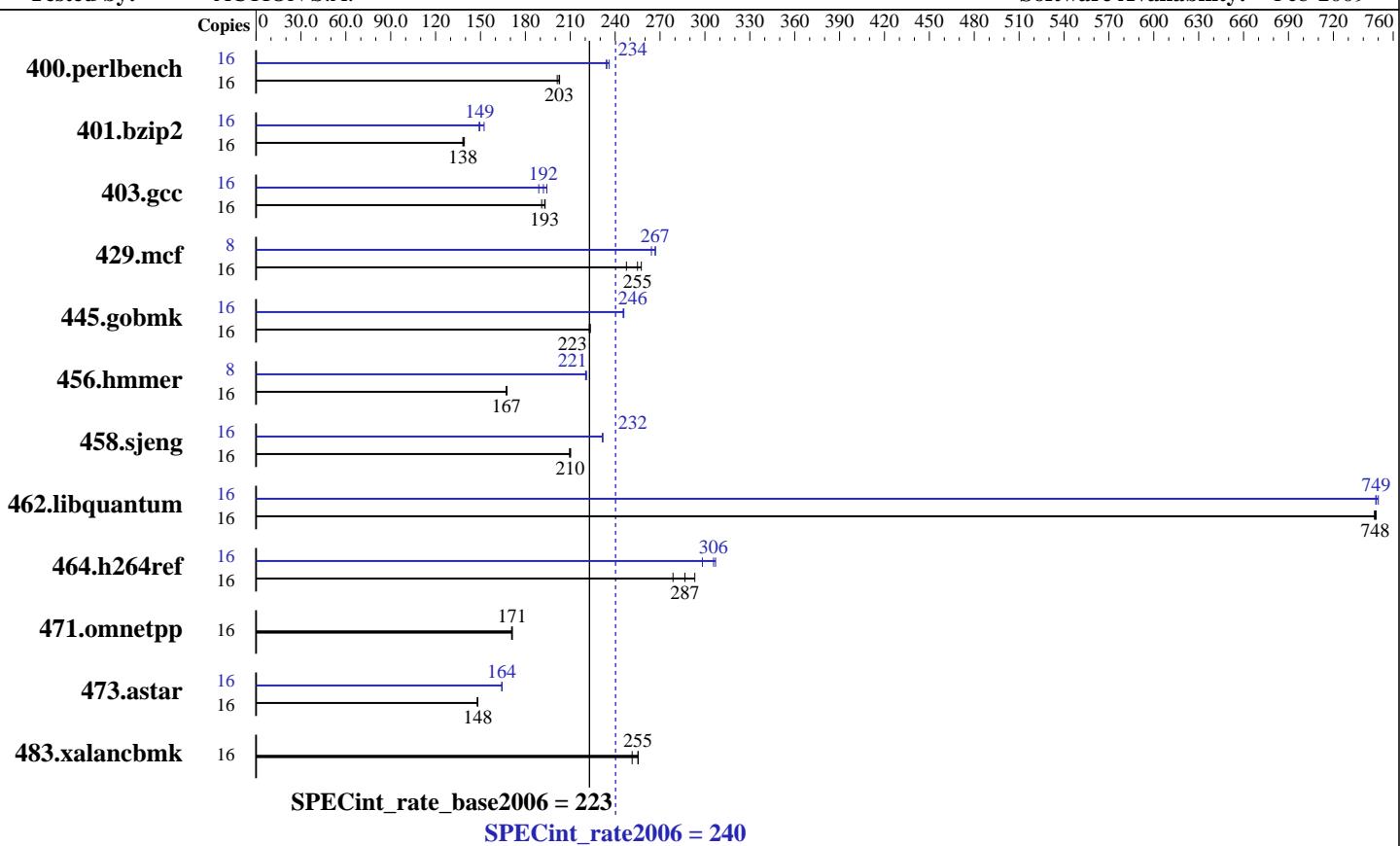
Test date: May-2009

Test sponsor: ACTION S.A.

Hardware Availability: Apr-2009

Tested by: ACTION S.A.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5560
CPU Characteristics: Intel Turbo Boost Technology up to 3.2 GHz
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x 4 GB DDR3-1333 DR RDIMM)
Disk Subsystem: 147 GB SAS, 15000 RPM
Other Hardware: None

Software

Operating System: Scientific Linux 5.3 (x86_64)
Compiler: kernel 2.6.18.128.1.1.el5
Auto Parallel: Intel C++ Compiler 11.0 for Linux
File System: Build 20080930 Package ID: l_cproc_p_11.0.066
System State: No
Base Pointers: ext3
Peak Pointers: Run level 3 (multi-user)
Other Software: 32-bit
32/64-bit
Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	777	201	772	203	771	203	16	668	234	662	236	667	234
401.bzip2	16	1116	138	1116	138	1109	139	16	1036	149	1033	149	1013	152
403.gcc	16	675	191	667	193	668	193	16	671	192	682	189	663	194
429.mcf	16	590	248	567	258	572	255	8	276	264	273	267	274	267
445.gobmk	16	751	223	753	223	753	223	16	683	246	683	246	684	245
456.hammer	16	893	167	890	168	892	167	8	338	221	339	220	338	221
458.sjeng	16	922	210	921	210	924	209	16	835	232	836	232	836	231
462.libquantum	16	444	747	443	748	443	749	16	443	748	443	749	442	750
464.h264ref	16	1207	293	1271	279	1235	287	16	1187	298	1153	307	1158	306
471.omnetpp	16	584	171	586	171	584	171	16	584	171	586	171	584	171
473.astar	16	760	148	758	148	758	148	16	684	164	683	164	684	164
483.xalancbmk	16	439	251	432	255	433	255	16	439	251	432	255	433	255

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

'numactl' was used to bind copies to the cores.

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmr: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmr: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
          -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
          -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
             -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
            -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes
```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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