



# SPEC® CFP2006 Result

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## Intel Corporation

### SPECfp®\_rate2006 = 28.8

Intel DQ45CB motherboard (Intel Core 2 Duo E7400)

### SPECfp\_rate\_base2006 = 27.9

CPU2006 license: 13

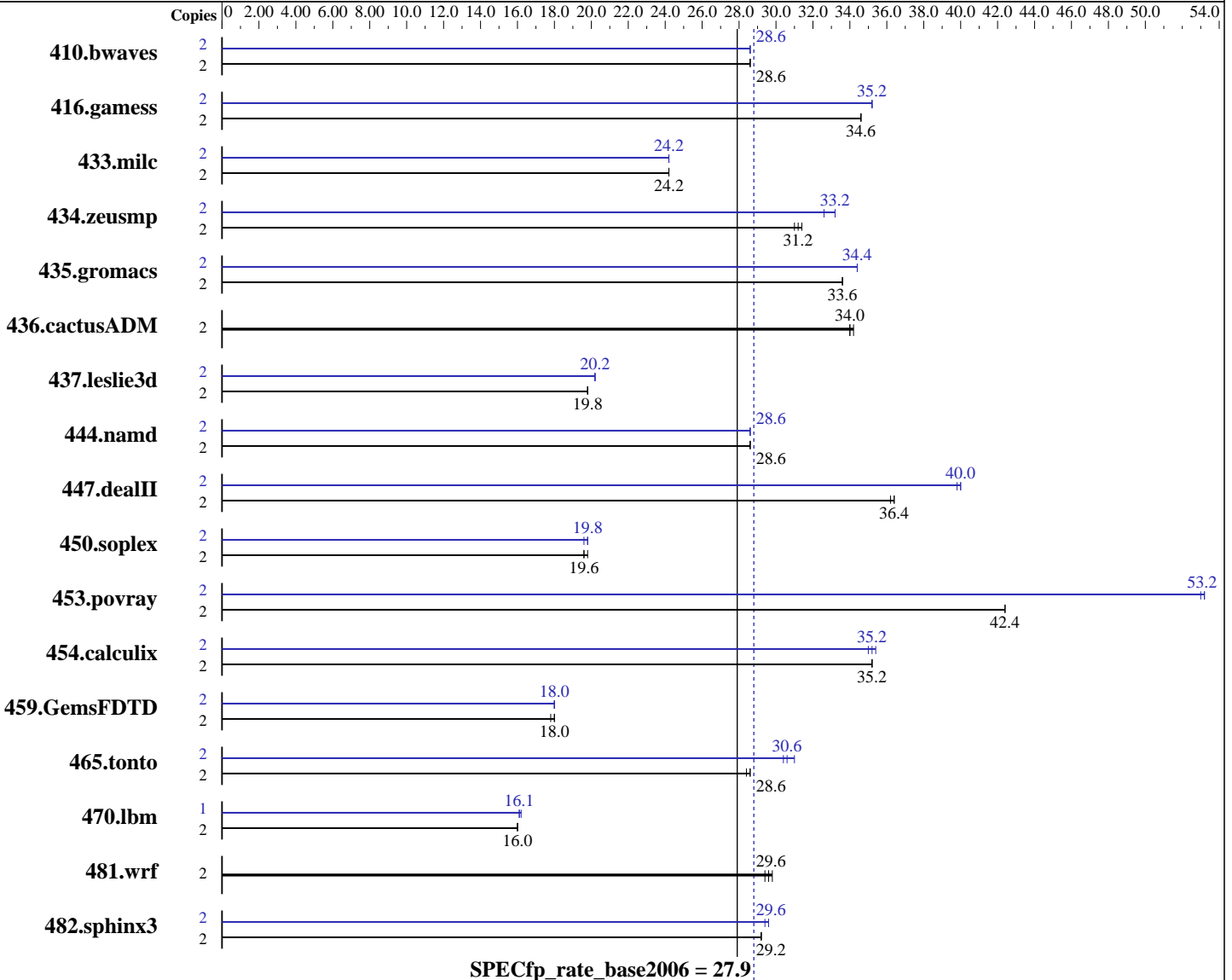
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2009

Hardware Availability: May-2009

Software Availability: Nov-2008



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#### Hardware

CPU Name: Intel Core 2 Duo E7400  
 CPU Characteristics:  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 3 MB I+D on chip per chip

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#### Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)  
 Compiler: Intel C++ Compiler Professional 11.0 for IA32  
 Build 20080930 Package ID: w\_cproc\_p\_11.0.054  
 Intel Visual Fortran Compiler Professional 11.0 for IA32  
 Build 20080930 Package ID: w\_cprof\_p\_11.0.054  
 Microsoft Visual Studio 2008 (for libraries)  
 Auto Parallel: No  
 File System: NTFS

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L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4x1GB DDR2-800 CL5)  
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM  
 Other Hardware: None

System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	<b>950</b>	<b>28.6</b>	949	28.6	952	28.6	2	948	28.6	949	28.6	<b>949</b>	<b>28.6</b>
416.gamess	2	1132	34.6	<b>1130</b>	<b>34.6</b>	1129	34.6	2	1113	35.2	<b>1113</b>	<b>35.2</b>	1113	35.2
433.milc	2	760	24.2	759	24.2	<b>760</b>	<b>24.2</b>	2	759	24.2	758	24.2	<b>758</b>	<b>24.2</b>
434.zeusmp	2	588	31.0	<b>585</b>	<b>31.2</b>	580	31.4	2	547	33.2	558	32.6	<b>547</b>	<b>33.2</b>
435.gromacs	2	425	33.6	<b>425</b>	<b>33.6</b>	424	33.6	2	415	34.4	415	34.4	<b>415</b>	<b>34.4</b>
436.cactusADM	2	<b>702</b>	<b>34.0</b>	699	34.2	703	34.0	2	<b>702</b>	<b>34.0</b>	699	34.2	703	34.0
437.leslie3d	2	950	19.8	<b>949</b>	<b>19.8</b>	947	19.8	2	<b>929</b>	<b>20.2</b>	932	20.2	928	20.2
444.namd	2	560	28.6	<b>560</b>	<b>28.6</b>	560	28.6	2	561	28.6	560	28.6	<b>560</b>	<b>28.6</b>
447.dealII	2	630	36.4	<b>630</b>	<b>36.4</b>	631	36.2	2	571	40.0	574	39.8	<b>573</b>	<b>40.0</b>
450.soplex	2	843	19.8	<b>847</b>	<b>19.6</b>	853	19.6	2	<b>842</b>	<b>19.8</b>	850	19.6	840	19.8
453.povray	2	250	42.4	<b>251</b>	<b>42.4</b>	251	42.4	2	201	53.0	200	53.2	<b>200</b>	<b>53.2</b>
454.calculix	2	468	35.2	<b>468</b>	<b>35.2</b>	469	35.2	2	<b>468</b>	<b>35.2</b>	467	35.4	471	35.0
459.GemsFDTD	2	1176	18.0	<b>1182</b>	<b>18.0</b>	1188	17.8	2	<b>1184</b>	<b>18.0</b>	1185	18.0	1184	18.0
465.tonto	2	689	28.6	691	28.4	<b>690</b>	<b>28.6</b>	2	<b>645</b>	<b>30.6</b>	636	31.0	648	30.4
470.lbm	2	1723	16.0	1721	16.0	<b>1721</b>	<b>16.0</b>	1	<b>851</b>	<b>16.1</b>	852	16.1	850	16.2
481.wrf	2	<b>755</b>	<b>29.6</b>	752	29.8	760	29.4	2	<b>755</b>	<b>29.6</b>	752	29.8	760	29.4
482.sphinx3	2	1337	29.2	<b>1336</b>	<b>29.2</b>	1335	29.2	2	1322	29.4	<b>1321</b>	<b>29.6</b>	1320	29.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## General Notes

Tested systems can be used with Shin-G ATX case,  
 Antec Truepower Trio power supply TP3-650  
 Binaries were built on Windows Vista Ultimate (32-bit)

## Base Compiler Invocation

C benchmarks:  
 icl -Qvc9 -Qc99

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qc99 ifort

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

## Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:  
icl -Qvc9 -Qc99 ifort

## Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Peak Optimization Flags

C benchmarks:

433.milc: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
/F1000000000

482.sphinx3: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib  
-link /FORCE:MULTIPLE

447.dealII: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qscalar-rep- /F1000000000 shlw32m.lib  
-link /FORCE:MULTIPLE

450.soplex: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib  
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000  
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

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## Peak Optimization Flags (Continued)

410.bwaves: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.1(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.1 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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