SPEC® CINT2006 Result

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Bull SAS

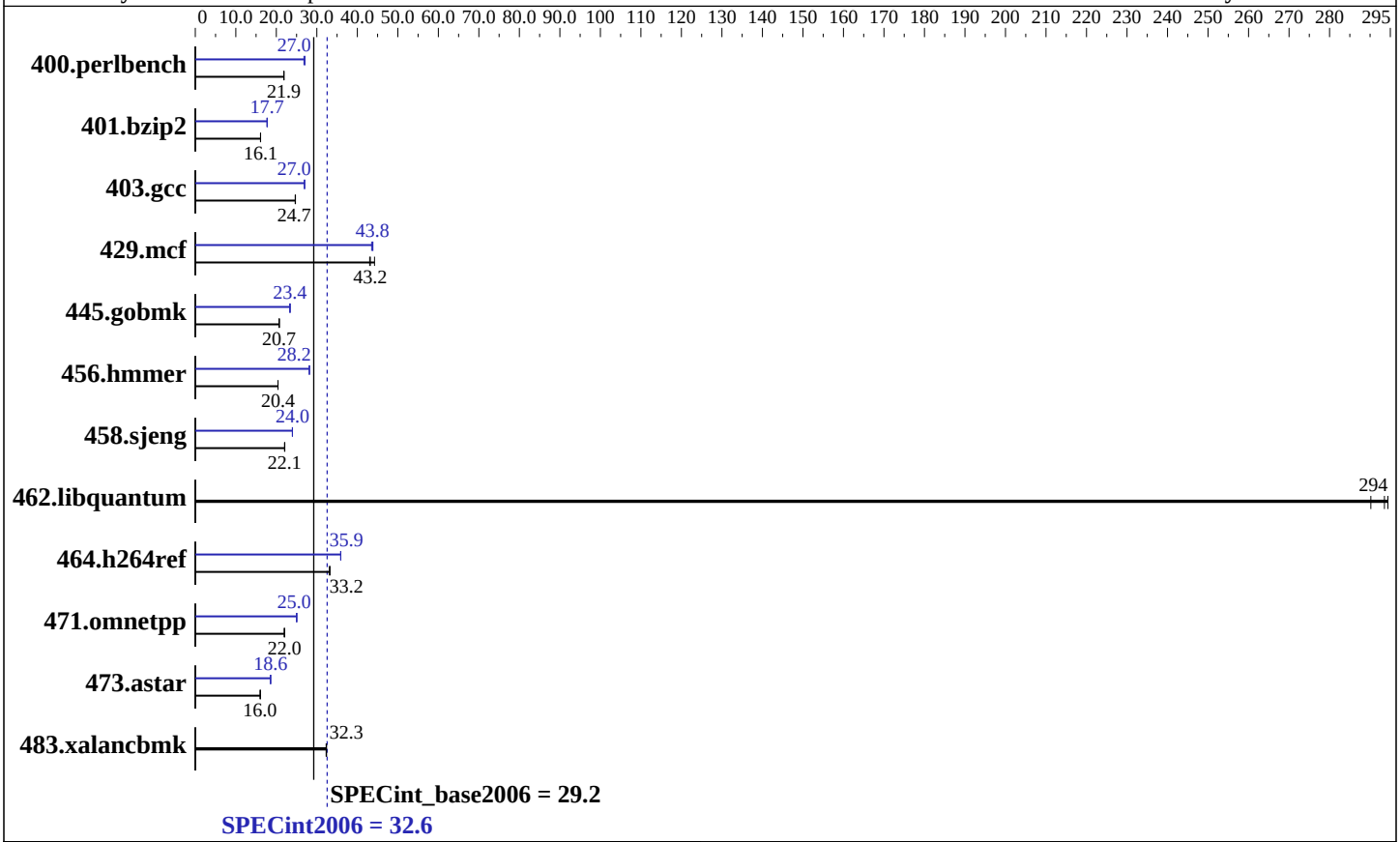
NovaScale R460 E2
(Intel Xeon X5550, 2.66 GHz)

SPECint®2006 = 32.6

SPECint_base2006 = 29.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: May-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon X5550
CPU Characteristics:	Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC)
Disk Subsystem:	1x146.5 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	446	21.9	447	21.8	**447**	**21.9**	364	26.8	**362**	**27.0**	361	27.1
401.bzip2	599	16.1	**599**	**16.1**	600	16.1	**544**	**17.7**	544	17.7	544	17.7
403.gcc	**326**	**24.7**	326	24.7	326	24.7	**299**	**27.0**	298	27.0	299	27.0
429.mcf	**211**	**43.2**	212	43.0	206	44.2	208	43.8	**208**	**43.8**	210	43.5
445.gobmk	506	20.7	507	20.7	**506**	**20.7**	**448**	**23.4**	450	23.3	448	23.4
456.hmmer	457	20.4	457	20.4	**457**	**20.4**	331	28.2	331	28.2	**331**	**28.2**
458.sjeng	**548**	**22.1**	549	22.0	548	22.1	504	24.0	505	23.9	**504**	**24.0**
462.libquantum	**70.6**	**294**	71.4	290	70.4	294	**70.6**	**294**	71.4	290	70.4	294
464.h264ref	666	33.2	**667**	**33.2**	667	33.2	**616**	**35.9**	617	35.9	616	35.9
471.omnetpp	284	22.0	**284**	**22.0**	284	22.0	249	25.1	250	25.0	**250**	**25.0**
473.astar	437	16.1	**438**	**16.0**	438	16.0	**377**	**18.6**	377	18.6	379	18.5
483.xalancbmk	**213**	**32.3**	214	32.3	213	32.4	**213**	**32.3**	214	32.3	213	32.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Platform Notes

```
BIOS setting:
NUMA configuration : Enabled
```

General Notes

```
The NEC Express5800/R120a-1(Intel Xeon X5550),
the NEC Express5800/R120a-2(Intel Xeon X5550),
the Bull NovaScale R440 E2 (Intel Xeon X5550, 2.66 GHz) and
the Bull NovaScale R460 E2 (Intel Xeon X5550, 2.66 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R120a-1(Intel Xeon X5550) model.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):
icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.xml`

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