



# SPEC<sup>®</sup> CINT2006 Result

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## Hewlett-Packard Company

SPECint<sup>®</sup>\_rate2006 = 237

ProLiant DL380 G6  
(2.67 GHz, Intel Xeon X5550)

SPECint\_rate\_base2006 = 221

CPU2006 license: 3

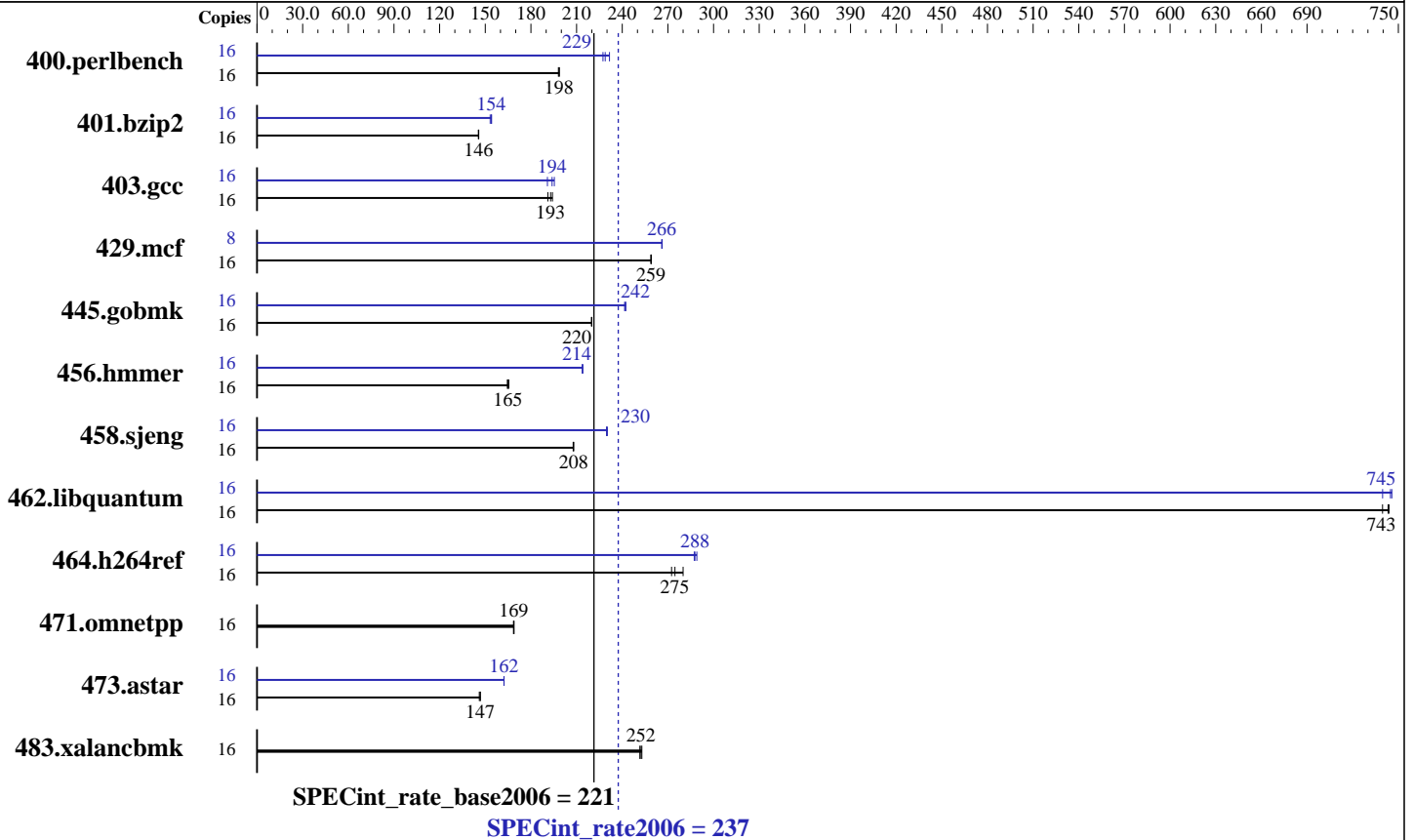
Test date: May-2009

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2009

Tested by: Hewlett-Packard Company

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.07 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6x8 GB PC3-10600R CL9)  
 Disk Subsystem: 1x146 GB 10K RPM SAS  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 5.3  
 Kernel 2.6.18-128.el5  
 Compiler: Intel C++ Compiler 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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### Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	16	789	198	787	199	<b>788</b>	<b>198</b>	16	688	227	675	232	<b>683</b>	<b>229</b>		
401.bzip2	16	1061	145	<b>1061</b>	<b>146</b>	1060	146	16	1002	154	1008	153	<b>1003</b>	<b>154</b>		
403.gcc	16	663	194	674	191	<b>668</b>	<b>193</b>	16	675	191	659	195	<b>664</b>	<b>194</b>		
429.mcf	16	563	259	564	259	<b>564</b>	<b>259</b>	8	274	266	274	266	<b>274</b>	<b>266</b>		
445.gobmk	16	763	220	<b>763</b>	<b>220</b>	763	220	16	695	242	693	242	<b>694</b>	<b>242</b>		
456.hammer	16	902	165	<b>906</b>	<b>165</b>	907	165	16	697	214	<b>698</b>	<b>214</b>	698	214		
458.sjeng	16	<b>931</b>	<b>208</b>	930	208	932	208	16	841	230	843	230	<b>843</b>	<b>230</b>		
462.libquantum	16	448	740	<b>446</b>	<b>743</b>	446	744	16	<b>445</b>	<b>745</b>	448	740	445	746		
464.h264ref	16	1265	280	<b>1289</b>	<b>275</b>	1300	272	16	1225	289	<b>1231</b>	<b>288</b>	1232	287		
471.omnetpp	16	592	169	<b>593</b>	<b>169</b>	593	169	16	592	169	<b>593</b>	<b>169</b>	593	169		
473.astar	16	765	147	<b>766</b>	<b>147</b>	769	146	16	692	162	692	162	<b>692</b>	<b>162</b>		
483.xalancbmk	16	<b>438</b>	<b>252</b>	439	252	437	253	16	<b>438</b>	<b>252</b>	439	252	437	253		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

### Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode  
Power Profile set to Maximum Performance  
Thermal Configuration set to Increased Cooling

### Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbenc: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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