SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant DL380 G6
(2.27 GHz, Intel Xeon E5520)

SPECint®_rate2006 = 199
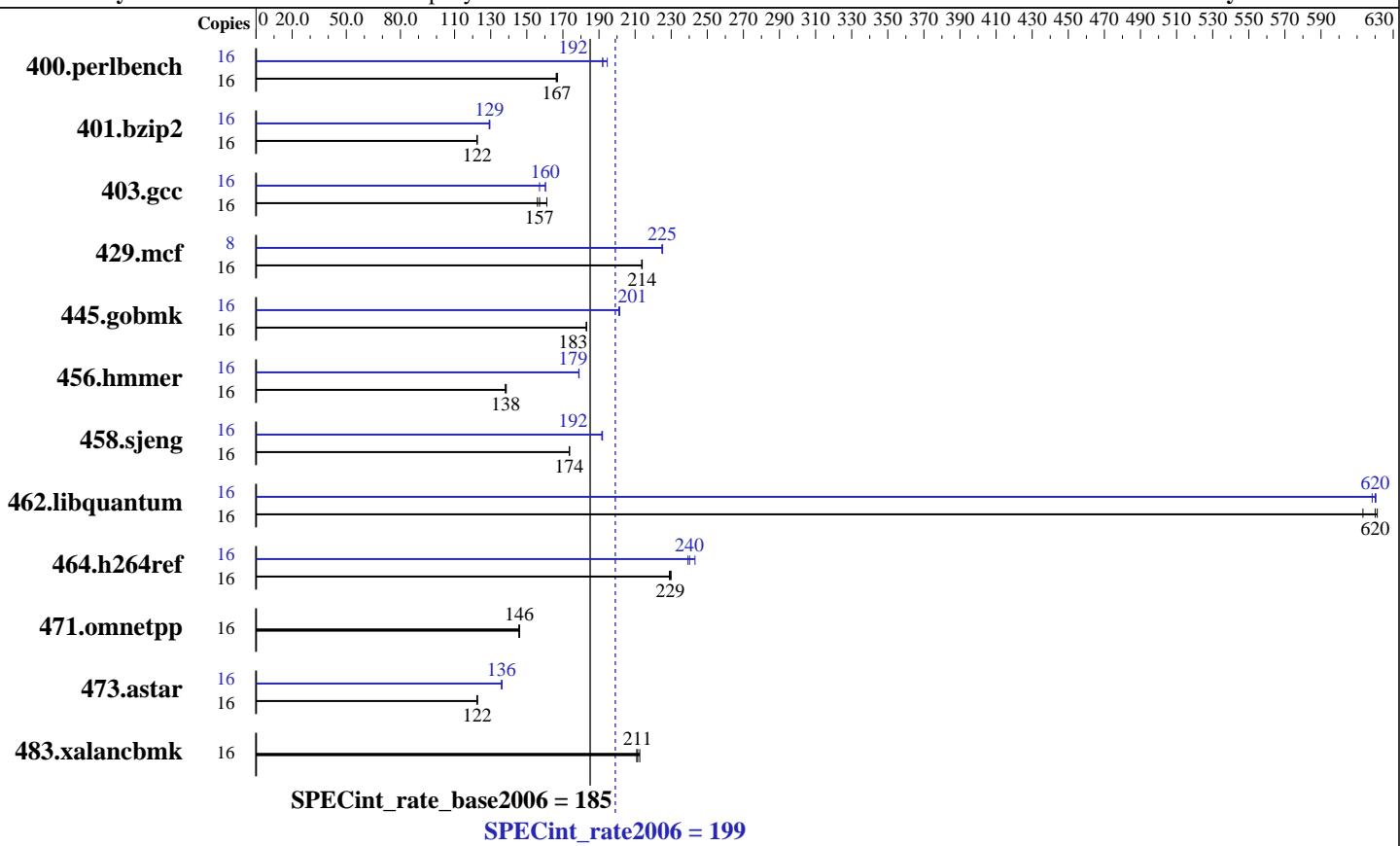
SPECint_rate_base2006 = 185

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: May-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009

Copies 0 20.0 50.0 80.0 110 130 150 170 190 210 230 250 270 290 310 330 350 370 390 410 430 450 470 490 510 530 550 570 590 630

Benchmark	Copies	Value
400.perlbench	16	192
400.perlbench	16	167
401.bzip2	16	129
401.bzip2	16	122
403.gcc	16	160
403.gcc	16	157
429.mcf	8	225
429.mcf	16	214
445.gobmk	16	201
445.gobmk	16	183
456.hmmer	16	179
456.hmmer	16	138
458.sjeng	16	192
458.sjeng	16	174
462.libquantum	16	620
462.libquantum	16	620
464.h264ref	16	240
464.h264ref	16	229
471.omnetpp	16	146
473.astar	16	136
473.astar	16	122
483.xalancbmk	16	211

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Hardware

CPU Name:	Intel Xeon E5520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (6x8 GB PC3-10600R CL9)
Disk Subsystem:	1x146 GB 10K RPM SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.3 Kernel 2.6.18-128.el5
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	936	167	**938**	**167**	940	166	16	814	192	803	195	**814**	**192**
401.bzip2	16	**1261**	**122**	1260	123	1261	122	16	1195	129	**1194**	**129**	1192	130
403.gcc	16	826	156	800	161	**820**	**157**	16	**804**	**160**	803	160	820	157
429.mcf	16	683	214	682	214	**682**	**214**	8	324	225	**324**	**225**	324	225
445.gobmk	16	**917**	**183**	918	183	916	183	16	833	201	835	201	**834**	**201**
456.hmmer	16	**1080**	**138**	1077	139	1081	138	16	**835**	**179**	835	179	834	179
458.sjeng	16	1116	174	**1116**	**174**	1114	174	16	1009	192	**1010**	**192**	1010	192
462.libquantum	16	**535**	**620**	541	613	534	621	16	**535**	**620**	534	620	536	618
464.h264ref	16	1540	230	1546	229	**1543**	**229**	16	1456	243	**1474**	**240**	1480	239
471.omnetpp	16	685	146	**685**	**146**	687	146	16	685	146	**685**	**146**	687	146
473.astar	16	915	123	**917**	**122**	918	122	16	824	136	826	136	**826**	**136**
483.xalancbmk	16	**522**	**211**	519	213	524	211	16	**522**	**211**	519	213	524	211

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The config file option 'submit' was used.
    numactl was used to bind copies to the cores
```

Platform Notes

```
   BIOS configuration:
     Power Regulator set to Static High Performance Mode
     Power Profile set to Maximum Performance
     Thermal Configuration set to Increased Cooling
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
```

Continued on next page

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Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
```
icpc
```

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

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Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-ansi-alias` `-opt-prefetch`

401.bzip2: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

403.gcc: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-inline-calloc`
`-opt-malloc-options=3`

429.mcf: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-prof-use(pass 2)` `-O2`
`-ipo` `-no-prec-div` `-ansi-alias`

456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
`-ansi-alias` `-auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll4` `-auto-ilp32`

462.libquantum: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static`
`-opt-malloc-options=3` `-opt-prefetch`

464.h264ref: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-static(pass 2)`
`-prof-use(pass 2)` `-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-xSSE4.2(pass 2)` `-prof-gen(pass 1)` `-ipo(pass 2)`
`-O3(pass 2)` `-no-prec-div(pass 2)` `-prof-use(pass 2)`
`-ansi-alias` `-opt-ra-region-strategy=routine` `-auto-ilp32`
`-Wl,-z,muldefs` `-L/spec/cpu2006.1.1/lib` `-lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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