



SPEC® CFP2006 Result

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Fujitsu

CELSIUS M470, Intel Xeon W3520

SPECfp®_rate2006 = 77.8

CPU2006 license: 19

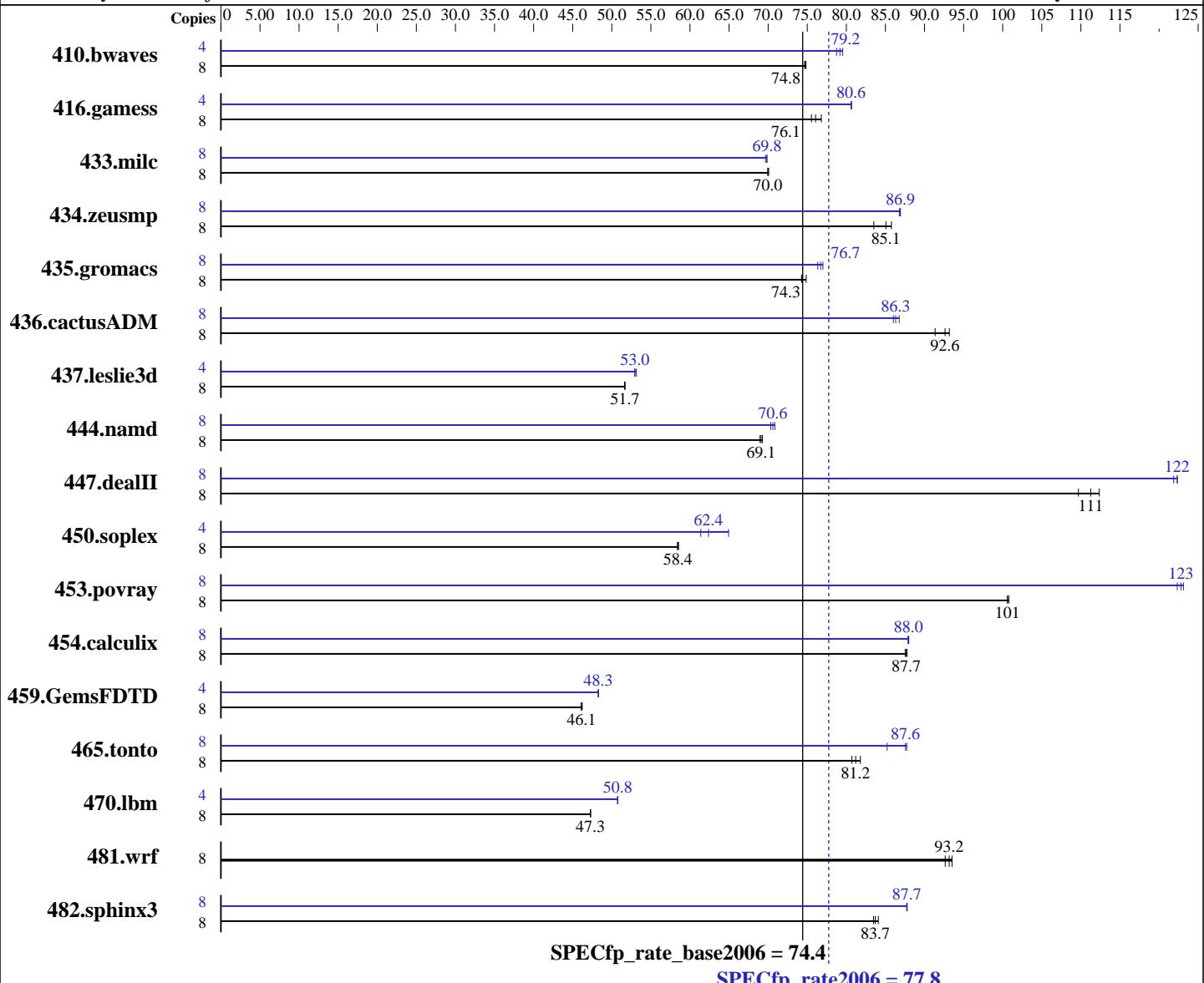
Test date: Apr-2009

Test sponsor: Fujitsu

Hardware Availability: Apr-2009

Tested by: Fujitsu

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon W3520
CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64)
SP2, kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux
Build 20090131 Package ID: l_cproc_p_11.0.080,
l_cprof_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (6x2 GB PC3 10600E, 2 rank, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1456	74.7	1453	74.8	1454	74.8	4	690	78.8	683	79.5	687	79.2
416.gamess	8	2074	75.5	2059	76.1	2039	76.8	4	972	80.6	972	80.6	971	80.7
433.milc	8	1048	70.1	1050	70.0	1050	70.0	8	1051	69.8	1054	69.7	1052	69.8
434.zeusmp	8	856	85.1	872	83.5	849	85.8	8	838	86.9	839	86.8	838	86.9
435.gromacs	8	763	74.9	769	74.3	769	74.3	8	742	77.0	744	76.7	748	76.3
436.cactusADM	8	1032	92.6	1026	93.2	1046	91.4	8	1108	86.3	1111	86.0	1102	86.8
437.leslie3d	8	1455	51.7	1456	51.7	1455	51.7	4	710	53.0	710	52.9	707	53.1
444.namd	8	930	69.0	929	69.1	926	69.3	8	909	70.6	912	70.3	905	70.9
447.dealII	8	823	111	815	112	834	110	8	748	122	751	122	748	122
450.soplex	8	1142	58.4	1143	58.4	1140	58.5	4	543	61.4	535	62.4	514	64.9
453.povray	8	423	101	423	101	422	101	8	348	122	347	123	346	123
454.calculix	8	753	87.7	754	87.5	752	87.8	8	750	88.0	750	88.0	751	87.9
459.GemsFDTD	8	1842	46.1	1836	46.2	1841	46.1	4	878	48.3	879	48.3	879	48.3
465.tonto	8	975	80.7	962	81.8	970	81.2	8	924	85.2	899	87.6	897	87.7
470.lbm	8	2325	47.3	2325	47.3	2325	47.3	4	1083	50.8	1083	50.8	1083	50.7
481.wrf	8	956	93.5	965	92.6	959	93.2	8	956	93.5	965	92.6	959	93.2
482.sphinx3	8	1863	83.7	1854	84.1	1868	83.5	8	1776	87.8	1777	87.7	1777	87.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

numactl --localalloc --physcpubind was used to bind copies to the cores and their local memory. The complete list of parameters may be found in the config file.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run



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Platform Notes

BIOS configuration:

Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

General Notes

For information about Fujitsu please visit:

<http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

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Peak Optimization Flags (Continued)

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -opt-prefetch -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.07.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.07.xml>

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For other inquiries, please contact webmaster@spec.org.

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