



# SPEC<sup>®</sup> CINT2006 Result

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Dell Inc.

SPECint<sup>®</sup>\_rate2006 = 142

PowerEdge T610 (Intel Xeon E5506, 2.13 GHz)

SPECint\_rate\_base2006 = 132

CPU2006 license: 55

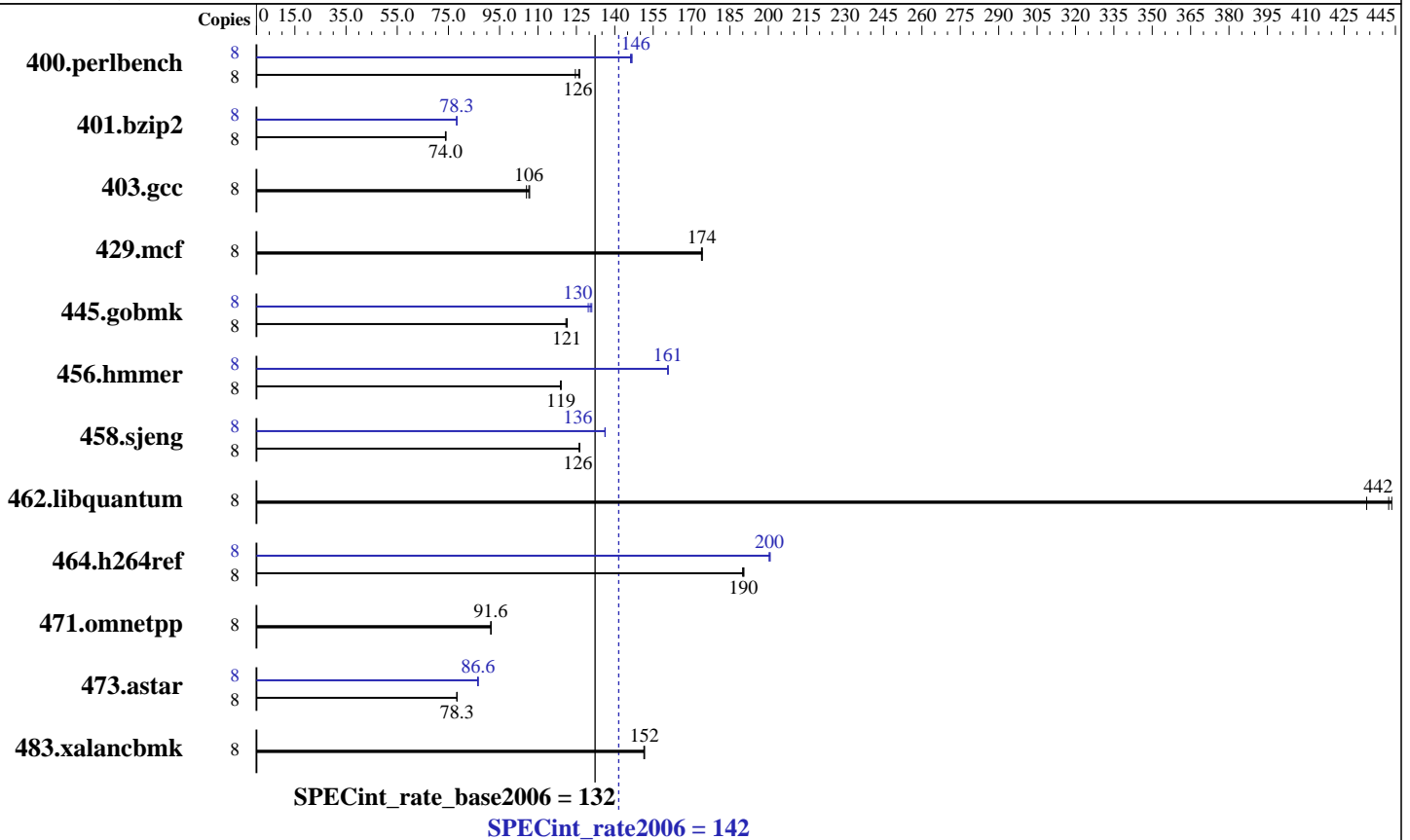
Test date: Apr-2009

Test sponsor: Dell Inc.

Hardware Availability: Mar-2009

Tested by: Dell Inc.

Software Availability: Mar-2009



## Hardware

CPU Name: Intel Xeon E5506  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	627	125	<b><u>621</u></b>	<b><u>126</u></b>	619	126	8	<b><u>534</u></b>	<b><u>146</u></b>	533	147	535	146
401.bzip2	8	1043	74.0	<b><u>1044</u></b>	<b><u>74.0</u></b>	1046	73.8	8	986	78.3	<b><u>986</u></b>	<b><u>78.3</u></b>	987	78.2
403.gcc	8	603	107	<b><u>605</u></b>	<b><u>106</u></b>	610	105	8	603	107	<b><u>605</u></b>	<b><u>106</u></b>	610	105
429.mcf	8	<b><u>419</u></b>	<b><u>174</u></b>	419	174	420	174	8	<b><u>419</u></b>	<b><u>174</u></b>	419	174	420	174
445.gobmk	8	691	121	694	121	<b><u>692</u></b>	<b><u>121</u></b>	8	647	130	641	131	<b><u>643</u></b>	<b><u>130</u></b>
456.hammer	8	628	119	<b><u>627</u></b>	<b><u>119</u></b>	627	119	8	464	161	<b><u>464</u></b>	<b><u>161</u></b>	464	161
458.sjeng	8	768	126	<b><u>767</u></b>	<b><u>126</u></b>	766	126	8	711	136	710	136	<b><u>710</u></b>	<b><u>136</u></b>
462.libquantum	8	<b><u>375</u></b>	<b><u>442</u></b>	374	444	382	434	8	<b><u>375</u></b>	<b><u>442</u></b>	374	444	382	434
464.h264ref	8	<b><u>931</u></b>	<b><u>190</u></b>	929	190	932	190	8	884	200	<b><u>883</u></b>	<b><u>200</u></b>	882	201
471.omnetpp	8	<b><u>546</u></b>	<b><u>91.6</u></b>	545	91.7	546	91.6	8	<b><u>546</u></b>	<b><u>91.6</u></b>	545	91.7	546	91.6
473.astar	8	<b><u>717</u></b>	<b><u>78.3</u></b>	717	78.3	716	78.4	8	<b><u>649</u></b>	<b><u>86.6</u></b>	650	86.4	648	86.7
483.xalanbmk	8	365	151	364	152	<b><u>364</u></b>	<b><u>152</u></b>	8	365	151	364	152	<b><u>364</u></b>	<b><u>152</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

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## Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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