SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint®_rate2006 = 251

Acer Altos G540 M2 (Intel Xeon X5570)

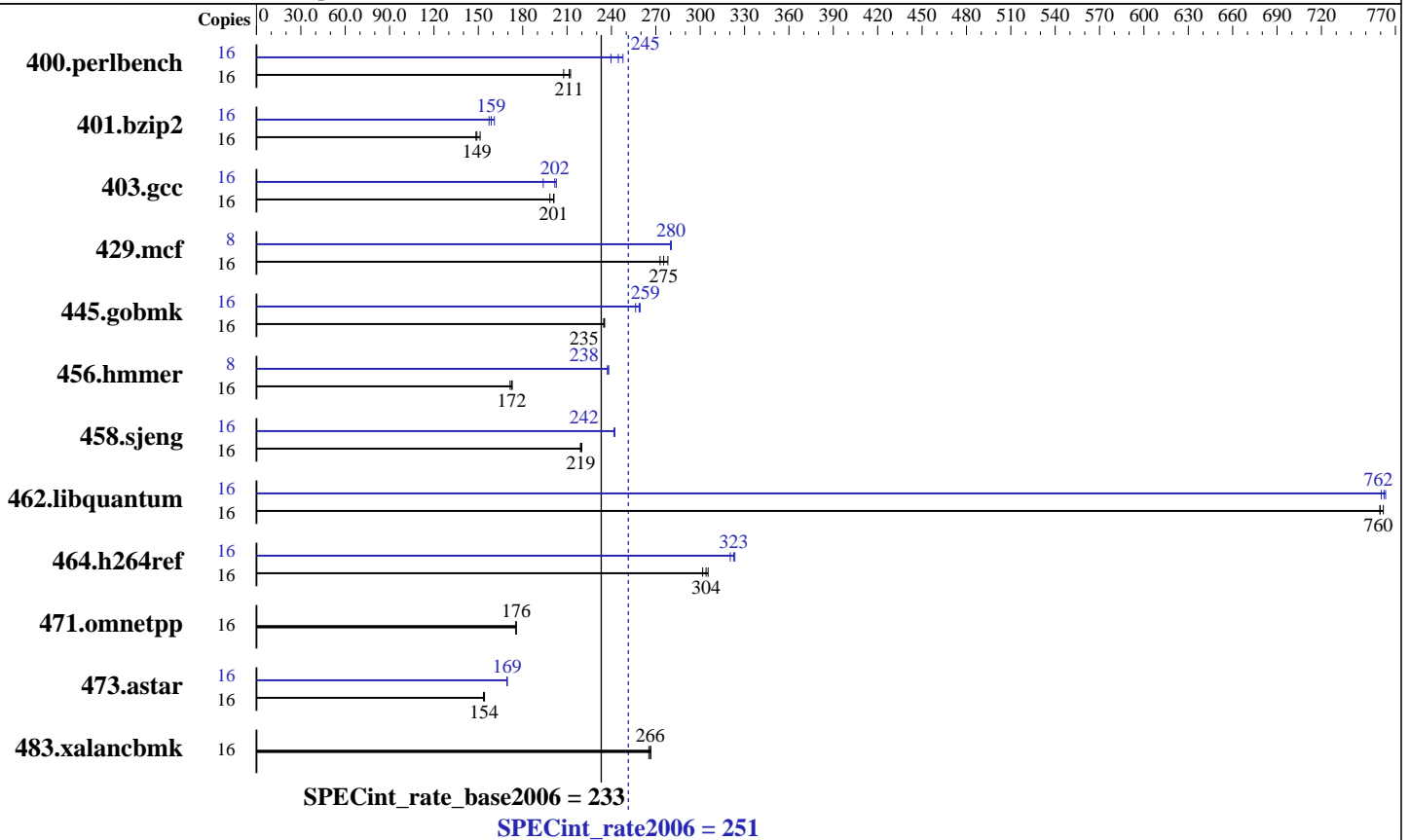SPECint_rate_base2006 = 233

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Copies

Benchmark	Copies	Base	Peak
400.perlbench	16	211	245
401.bzip2	16	149	159
403.gcc	16	201	202
429.mcf	8 / 16	275	280
445.gobmk	16	235	259
456.hmmer	8 / 16	172	238
458.sjeng	16	219	242
462.libquantum	16	760	762
464.h264ref	16	304	323
471.omnetpp	16	176	
473.astar	16	154	169
483.xalancbmk	16	266	

SPECint_rate_base2006 = 233

SPECint_rate2006 = 251

Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB Registered ECC DDR3-1333)
Disk Subsystem:	320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer Altos G540 M2 (Intel Xeon X5570)

SPECint_rate2006 = 251

SPECint_rate_base2006 = 233

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**739**	**211**	753	208	736	212	16	652	240	**639**	**245**	631	248
401.bzip2	16	1040	148	**1036**	**149**	1021	151	16	**973**	**159**	960	161	982	157
403.gcc	16	**641**	**201**	640	201	649	198	16	635	203	**639**	**202**	664	194
429.mcf	16	535	273	**530**	**275**	525	278	8	261	280	260	280	**260**	**280**
445.gobmk	16	715	235	713	235	**714**	**235**	16	**649**	**259**	655	256	647	259
456.hmmer	16	871	171	**866**	**172**	863	173	8	315	237	314	238	**314**	**238**
458.sjeng	16	**883**	**219**	884	219	880	220	16	**799**	**242**	801	242	799	242
462.libquantum	16	436	760	**436**	**760**	435	762	16	436	760	**435**	**762**	434	763
464.h264ref	16	1160	305	**1165**	**304**	1174	302	16	1095	323	**1097**	**323**	1105	320
471.omnetpp	16	**570**	**176**	570	175	570	176	16	**570**	**176**	570	175	570	176
473.astar	16	728	154	732	153	**729**	**154**	16	**663**	**169**	663	169	663	169
483.xalancbmk	16	414	267	416	265	**415**	**266**	16	414	267	416	265	**415**	**266**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was set for stacksize unlimited
```

Platform Notes

```
BIOS setting
  Hardware Prefetcher: Enabled
  Adjacent Cache Line Prefetch: Enabled
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Acer Incorporated

Acer Altos G540 M2 (Intel Xeon X5570)

SPECint_rate2006 =   251

SPECint_rate_base2006 = 233

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):
```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Acer Altos G540 M2 (Intel Xeon X5570)

SPECint_rate2006 = 251

SPECint_rate_base2006 = 233

CPU2006 license: 97	**Test date:** Apr-2009
Test sponsor: Acer Incorporated	**Hardware Availability:** Mar-2009
Tested by: Acer Incorporated	**Software Availability:** Feb-2009

Peak Portability Flags (Continued)

```
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
        473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -ansi-alias -opt-prefetch

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                   -opt-malloc-options=3

         429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                   -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                   -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll4 -auto-ilp32

   462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                   -opt-malloc-options=3 -opt-prefetch

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                   -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: basepeak = yes

        473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                   -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                   -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

Continued on next page

Acer Incorporated

Acer Altos G540 M2 (Intel Xeon X5570)

SPECint_rate2006 =   251

SPECint_rate_base2006 = 233

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 23:53:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 May 2009.