SPEC® CINT2006 Result
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Dell Inc.

PowerEdge M610 (Intel Xeon E5506, 2.13 GHz)

SPECint®_rate2006 = 142

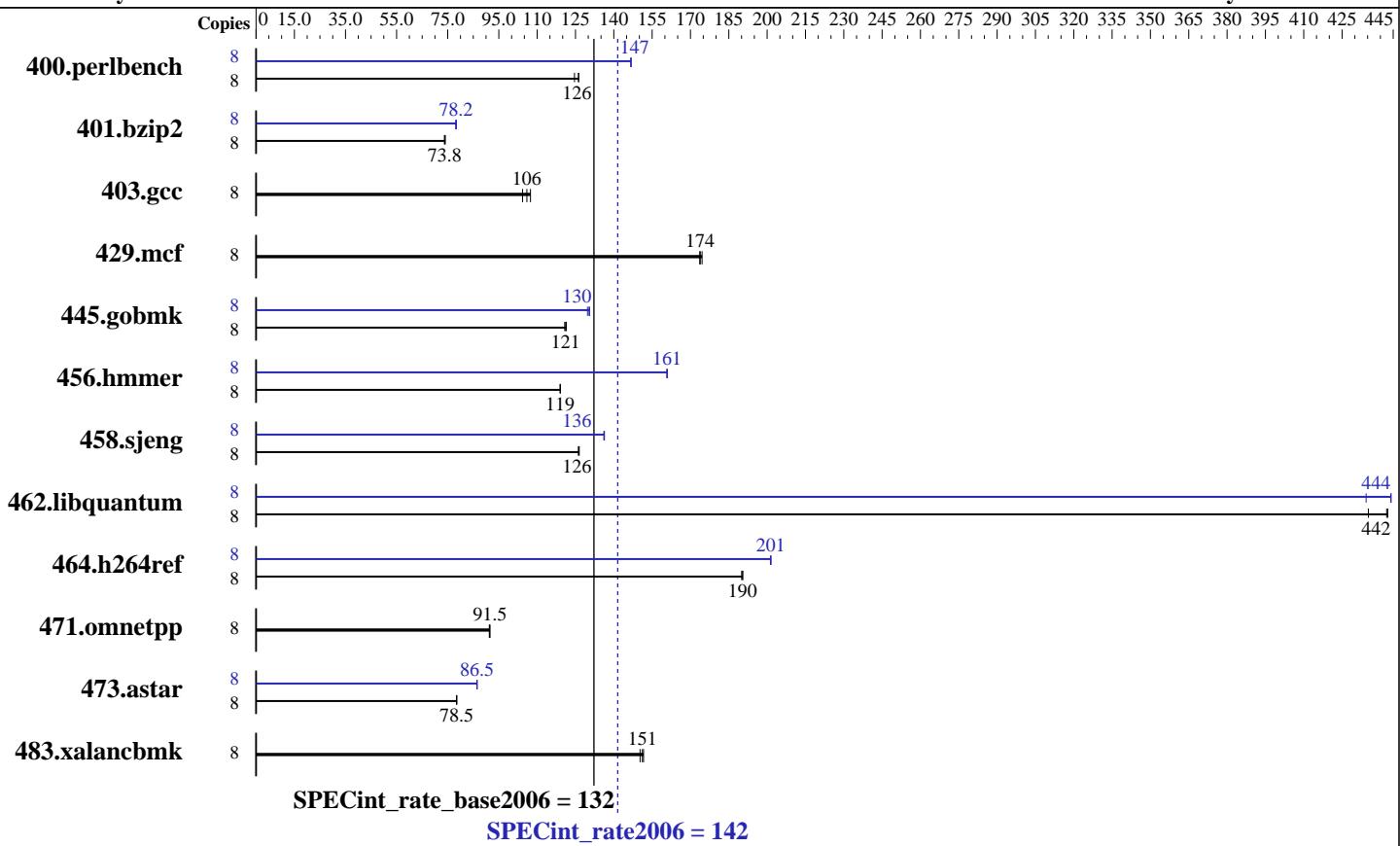SPECint_rate_base2006 = 132

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Mar-2009

Hardware Availability: Mar-2009

Software Availability: Mar-2009

Copies 0 15.0 35.0 55.0 75.0 95.0 110 125 140 155 170 185 200 215 230 245 260 275 290 305 320 335 350 365 380 395 410 425 445

Benchmark		
400.perlbench	8	147
	8	126
401.bzip2	8	78.2
	8	73.8
403.gcc	8	106
429.mcf	8	174
445.gobmk	8	130
	8	121
456.hmmer	8	161
	8	119
458.sjeng	8	136
	8	126
462.libquantum	8	444
	8	442
464.h264ref	8	201
	8	190
471.omnetpp	8	91.5
473.astar	8	86.5
	8	78.5
483.xalancbmk	8	151

SPECint_rate_base2006 = 132

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Hardware

CPU Name:	Intel Xeon E5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 146 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	627	125	619	126	**620**	**126**	8	533	147	**533**	**147**	533	147
401.bzip2	8	1042	74.1	1047	73.7	**1046**	**73.8**	8	987	78.2	985	78.4	**987**	**78.2**
403.gcc	8	618	104	**608**	**106**	600	107	8	618	104	**608**	**106**	600	107
429.mcf	8	418	175	**420**	**174**	420	174	8	418	175	**420**	**174**	420	174
445.gobmk	8	**693**	**121**	691	121	695	121	8	643	131	**644**	**130**	647	130
456.hmmer	8	**627**	**119**	627	119	627	119	8	**464**	**161**	464	161	464	161
458.sjeng	8	765	126	**767**	**126**	767	126	8	710	136	711	136	**711**	**136**
462.libquantum	8	381	435	**375**	**442**	374	443	8	382	434	**373**	**444**	373	444
464.h264ref	8	930	190	932	190	**930**	**190**	8	**879**	**201**	879	201	880	201
471.omnetpp	8	**547**	**91.5**	547	91.5	548	91.3	8	**547**	**91.5**	547	91.5	548	91.3
473.astar	8	715	78.6	716	78.4	**716**	**78.5**	8	650	86.4	**649**	**86.5**	649	86.5
483.xalancbmk	8	367	150	364	152	**365**	**151**	8	367	150	364	152	**365**	**151**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch
```

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Peak Optimization Flags (Continued)

```
     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: basepeak = yes

       429.mcf: basepeak = yes

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                -ipo -no-prec-div -ansi-alias

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll4 -auto-ilp32

 462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                -opt-malloc-options=3 -opt-prefetch

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: basepeak = yes

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

 483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
       403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

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You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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