



# SPEC® CINT2006 Result

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NTT System S. A.

**SPECint®2006 = 20.9**

NTT Tytan 2206I (Intel Xeon E5410)

**SPECint\_base2006 = 18.7**

CPU2006 license: 9013

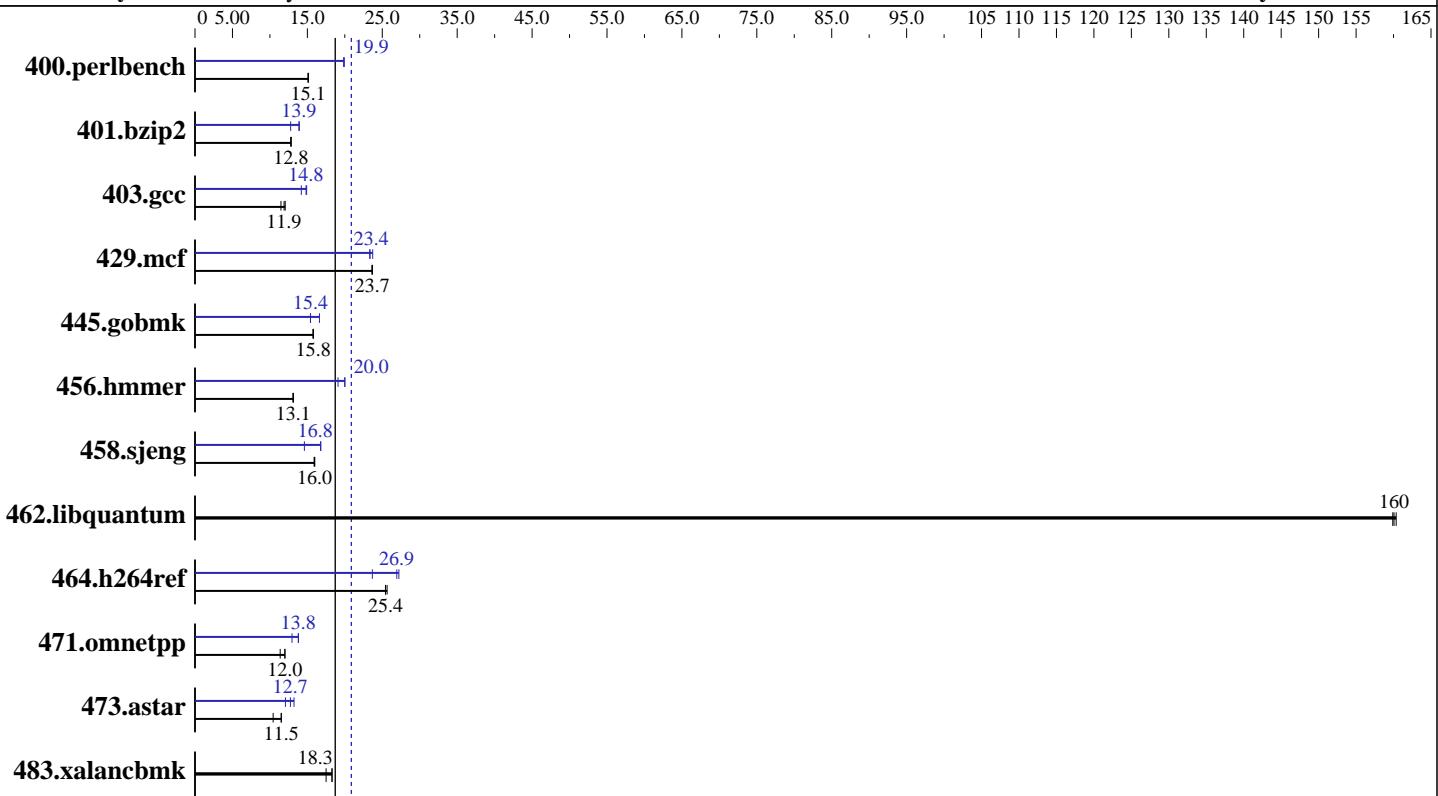
Test date: Jan-2009

Test sponsor: NTT System S. A.

Hardware Availability: Dec-2008

Tested by: NTT System S. A.

Software Availability: Dec-2008



## Hardware

CPU Name: Intel Xeon E5410  
CPU Characteristics: 2.33 GHz, 2x6 MB P2 shared, 1333 MHz System Bus  
CPU MHz: 2333  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
L3 Cache: None  
Other Cache: None  
Memory: 16 GB (4 x 4GB DDR2-667 FBDIMM)  
Disk Subsystem: 147 GB SAS, 1000RPM  
Other Hardware: None

## Software

Operating System: SuSe Linux Enterprise Server 10 SP2, Kernel 2.6.16.60-0.21-smp  
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l\_cproc\_p\_11.0.066  
Auto Parallel: Yes  
File System: ReiserFS  
System State: Run level 3 (multi-user)  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	647	15.1	646	15.1	<b>646</b>	<b>15.1</b>	492	19.9	<b>491</b>	<b>19.9</b>	491	19.9
401.bzip2	752	12.8	<b>753</b>	<b>12.8</b>	754	12.8	<b>696</b>	<b>13.9</b>	756	12.8	693	13.9
403.gcc	668	12.0	<b>677</b>	<b>11.9</b>	702	11.5	<b>543</b>	<b>14.8</b>	540	14.9	567	14.2
429.mcf	385	23.7	386	23.6	<b>385</b>	<b>23.7</b>	<b>390</b>	<b>23.4</b>	391	23.3	384	23.7
445.gobmk	665	15.8	<b>665</b>	<b>15.8</b>	665	15.8	<b>680</b>	<b>15.4</b>	631	16.6	681	15.4
456.hammer	712	13.1	<b>712</b>	<b>13.1</b>	712	13.1	466	20.0	489	19.1	<b>467</b>	<b>20.0</b>
458.sjeng	<b>758</b>	<b>16.0</b>	757	16.0	761	15.9	829	14.6	<b>721</b>	<b>16.8</b>	721	16.8
462.libquantum	130	160	<b>129</b>	<b>160</b>	129	160	130	160	<b>129</b>	<b>160</b>	129	160
464.h264ref	863	25.6	<b>870</b>	<b>25.4</b>	870	25.4	<b>821</b>	<b>26.9</b>	813	27.2	935	23.7
471.omnetpp	550	11.4	<b>521</b>	<b>12.0</b>	520	12.0	453	13.8	483	13.0	<b>453</b>	<b>13.8</b>
473.astar	<b>610</b>	<b>11.5</b>	673	10.4	610	11.5	<b>551</b>	<b>12.7</b>	531	13.2	581	12.1
483.xalancbmk	<b>377</b>	<b>18.3</b>	377	18.3	394	17.5	<b>377</b>	<b>18.3</b>	377	18.3	394	17.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 200M

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc
```

C++ benchmarks:

icpc

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch
```

```
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias
```

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## Peak Optimization Flags (Continued)

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll12  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll14

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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