



# SPEC® CINT2006 Result

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## Fujitsu

## SPECint®\_rate2006 = 134

## CELSIUS M470, Intel Xeon W5580

## SPECint\_rate\_base2006 = 125

CPU2006 license: 19

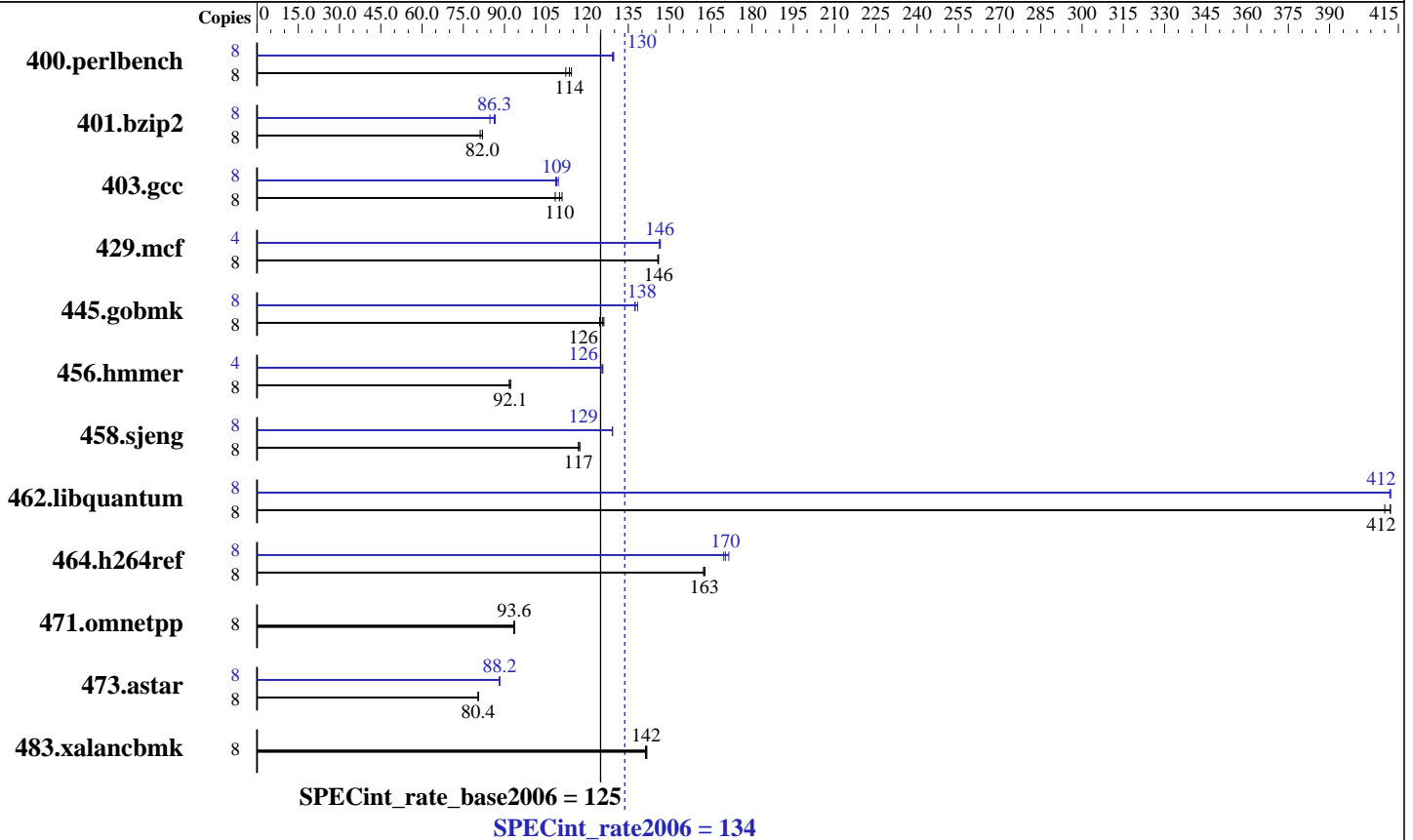
Test sponsor: Fujitsu

Tested by: Fujitsu Siemens Computers

Test date: Mar-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon W5580  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 12 GB (6x2 GB PC3 10600E, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 10 (x86\_64) SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	684	114	696	112	<b>688</b>	<b>114</b>	8	605	129	<b>603</b>	<b>130</b>	603	130
401.bzip2	8	<b>942</b>	<b>82.0</b>	951	81.1	942	82.0	8	911	84.7	<b>895</b>	<b>86.3</b>	891	86.6
403.gcc	8	<b>586</b>	<b>110</b>	581	111	594	108	8	593	109	588	110	<b>591</b>	<b>109</b>
429.mcf	8	<b>500</b>	<b>146</b>	500	146	501	146	4	249	147	249	146	<b>249</b>	<b>146</b>
445.gobmk	8	665	126	<b>668</b>	<b>126</b>	674	125	8	606	138	<b>610</b>	<b>138</b>	611	137
456.hammer	8	814	91.7	810	92.2	<b>811</b>	<b>92.1</b>	4	298	125	297	126	<b>297</b>	<b>126</b>
458.sjeng	8	824	117	<b>828</b>	<b>117</b>	828	117	8	<b>749</b>	<b>129</b>	749	129	749	129
462.libquantum	8	<b>402</b>	<b>412</b>	402	412	404	410	8	<b>402</b>	<b>412</b>	402	412	402	412
464.h264ref	8	1087	163	<b>1088</b>	<b>163</b>	1091	162	8	1032	172	<b>1040</b>	<b>170</b>	1043	170
471.omnetpp	8	535	93.4	<b>534</b>	<b>93.6</b>	534	93.6	8	535	93.4	<b>534</b>	<b>93.6</b>	534	93.6
473.astar	8	697	80.6	<b>698</b>	<b>80.4</b>	698	80.4	8	<b>636</b>	<b>88.2</b>	638	88.0	636	88.2
483.xalancbmk	8	<b>390</b>	<b>142</b>	389	142	391	141	8	<b>390</b>	<b>142</b>	389	142	391	141

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

## General Notes

For information about Fujitsu please visit:  
<http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.04.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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