



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5215)

SPECfp[®]_rate2006 = 40.5

SPECfp_rate_base2006 = 38.4

CPU2006 license: 9006

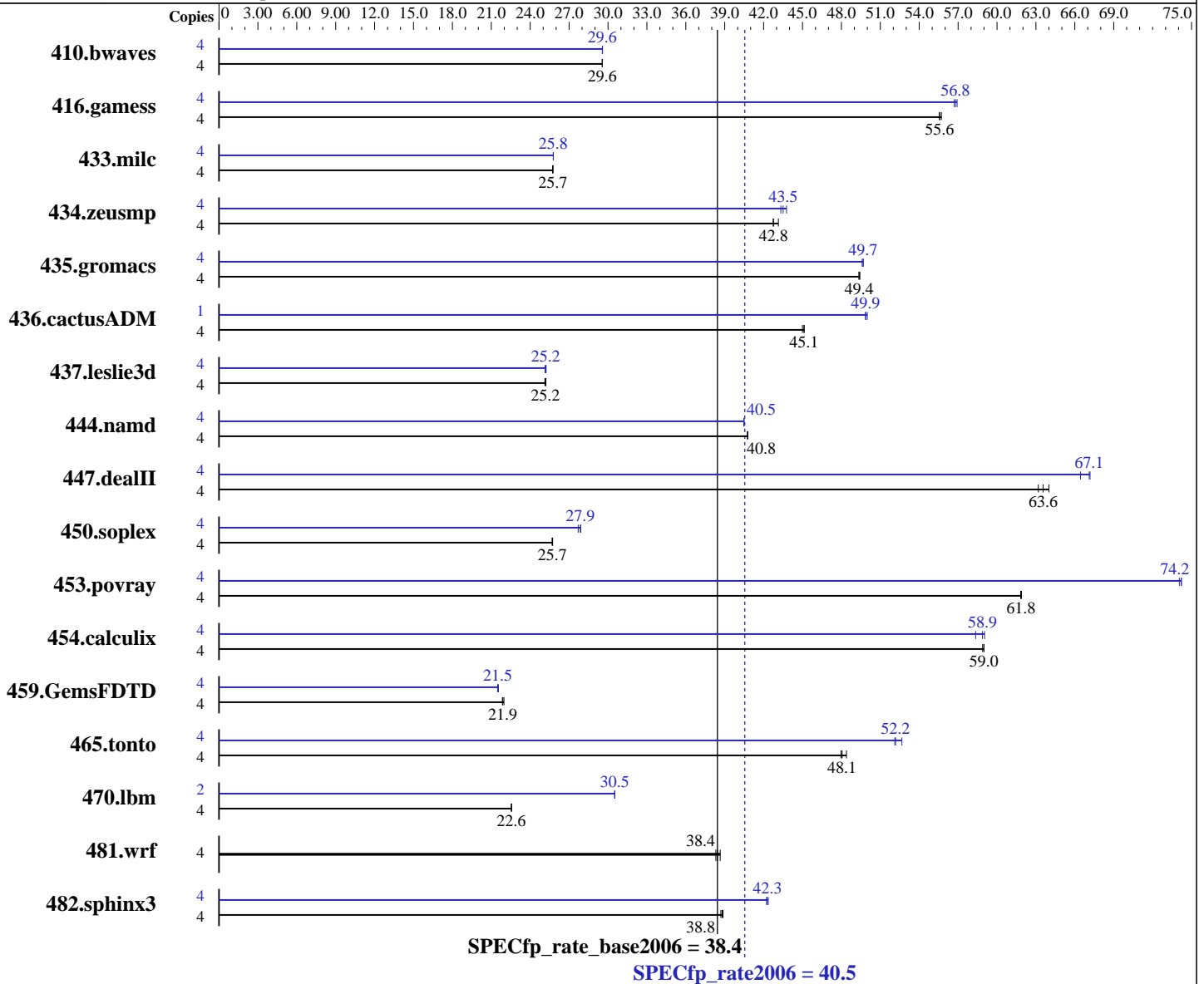
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jan-2009

Hardware Availability: Jan-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon L5215
 CPU Characteristics: 1066 MHz system bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074, l_cprof_p_11.0.074
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5215)

SPECfp_rate2006 = 40.5

SPECfp_rate_base2006 = 38.4

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jan-2009
Hardware Availability: Jan-2009
Software Availability: Nov-2008

L3 Cache: None
Other Cache: None
Memory: 16 GB (4x4 GB PC2-5300P, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x80 GB SATAII, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1840	29.5	1838	29.6	<u>1839</u>	<u>29.6</u>	4	1839	29.6	<u>1839</u>	<u>29.6</u>	1838	29.6
416.gamess	4	1410	55.6	1406	55.7	<u>1409</u>	<u>55.6</u>	4	1381	56.7	1376	56.9	<u>1379</u>	<u>56.8</u>
433.milc	4	1426	25.7	1427	25.7	<u>1426</u>	<u>25.7</u>	4	1424	25.8	1424	25.8	<u>1424</u>	<u>25.8</u>
434.zeusmp	4	852	42.7	844	43.1	<u>851</u>	<u>42.8</u>	4	832	43.8	840	43.3	<u>837</u>	<u>43.5</u>
435.gromacs	4	<u>579</u>	<u>49.4</u>	578	49.4	579	49.4	4	<u>575</u>	<u>49.7</u>	575	49.7	576	49.6
436.cactusADM	4	1062	45.0	1059	45.1	<u>1060</u>	<u>45.1</u>	1	239	50.0	<u>240</u>	<u>49.9</u>	240	49.9
437.leslie3d	4	1496	25.1	1491	25.2	<u>1493</u>	<u>25.2</u>	4	1495	25.1	<u>1492</u>	<u>25.2</u>	1490	25.2
444.namd	4	<u>787</u>	<u>40.8</u>	787	40.8	787	40.7	4	792	40.5	<u>792</u>	<u>40.5</u>	792	40.5
447.dealII	4	724	63.2	<u>720</u>	<u>63.6</u>	715	64.0	4	689	66.4	<u>682</u>	<u>67.1</u>	681	67.2
450.soplex	4	1299	25.7	1296	25.7	<u>1297</u>	<u>25.7</u>	4	1204	27.7	<u>1197</u>	<u>27.9</u>	1196	27.9
453.povray	4	344	61.8	<u>344</u>	<u>61.8</u>	344	61.9	4	287	74.2	<u>287</u>	<u>74.2</u>	287	74.1
454.calculix	4	560	58.9	559	59.0	<u>560</u>	<u>59.0</u>	4	<u>560</u>	<u>58.9</u>	559	59.0	566	58.4
459.GemsFDTD	4	1942	21.9	<u>1939</u>	<u>21.9</u>	1931	22.0	4	<u>1974</u>	<u>21.5</u>	1974	21.5	1969	21.5
465.tonto	4	813	48.4	<u>819</u>	<u>48.1</u>	821	48.0	4	747	52.7	<u>754</u>	<u>52.2</u>	755	52.1
470.lbm	4	2437	22.5	<u>2437</u>	<u>22.6</u>	2436	22.6	2	900	30.5	901	30.5	<u>900</u>	<u>30.5</u>
481.wrf	4	1166	38.3	1156	38.7	<u>1162</u>	<u>38.4</u>	4	1166	38.3	1156	38.7	<u>1162</u>	<u>38.4</u>
482.sphinx3	4	2015	38.7	<u>2009</u>	<u>38.8</u>	2006	38.9	4	1847	42.2	<u>1843</u>	<u>42.3</u>	1841	42.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5215)

SPECfp_rate2006 = 40.5

SPECfp_rate_base2006 = 38.4

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jan-2009
Hardware Availability: Jan-2009
Software Availability: Nov-2008

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5215)

SPECfp_rate2006 = 40.5

SPECfp_rate_base2006 = 38.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jan-2009

Hardware Availability: Jan-2009

Software Availability: Nov-2008

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/074/bin/ia32/icc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/074/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/074/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5215)

SPECfp_rate2006 = 40.5

SPECfp_rate_base2006 = 38.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jan-2009

Hardware Availability: Jan-2009

Software Availability: Nov-2008

Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5215)

SPECfp_rate2006 = 40.5

SPECfp_rate_base2006 = 38.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jan-2009

Hardware Availability: Jan-2009

Software Availability: Nov-2008

Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Tue Jul 22 23:00:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 3 March 2009.