



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp[®]_rate2006 = Not Run

Servidor Itautec MX201 (Intel Xeon E5440)

SPECfp_rate_base2006 = 71.7

CPU2006 license: 9001

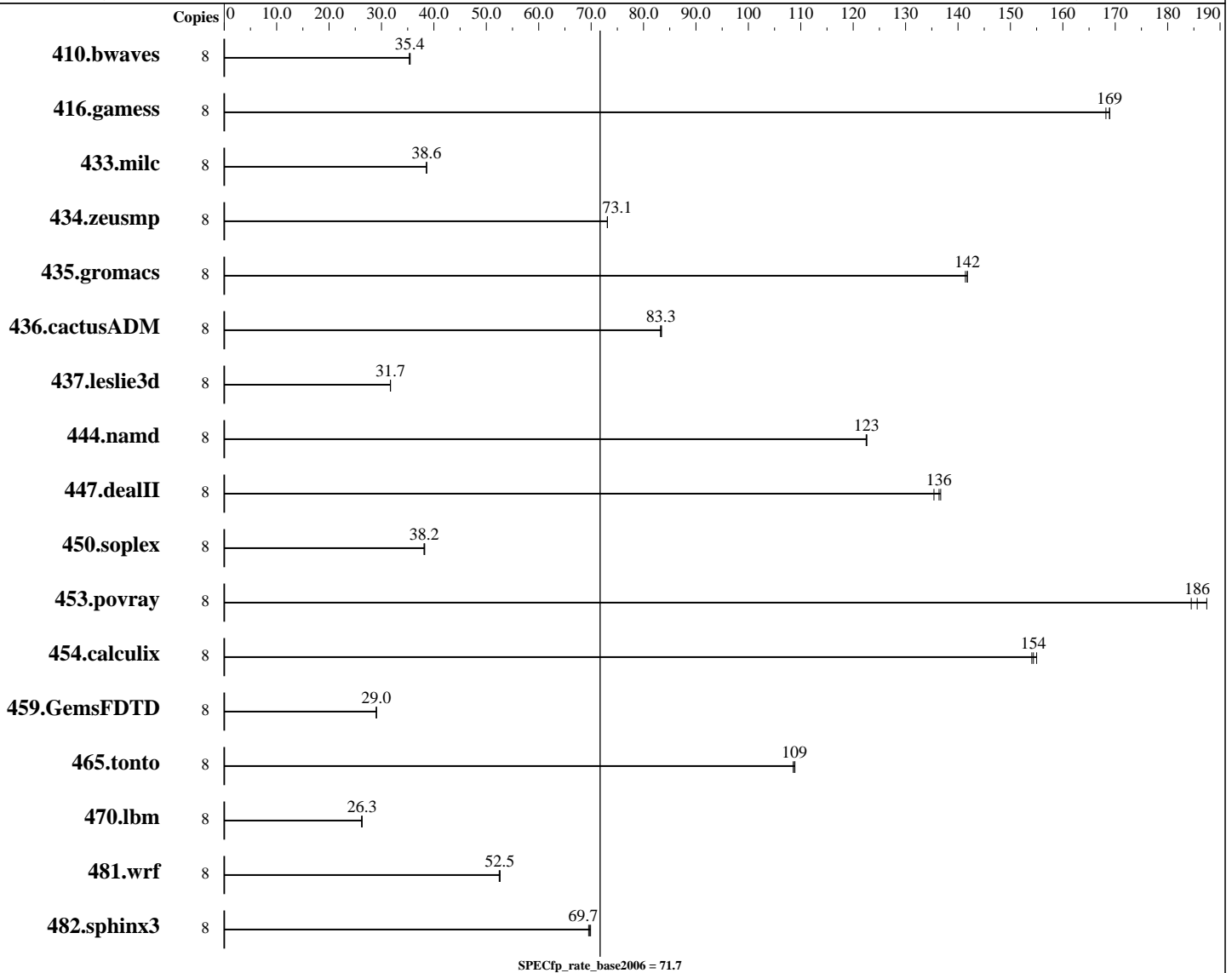
Test sponsor: Itautec

Tested by: Itautec

Test date: Jan-2009

Hardware Availability: Dec-2007

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics: 2830
 CPU MHz: Integrated
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_b_11.0.074, l_fproc_b_11.0.074
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp_rate2006 = Not Run

Servidor Itaotec MX201 (Intel Xeon E5440)

SPECfp_rate_base2006 = 71.7

CPU2006 license: 9001

Test date: Jan-2009

Test sponsor: Itaotec

Hardware Availability: Dec-2007

Tested by: Itaotec

Software Availability: Nov-2008

L3 Cache: None
Other Cache: None
Memory: 16 GB (4*4 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem: 1 x SAS, 36GB, 15000RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3064	35.5	3079	35.3	3073	35.4							
416.gamess	8	927	169	927	169	931	168							
433.milc	8	1903	38.6	1903	38.6	1903	38.6							
434.zeusmp	8	996	73.1	996	73.1	996	73.1							
435.gromacs	8	404	141	403	142	403	142							
436.cactusADM	8	1149	83.2	1146	83.4	1147	83.3							
437.leslie3d	8	2373	31.7	2370	31.7	2372	31.7							
444.namd	8	523	123	524	123	524	122							
447.dealII	8	671	136	676	135	670	137							
450.soplex	8	1750	38.1	1744	38.3	1748	38.2							
453.povray	8	227	187	229	186	231	184							
454.calculix	8	428	154	426	155	428	154							
459.GemsFDTD	8	2924	29.0	2925	29.0	2924	29.0							
465.tonto	8	724	109	725	109	723	109							
470.lbm	8	4195	26.2	4182	26.3	4181	26.3							
481.wrf	8	1701	52.5	1703	52.5	1698	52.6							
482.sphinx3	8	2243	69.5	2231	69.9	2236	69.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS Settings:
Hardware Prefetcher Disabled



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp_rate2006 = Not Run

Servidor Itaotec MX201 (Intel Xeon E5440)

SPECfp_rate_base2006 = 71.7

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Jan-2009
Hardware Availability: Dec-2007
Software Availability: Nov-2008

General Notes

This result was measured on the Servidor Itaotec MX221.
The Servidor Itaotec MX201 and the Servidor Itaotec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp_rate2006 = Not Run

Servidor Itautec MX201 (Intel Xeon E5440)

SPECfp_rate_base2006 = 71.7

CPU2006 license: 9001

Test sponsor: Itautec

Tested by: Itautec

Test date: Jan-2009

Hardware Availability: Dec-2007

Software Availability: Nov-2008

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Tue Jul 22 23:12:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 17 February 2009.