SPEC® CINT2006 Result
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Dell Inc.

PowerEdge R300 (Intel Xeon X3363, 2.83 GHz)

$SPECint^{®}2006 =$ 26.2

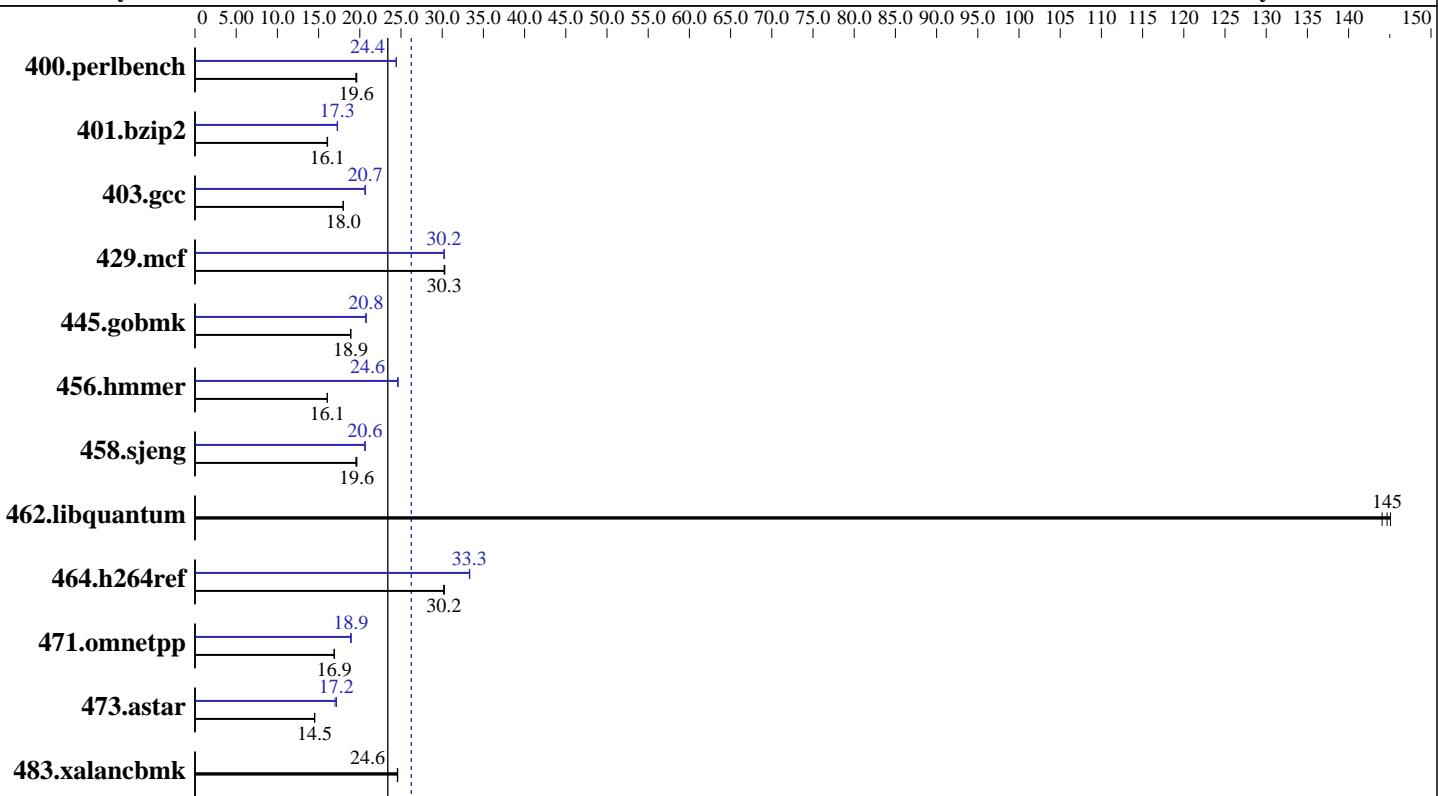SPECint_base2006 =   23.4

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2008

Benchmark results (peak shown in blue, base below):

- **400.perlbench** 24.4 / 19.6
- **401.bzip2** 17.3 / 16.1
- **403.gcc** 20.7 / 18.0
- **429.mcf** 30.2 / 30.3
- **445.gobmk** 20.8 / 18.9
- **456.hmmer** 24.6 / 16.1
- **458.sjeng** 20.6 / 19.6
- **462.libquantum** 145
- **464.h264ref** 33.3 / 30.2
- **471.omnetpp** 18.9 / 16.9
- **473.astar** 17.2 / 14.5
- **483.xalancbmk** 24.6

SPECint_base2006 = 23.4
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Hardware

CPU Name:	Intel Xeon X3363
CPU Characteristics:	
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4 x 4 GB DDR2-667 FBDIMM)
Disk Subsystem:	2 x 36 GB SAS, 15k RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	498	19.6	500	19.5	**499**	**19.6**	400	24.4	400	24.4	**400**	**24.4**
401.bzip2	600	16.1	602	16.0	**601**	**16.1**	**558**	**17.3**	559	17.3	558	17.3
403.gcc	447	18.0	**447**	**18.0**	448	18.0	**390**	**20.7**	390	20.6	390	20.7
429.mcf	302	30.2	301	30.3	**301**	**30.3**	**302**	**30.2**	301	30.3	302	30.2
445.gobmk	555	18.9	**555**	**18.9**	555	18.9	**505**	**20.8**	505	20.8	505	20.8
456.hmmer	581	16.1	**581**	**16.1**	581	16.1	379	24.6	379	24.6	**379**	**24.6**
458.sjeng	616	19.6	**617**	**19.6**	620	19.5	587	20.6	586	20.6	**587**	**20.6**
462.libquantum	**143**	**145**	144	144	143	145	**143**	**145**	144	144	143	145
464.h264ref	**732**	**30.2**	731	30.3	733	30.2	664	33.3	665	33.3	**664**	**33.3**
471.omnetpp	**370**	**16.9**	369	16.9	370	16.9	**330**	**18.9**	331	18.9	330	18.9
473.astar	483	14.5	484	14.5	**483**	**14.5**	409	17.2	413	17.0	**409**	**17.2**
483.xalancbmk	**281**	**24.6**	281	24.6	281	24.6	**281**	**24.6**	281	24.6	281	24.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

    OMP_NUM_THREADS set to number of processors
    KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
    -par-runtime-control -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

> 401.bzip2: `/opt/intel/Compiler/11.0/042/bin/intel64/icc`
> `-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib`
> `-I/opt/intel/Compiler/11.0/042/ipp/em64t/include`

> 456.hmmer: `/opt/intel/Compiler/11.0/042/bin/intel64/icc`
> `-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib`
> `-I/opt/intel/Compiler/11.0/042/ipp/em64t/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

> 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
> 401.bzip2: `-DSPEC_CPU_LP64`
> 456.hmmer: `-DSPEC_CPU_LP64`
> 462.libquantum: `-DSPEC_CPU_LINUX`
> 483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

> 400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3`
> `-no-prec-div -static -ansi-alias -opt-prefetch`

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Tested by:	Dell Inc.	**Software Availability:**	Nov-2008

Peak Optimization Flags (Continued)

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3`
`-no-prec-div -static -auto-ilp32 -opt-prefetch`
`-ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc`
`-opt-malloc-options=3`

429.mcf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3`
`-no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3`
`-no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
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