SPEC® CINT2006 Result

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Dell Inc.

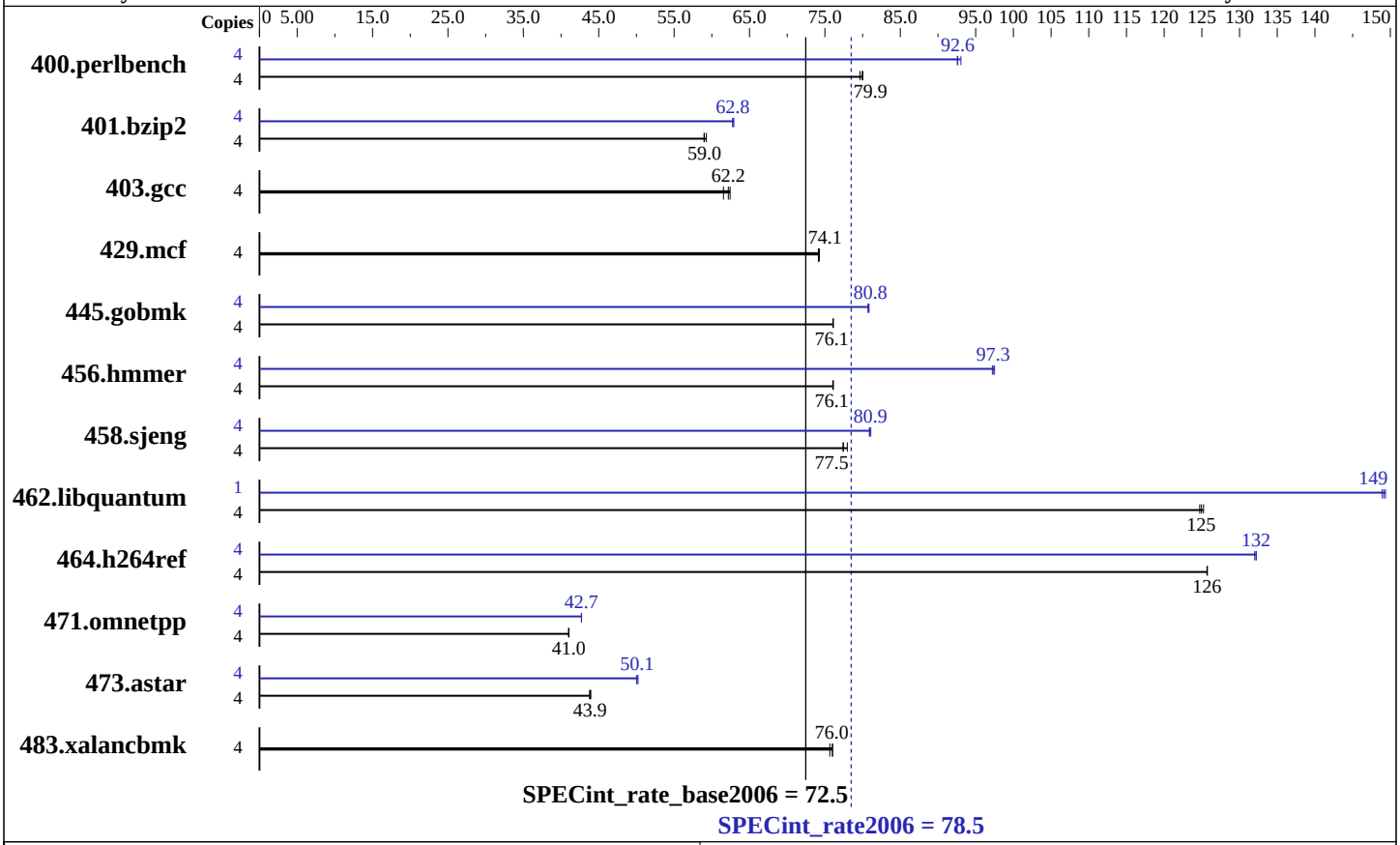
PowerEdge T300 (Intel Xeon X3363, 2.83 GHz)

SPECint®_rate2006 = 78.5

SPECint_rate_base2006 = 72.5

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X3363
CPU Characteristics:	
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4 x 4 GB DDR2-667 FBDIMM)
Disk Subsystem:	1 x 300 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**489**	**79.9**	491	79.7	488	80.0	4	422	92.6	**422**	**92.6**	420	93.0
401.bzip2	4	651	59.3	655	59.0	**654**	**59.0**	4	613	62.9	**615**	**62.8**	615	62.8
403.gcc	4	**518**	**62.2**	516	62.4	523	61.5	4	**518**	**62.2**	516	62.4	523	61.5
429.mcf	4	**492**	**74.1**	492	74.1	491	74.3	4	**492**	**74.1**	492	74.1	491	74.3
445.gobmk	4	552	76.1	**551**	**76.1**	551	76.1	4	520	80.7	**520**	**80.8**	519	80.9
456.hmmer	4	**490**	**76.1**	490	76.1	491	76.0	4	383	97.5	**383**	**97.3**	384	97.2
458.sjeng	4	621	78.0	625	77.4	**625**	**77.5**	4	597	81.1	**598**	**80.9**	598	80.9
462.libquantum	4	**663**	**125**	665	125	662	125	1	139	149	**139**	**149**	139	149
464.h264ref	4	704	126	**704**	**126**	704	126	4	**670**	**132**	671	132	670	132
471.omnetpp	4	610	41.0	609	41.0	**609**	**41.0**	4	586	42.7	**585**	**42.7**	585	42.7
473.astar	4	639	43.9	641	43.8	**640**	**43.9**	4	561	50.0	**561**	**50.1**	559	50.2
483.xalancbmk	4	**363**	**76.0**	365	75.7	363	76.0	4	**363**	**76.0**	365	75.7	363	76.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

General Notes

```
taskset was used to bind processes to cores except
for 462.libquantum peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/042/bin/intel64/icc -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib -I/opt/intel/Compiler/11.0/042/ipp/em64t/include`

456.hmmer: `/opt/intel/Compiler/11.0/042/bin/intel64/icc -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib -I/opt/intel/Compiler/11.0/042/ipp/em64t/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -parallel -par-runtime-control -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml`

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For questions about this result, please contact the tester.
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