



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 89.4

Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECint_rate_base2006 = 85.1

CPU2006 license: 55

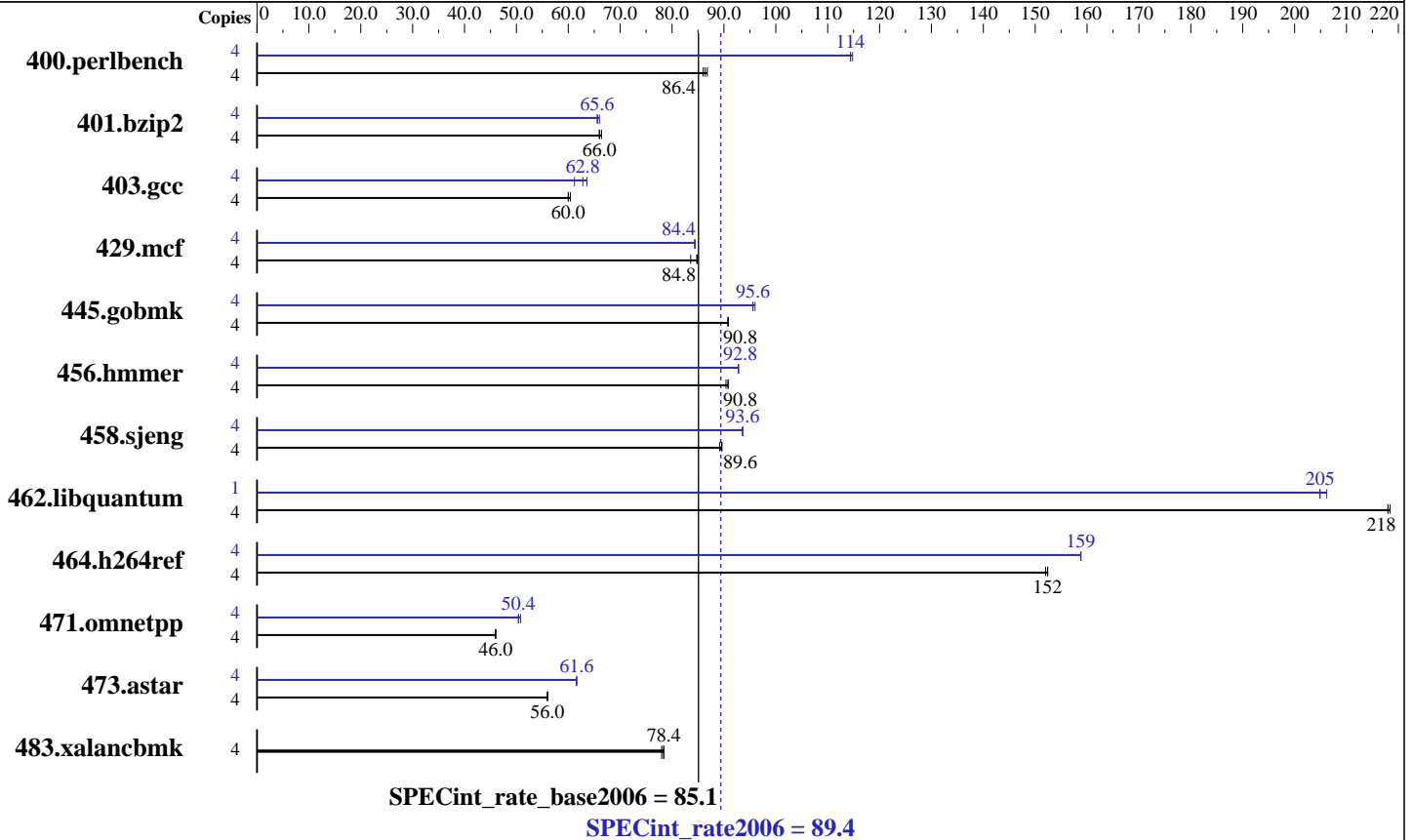
Test date: Dec-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5272
 CPU Characteristics: 1600 MHz Bus Speed
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2-800 FB-DIMM, CL5)
 Disk Subsystem: 1 x 80 GB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Business SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA-32, Version 11.0
 Build 20080930 Package ID: w_cproc_p_11.0.061
 Microsoft Visual Studio 2008 SP1
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	453	86.0	<u>452</u>	<u>86.4</u>	451	86.8	4	341	115	<u>341</u>	<u>114</u>	341	114
401.bzip2	4	586	66.0	580	66.4	<u>586</u>	<u>66.0</u>	4	586	66.0	589	65.6	<u>588</u>	<u>65.6</u>
403.gcc	4	538	60.0	<u>535</u>	<u>60.0</u>	535	60.4	4	507	63.6	<u>513</u>	<u>62.8</u>	528	61.2
429.mcf	4	431	84.8	436	83.6	<u>431</u>	<u>84.8</u>	4	432	84.4	432	84.4	<u>432</u>	<u>84.4</u>
445.gobmk	4	<u>462</u>	<u>90.8</u>	463	90.8	462	90.8	4	<u>439</u>	<u>95.6</u>	438	96.0	439	95.6
456.hammer	4	411	90.8	<u>411</u>	<u>90.8</u>	412	90.4	4	402	92.8	<u>403</u>	<u>92.8</u>	403	92.8
458.sjeng	4	<u>541</u>	<u>89.6</u>	541	89.6	542	89.2	4	517	93.6	<u>517</u>	<u>93.6</u>	518	93.6
462.libquantum	4	380	218	<u>380</u>	<u>218</u>	380	218	1	<u>101</u>	<u>205</u>	101	206	101	205
464.h264ref	4	<u>582</u>	<u>152</u>	582	152	582	152	4	557	159	557	159	<u>557</u>	<u>159</u>
471.omnetpp	4	543	46.0	544	46.0	<u>544</u>	<u>46.0</u>	4	<u>495</u>	<u>50.4</u>	494	50.8	495	50.4
473.astar	4	<u>502</u>	<u>56.0</u>	502	56.0	502	56.0	4	457	61.6	456	61.6	<u>456</u>	<u>61.6</u>
483.xalancbmk	4	353	78.0	351	78.4	<u>352</u>	<u>78.4</u>	4	353	78.0	351	78.4	<u>352</u>	<u>78.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings

Adjacent Cache Line Prefetch set to ON

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000

403.gcc: -Qprof_gen(pass 1) -QxSSE4.1 -Qprof_use(pass 2) -Qipo -O3
-Qprec-div- /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qparallel -Qpar-runtime-control -Qvec-guard-write
/F512000000

464.h264ref: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.xml>



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For other inquiries, please contact webmaster@spec.org.

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