



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge 2900 III (Intel Xeon E5420, 2.50 GHz)

SPECint_rate_base2006 = 118

CPU2006 license: 55

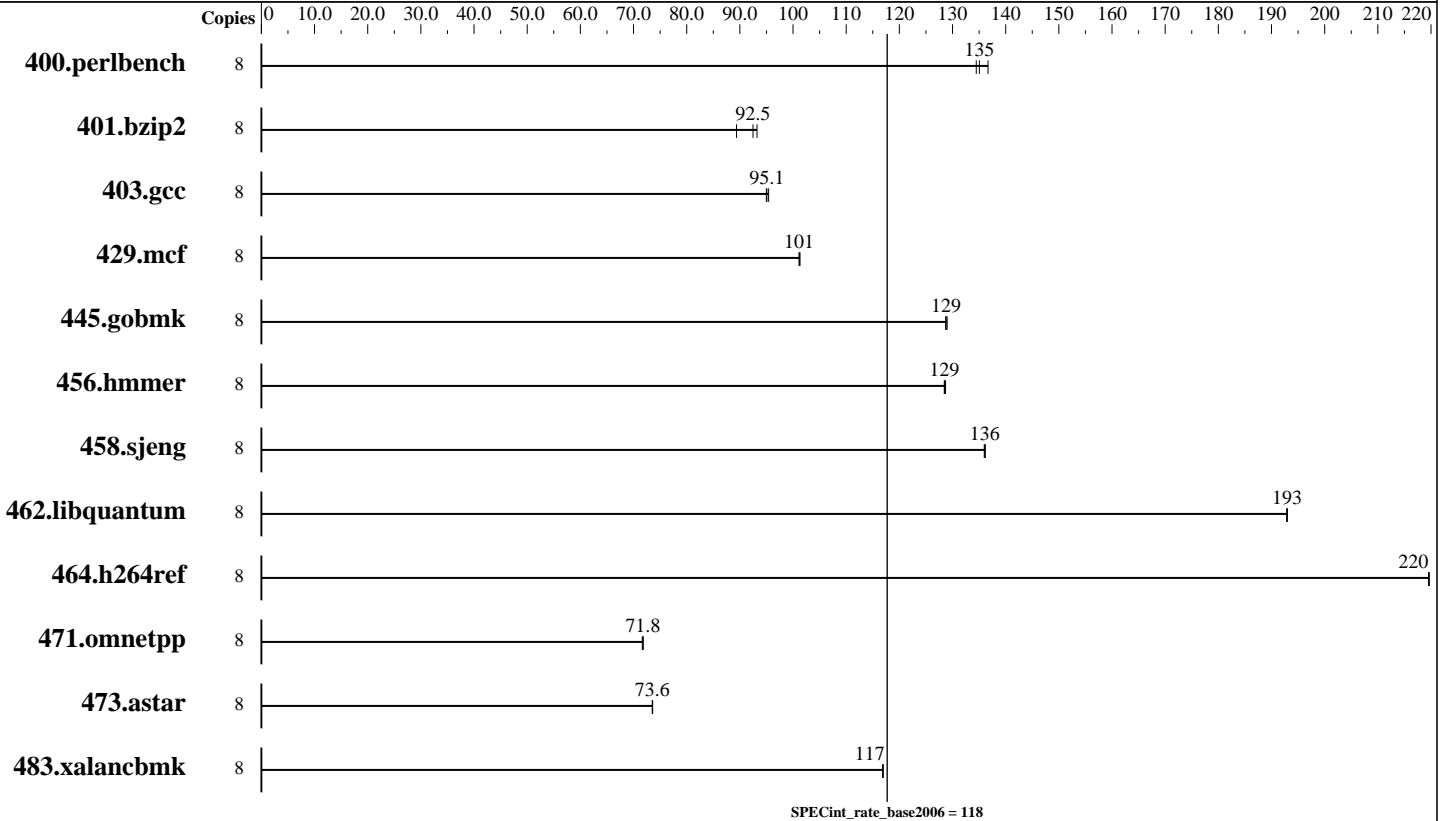
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5420
 CPU Characteristics:
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667 MHz CL5 DDR2 Dual Rank)
 Disk Subsystem: 2 x 73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	581	134	572	137	<u>579</u>	<u>135</u>							
401.bzip2	8	864	89.4	828	93.2	<u>835</u>	<u>92.5</u>							
403.gcc	8	675	95.4	<u>677</u>	<u>95.1</u>	678	95.0							
429.mcf	8	720	101	<u>721</u>	<u>101</u>	721	101							
445.gobmk	8	<u>652</u>	<u>129</u>	652	129	651	129							
456.hammer	8	581	128	580	129	<u>581</u>	<u>129</u>							
458.sjeng	8	712	136	711	136	<u>711</u>	<u>136</u>							
462.libquantum	8	859	193	<u>859</u>	<u>193</u>	859	193							
464.h264ref	8	<u>806</u>	<u>220</u>	806	220	806	220							
471.omnetpp	8	<u>697</u>	<u>71.8</u>	696	71.8	698	71.6							
473.astar	8	<u>764</u>	<u>73.6</u>	763	73.6	764	73.5							
483.xalancbmk	8	<u>472</u>	<u>117</u>	472	117	472	117							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.01.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.01.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.10.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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