SPEC® CINT2006 Result

Bull SAS

NovaScale B260
(Intel Xeon E5430, 2.66 GHz)

SPECint®_rate2006 =   114

SPECint_rate_base2006 = 89.0

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Sep-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007

SPECint_rate_base2006 = 89.0

SPECint_rate2006 = 114

Hardware

CPU Name: Intel Xeon E5430
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (4x4 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux
Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15
SmartHeap library V8.1

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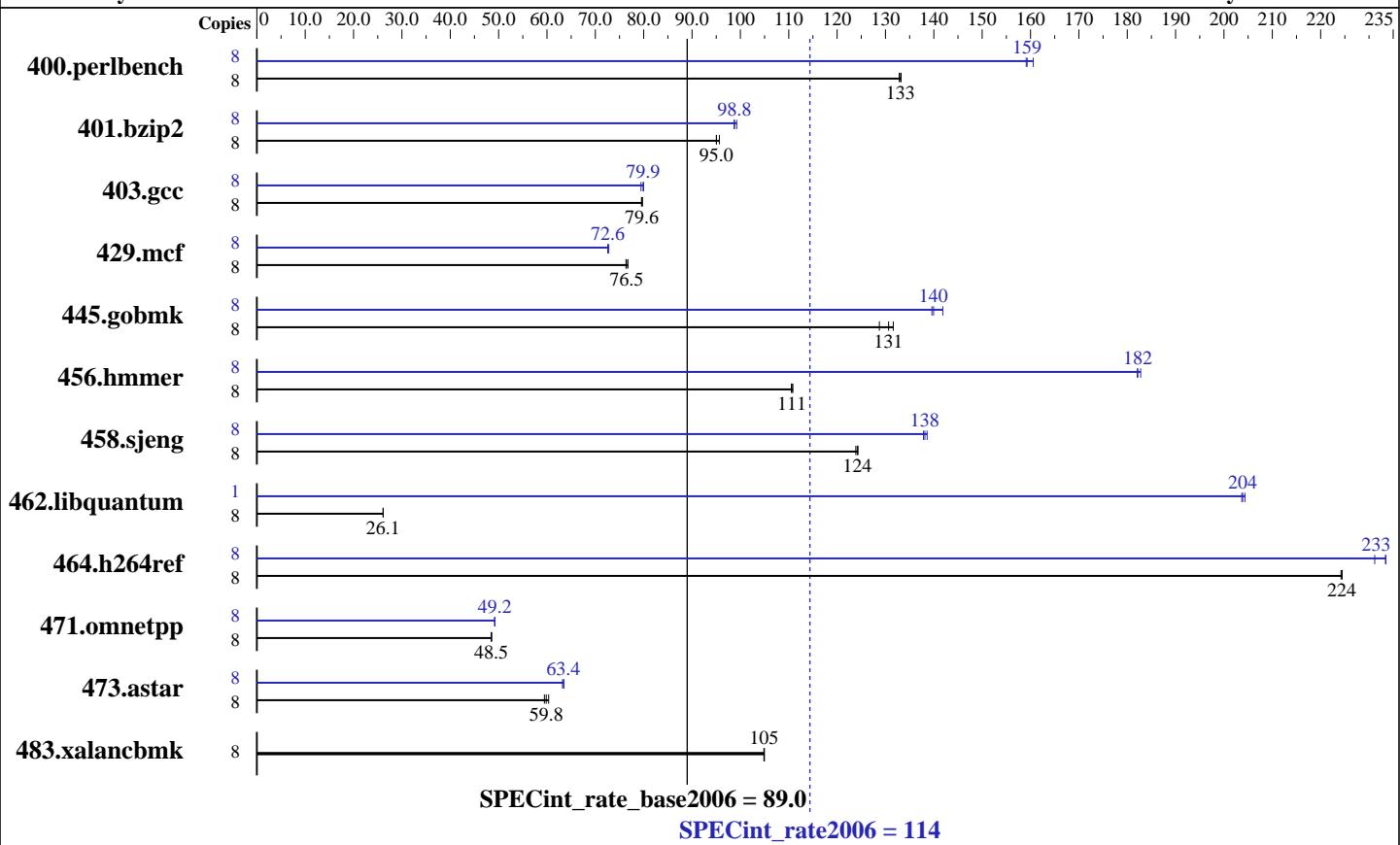
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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	**587**	**133**	588	133	587	133	8	487	161	491	159	**491**	**159**
401.bzip2	8	812	95.0	807	95.6	**812**	**95.0**	8	778	99.2	782	98.7	**781**	**98.8**
403.gcc	8	**809**	**79.6**	810	79.5	808	79.7	8	805	80.0	**806**	**79.9**	811	79.4
429.mcf	8	**954**	**76.5**	951	76.7	956	76.3	8	1005	72.6	1002	72.8	**1005**	**72.6**
445.gobmk	8	652	129	638	132	**642**	**131**	8	592	142	**600**	**140**	601	140
456.hmmer	8	675	111	**674**	**111**	673	111	8	**410**	**182**	408	183	410	182
458.sjeng	8	**780**	**124**	779	124	781	124	8	698	139	**701**	**138**	702	138
462.libquantum	8	6346	26.1	6345	26.1	**6346**	**26.1**	1	**102**	**204**	101	204	102	204
464.h264ref	8	789	224	790	224	**789**	**224**	8	766	231	**759**	**233**	758	234
471.omnetpp	8	**1031**	**48.5**	1031	48.5	1030	48.5	8	1016	49.2	1016	49.2	**1016**	**49.2**
473.astar	8	**938**	**59.8**	931	60.3	944	59.5	8	**885**	**63.4**	884	63.5	889	63.2
483.xalancbmk	8	526	105	**526**	**105**	526	105	8	526	105	**526**	**105**	526	105

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
    OMP_NUM_THREADS set to number of cores
    KMP_AFFINITY set to physical,0
    KMP_STACKSIZE set to 64M

Platform Notes

    BIOS configuration:
    Hardware Prefetcher Enabled
    Adjacent Cache-Line Prefetch Disabled

General Notes

    All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
    for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
        icc

C++ benchmarks:
        icpc

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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