# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

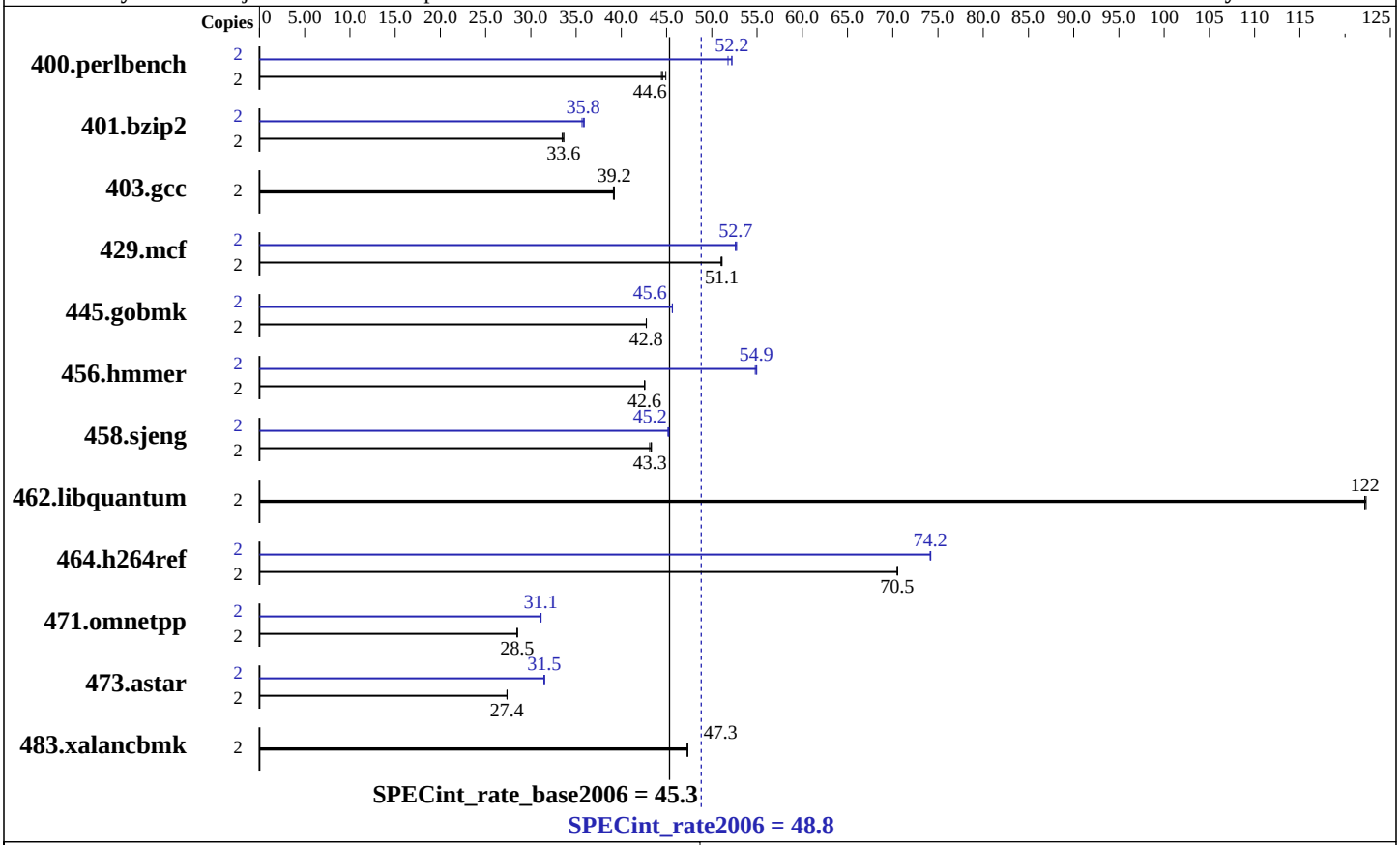
PRIMERGY TX150 S6, Intel Xeon E3120, 3.16 GHz

SPECint®_rate2006 = 48.8

SPECint_rate_base2006 = 45.3

**CPU2006 license:** 22
**Test sponsor:** Fujitsu Siemens Computers
**Tested by:** Fujitsu Siemens Computers

**Test date:** Sep-2008
**Hardware Availability:** Oct-2008
**Software Availability:** Nov-2008




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3120 |
| CPU Characteristics: | 1333 MHz system bus |
| CPU MHz: | 3167 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 6 MB I+D on chip per chip |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC) |
| Disk Subsystem: | 1x SATA, 80 GB, 7200 rpm |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 2 | **439** | **44.6** | 435 | 44.9 | 440 | 44.4 | 2 | 374 | 52.2 | 377 | 51.8 | **374** | **52.2** |
| 401.bzip2 | 2 | **574** | **33.6** | 574 | 33.6 | 577 | 33.5 | 2 | 538 | 35.9 | 541 | 35.7 | **539** | **35.8** |
| 403.gcc | 2 | **411** | **39.2** | 412 | 39.1 | 411 | 39.2 | 2 | **411** | **39.2** | 412 | 39.1 | 411 | 39.2 |
| 429.mcf | 2 | 358 | 51.0 | **357** | **51.1** | 357 | 51.1 | 2 | 346 | 52.7 | 347 | 52.6 | **346** | **52.7** |
| 445.gobmk | 2 | 491 | 42.8 | **491** | **42.8** | 491 | 42.8 | 2 | **460** | **45.6** | 460 | 45.6 | 460 | 45.6 |
| 456.hmmer | 2 | 438 | 42.6 | 438 | 42.6 | **438** | **42.6** | 2 | 339 | 55.0 | 341 | 54.8 | **340** | **54.9** |
| 458.sjeng | 2 | 561 | 43.1 | 559 | 43.3 | **559** | **43.3** | 2 | 535 | 45.3 | 536 | 45.1 | **535** | **45.2** |
| 462.libquantum | 2 | **339** | **122** | 339 | 122 | 339 | 122 | 2 | **339** | **122** | 339 | 122 | 339 | 122 |
| 464.h264ref | 2 | 627 | 70.5 | 628 | 70.5 | **628** | **70.5** | 2 | **597** | **74.2** | 596 | 74.2 | 597 | 74.1 |
| 471.omnetpp | 2 | 438 | 28.5 | **439** | **28.5** | 439 | 28.5 | 2 | 402 | 31.1 | **402** | **31.1** | 402 | 31.1 |
| 473.astar | 2 | **513** | **27.4** | 513 | 27.4 | 513 | 27.4 | 2 | 446 | 31.5 | 447 | 31.4 | **446** | **31.5** |
| 483.xalancbmk | 2 | 291 | 47.3 | 292 | 47.3 | **292** | **47.3** | 2 | 291 | 47.3 | 292 | 47.3 | **292** | **47.3** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable
```

## General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

## Base Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:
icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

Continued on next page

## Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.xml`


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For other inquiries, please contact webmaster@spec.org.

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