SPEC® CINT2006 Result
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Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge M600 (Intel Xeon E5420, 2.50 GHz)
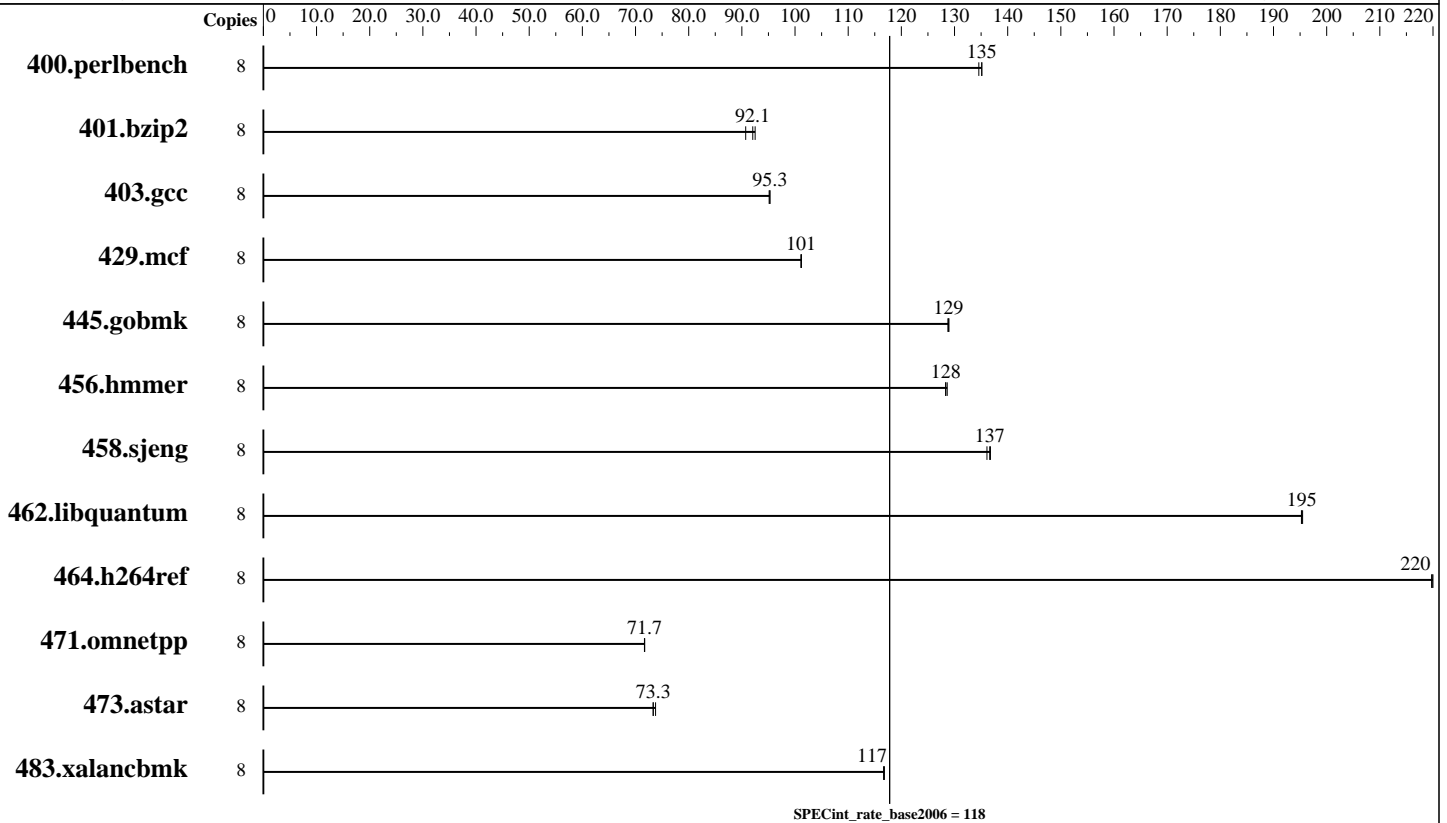
SPECint_rate_base2006 = 118

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Copies		
400.perlbench	8	135
401.bzip2	8	92.1
403.gcc	8	95.3
429.mcf	8	101
445.gobmk	8	129
456.hmmer	8	128
458.sjeng	8	137
462.libquantum	8	195
464.h264ref	8	220
471.omnetpp	8	71.7
473.astar	8	73.3
483.xalancbmk	8	117

SPECint_rate_base2006 = 118

Hardware

CPU Name: Intel Xeon E5420
CPU Characteristics:
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem: 1 x 80 GB 5400 RPM SATA
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cc_b_11.0.042
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	581	135	578	135	**578**	**135**							
401.bzip2	8	835	92.5	851	90.7	**839**	**92.1**							
403.gcc	8	676	95.3	**676**	**95.3**	677	95.1							
429.mcf	8	721	101	722	101	**721**	**101**							
445.gobmk	8	**651**	**129**	651	129	652	129							
456.hmmer	8	582	128	580	129	**582**	**128**							
458.sjeng	8	711	136	708	137	**709**	**137**							
462.libquantum	8	849	195	**849**	**195**	848	195							
464.h264ref	8	805	220	806	220	**805**	**220**							
471.omnetpp	8	698	71.7	697	71.7	**697**	**71.7**							
473.astar	8	761	73.8	766	73.3	**766**	**73.3**							
483.xalancbmk	8	473	117	473	117	**473**	**117**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.

Operating System Notes

     'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

     BIOS Settings:
     Hardware Prefetcher = Disabled (Default = Enabled)
     Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

General Notes

     All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
     for peak, are compiled in 64-bit mode
     taskset was used to bind processes to cores except
     for 462.libquantum peak

Base Compiler Invocation

    C benchmarks:
        icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
    -xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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