



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 26.8

PowerEdge M600 (Intel Xeon L5240, 3.00 GHz)

SPECint\_base2006 = 23.6

CPU2006 license: 55

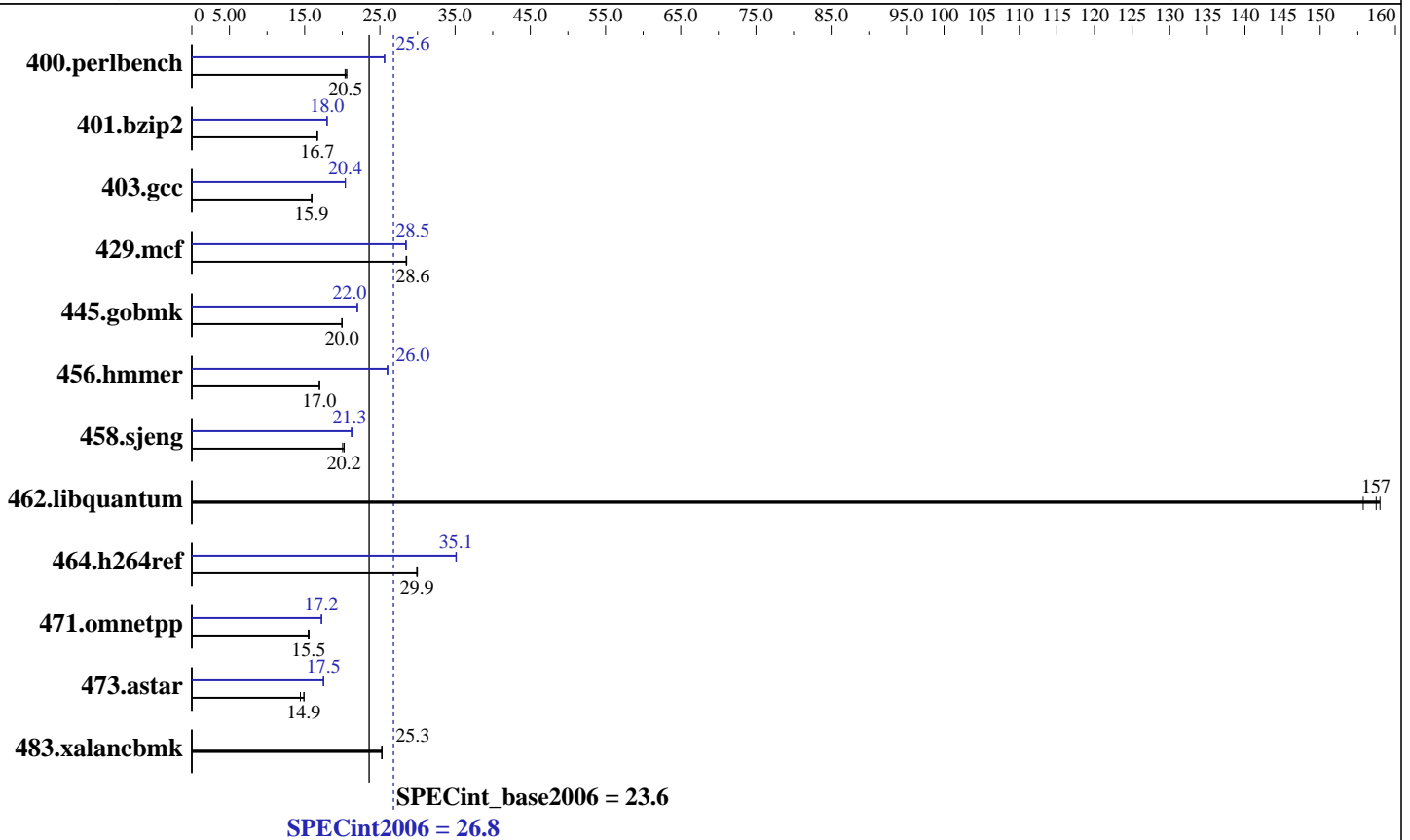
Test date: Sep-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



## Hardware

CPU Name: Intel Xeon L5240  
 CPU Characteristics: 3000  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4x4 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)  
 Disk Subsystem: 1 x 80 GB SATA 5400 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	480	20.4	<b><u>477</u></b>	<b><u>20.5</u></b>	474	20.6	381	25.6	<b><u>381</u></b>	<b><u>25.6</u></b>	381	25.6
401.bzip2	<b><u>578</u></b>	<b><u>16.7</u></b>	578	16.7	580	16.6	537	18.0	<b><u>537</u></b>	<b><u>18.0</u></b>	537	18.0
403.gcc	<b><u>505</u></b>	<b><u>15.9</u></b>	506	15.9	504	16.0	<b><u>395</u></b>	<b><u>20.4</u></b>	395	20.4	394	20.4
429.mcf	320	28.5	<b><u>319</u></b>	<b><u>28.6</u></b>	319	28.6	321	28.4	<b><u>320</u></b>	<b><u>28.5</u></b>	320	28.5
445.gobmk	<b><u>525</u></b>	<b><u>20.0</u></b>	525	20.0	526	19.9	<b><u>477</u></b>	<b><u>22.0</u></b>	477	22.0	477	22.0
456.hmmmer	550	17.0	<b><u>550</u></b>	<b><u>17.0</u></b>	550	17.0	<b><u>358</u></b>	<b><u>26.0</u></b>	358	26.0	359	26.0
458.sjeng	604	20.0	598	20.2	<b><u>598</u></b>	<b><u>20.2</u></b>	569	21.3	<b><u>569</u></b>	<b><u>21.3</u></b>	570	21.2
462.libquantum	<b><u>132</u></b>	<b><u>157</u></b>	131	158	133	156	<b><u>132</u></b>	<b><u>157</u></b>	131	158	133	156
464.h264ref	738	30.0	<b><u>740</u></b>	<b><u>29.9</u></b>	740	29.9	630	35.1	<b><u>630</u></b>	<b><u>35.1</u></b>	629	35.2
471.omnetpp	401	15.6	<b><u>402</u></b>	<b><u>15.5</u></b>	403	15.5	363	17.2	<b><u>363</u></b>	<b><u>17.2</u></b>	362	17.3
473.astar	<b><u>471</u></b>	<b><u>14.9</u></b>	486	14.4	471	14.9	402	17.5	400	17.5	<b><u>400</u></b>	<b><u>17.5</u></b>
483.xalanbmk	273	25.3	273	25.2	<b><u>273</u></b>	<b><u>25.3</u></b>	273	25.3	273	25.2	<b><u>273</u></b>	<b><u>25.3</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode  
OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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