SPEC® CFP2006 Result

Dell Inc.

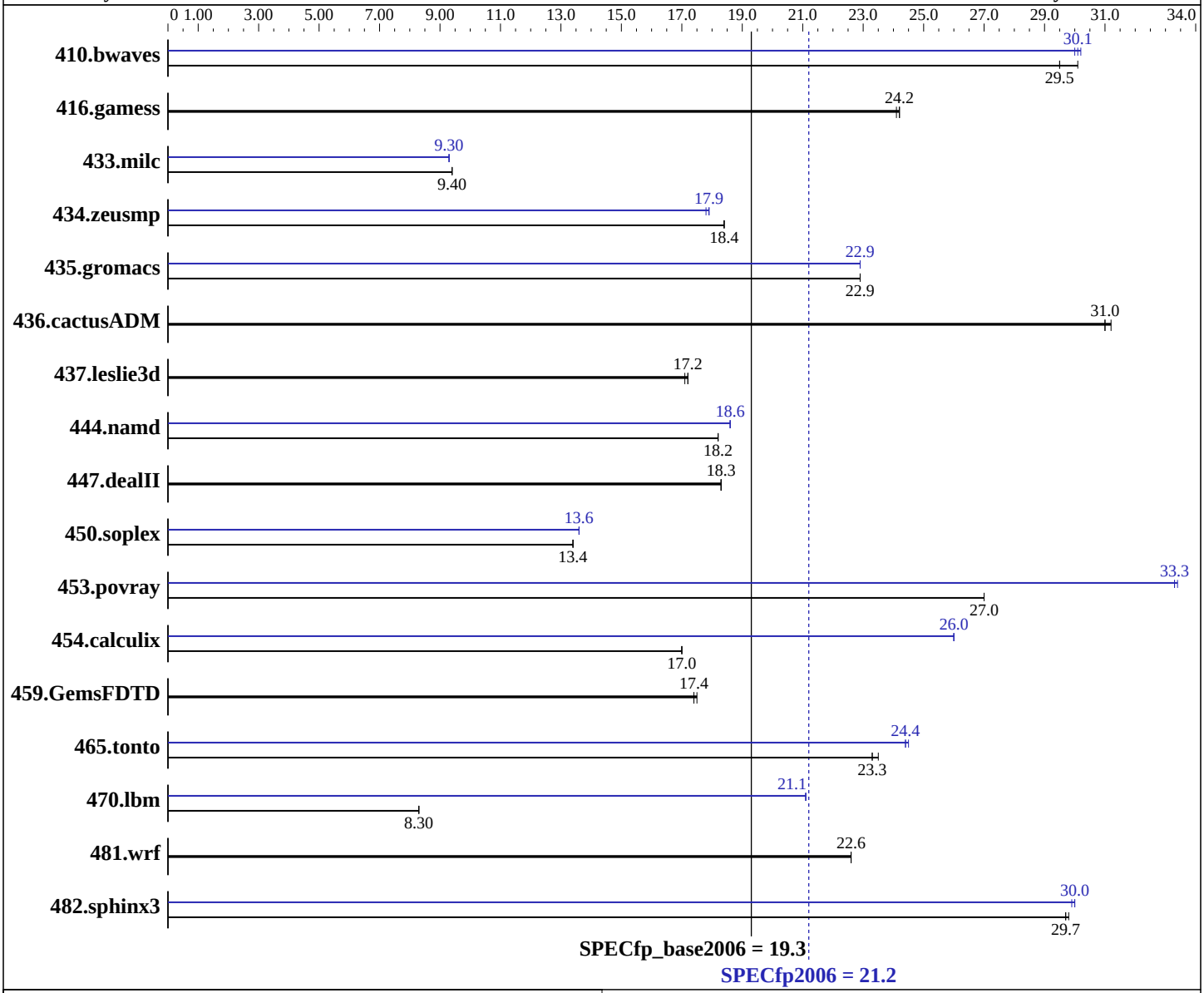
Dell Precision R5400 (Intel X5270, 3.50 GHz)

SPECfp®2006 = 21.2

SPECfp_base2006 = 19.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Oct-2008
Software Availability: Mar-2008



Hardware

CPU Name:	Intel Xeon X5270
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Business SP1 (64-bit)
Compiler:	Intel C++ Compiler for Intel 64, Version 10.1 Build 20080312 Package ID: w_cc_p_10.1.021 Intel Visual Fortran Compiler for Intel 64, Version 10.1 Build 20080312 Package ID: w_fc_p_10.1.021 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB 667 MHz CL5 FB-DIMM)
Disk Subsystem:	1 x 320 GB SATA 7200 RPM
Other Hardware:	None

System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 for x64

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	451	30.1	461	29.5	**460**	**29.5**	449	30.2	**452**	**30.1**	453	30.0
416.gamess	811	24.2	**811**	**24.2**	811	24.1	811	24.2	**811**	**24.2**	811	24.1
433.milc	975	9.40	977	9.40	**976**	**9.40**	986	9.30	985	9.30	**986**	**9.30**
434.zeusmp	494	18.4	494	18.4	**494**	**18.4**	510	17.9	**510**	**17.9**	510	17.8
435.gromacs	**312**	**22.9**	312	22.9	311	22.9	312	22.9	**312**	**22.9**	312	22.9
436.cactusADM	383	31.2	**385**	**31.0**	385	31.0	383	31.2	**385**	**31.0**	385	31.0
437.leslie3d	547	17.2	**548**	**17.2**	549	17.1	547	17.2	**548**	**17.2**	549	17.1
444.namd	441	18.2	441	18.2	**441**	**18.2**	**431**	**18.6**	431	18.6	432	18.6
447.dealII	627	18.3	627	18.3	**627**	**18.3**	627	18.3	627	18.3	**627**	**18.3**
450.soplex	620	13.4	**621**	**13.4**	621	13.4	**612**	**13.6**	612	13.6	613	13.6
453.povray	197	27.0	**197**	**27.0**	197	27.0	159	33.4	160	33.3	**160**	**33.3**
454.calculix	485	17.0	**485**	**17.0**	485	17.0	317	26.0	**317**	**26.0**	317	26.0
459.GemsFDTD	**609**	**17.4**	607	17.5	609	17.4	**609**	**17.4**	607	17.5	609	17.4
465.tonto	**423**	**23.3**	419	23.5	423	23.3	403	24.4	**403**	**24.4**	402	24.5
470.lbm	1651	8.30	**1651**	**8.30**	1650	8.30	652	21.1	**651**	**21.1**	651	21.1
481.wrf	494	22.6	**495**	**22.6**	495	22.6	494	22.6	**495**	**22.6**	495	22.6
482.sphinx3	**656**	**29.7**	655	29.8	656	29.7	649	30.0	**650**	**30.0**	651	29.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings
  Adjacent Cache Line Prefetch set to ON
```

Base Compiler Invocation

C benchmarks:

```
icl -Qstd=c99
```

C++ benchmarks:

```
icl
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -Qauto-ilp32 -Qparallel /F512000000 libguide40.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qauto-ilp32 -Qparallel -Qcxx_features /F512000000 shlw64m.lib
libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast -Qparallel /F1000000000 libguide40.lib
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast -Qauto-ilp32 -Qparallel /F1000000000 libguide40.lib
-link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qstd=c99

C++ benchmarks:
icl

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll2 -Oa /F512000000 libguide40.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll2 -Qscalar-rep- -Qprefetch /F512000000 libguide40.lib -link /FORCE:MULTIPLE

482.sphinx3: -fast -Qauto-ilp32 -Qunroll2 /F512000000 libguide40.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Oa -Qcxx_features /F512000000 shlw64m.lib libguide40.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qparallel -Qcxx_features /F512000000 shlw64m.lib libguide40.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll4 -Qansi-alias -Qcxx_features /F512000000 shlw64m.lib libguide40.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-fast -Qauto-ilp32 -Qparallel -Qprefetch /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

416.gamess: `basepeak = yes`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qauto-ilp32 -Qunroll4 -Qauto /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qauto-ilp32 -Oa -Qprefetch /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast -Qauto-ilp32 -Qunroll-aggressive /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090713.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:17:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 October 2008.