



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 30.1

CELSIUS R650, Intel Xeon X5270 processor

SPECint_base2006 = 26.4

CPU2006 license: 22

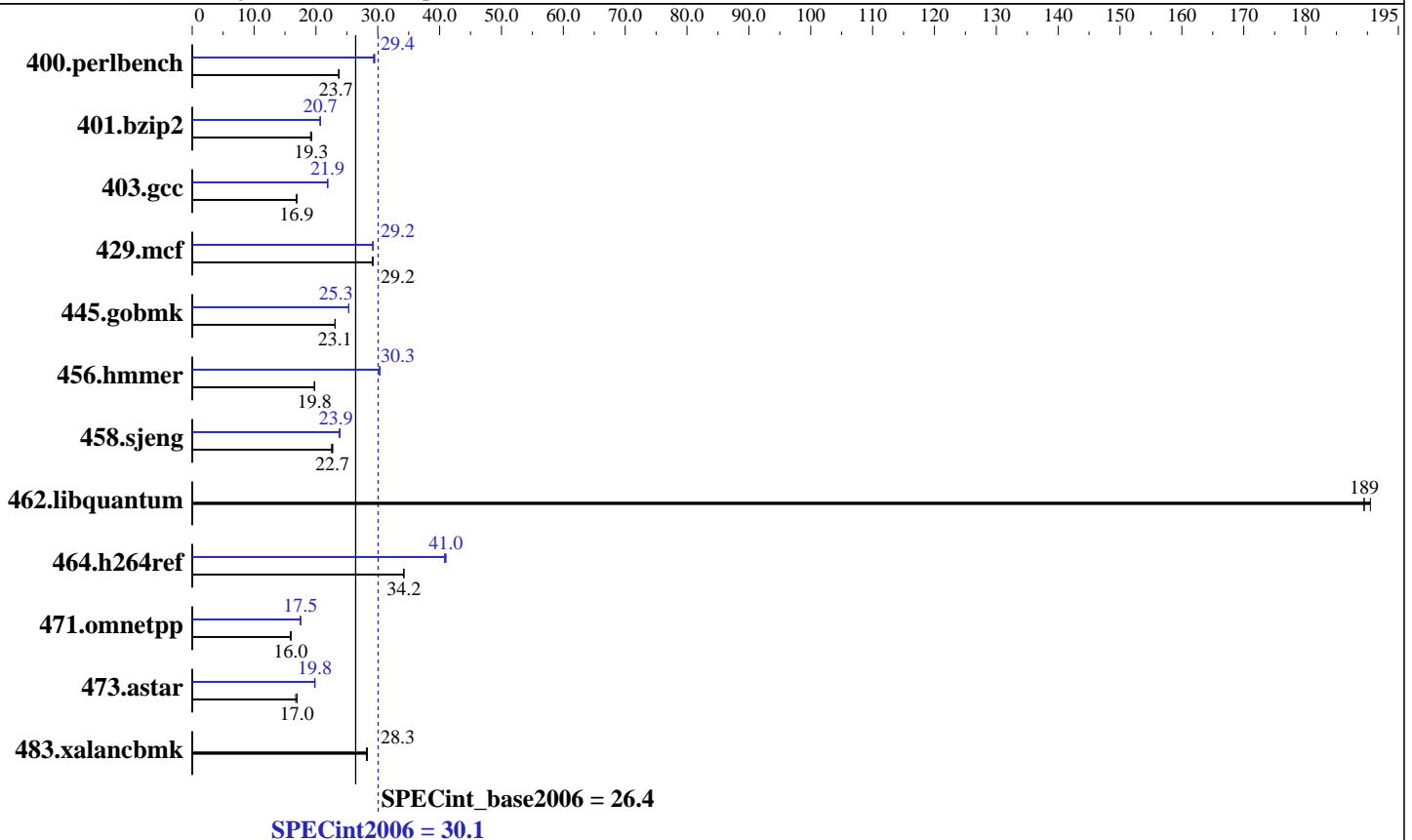
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Sep-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5270
 CPU Characteristics:
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP2
 kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080730, Package ID l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User, Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	413	23.6	411	23.8	<u>412</u>	<u>23.7</u>	<u>332</u>	<u>29.4</u>	333	29.3	331	29.5
401.bzip2	<u>501</u>	<u>19.3</u>	504	19.1	500	19.3	466	20.7	<u>466</u>	<u>20.7</u>	467	20.6
403.gcc	<u>476</u>	<u>16.9</u>	476	16.9	478	16.8	368	21.9	<u>368</u>	<u>21.9</u>	368	21.9
429.mcf	313	29.1	312	29.2	<u>312</u>	<u>29.2</u>	<u>312</u>	<u>29.2</u>	312	29.2	312	29.2
445.gobmk	454	23.1	<u>454</u>	<u>23.1</u>	454	23.1	415	25.3	<u>415</u>	<u>25.3</u>	414	25.3
456.hammer	<u>472</u>	<u>19.8</u>	472	19.8	472	19.8	308	30.3	308	30.3	<u>308</u>	<u>30.3</u>
458.sjeng	<u>534</u>	<u>22.7</u>	533	22.7	537	22.5	509	23.8	<u>507</u>	<u>23.9</u>	506	23.9
462.libquantum	109	190	<u>109</u>	<u>189</u>	109	189	109	190	<u>109</u>	<u>189</u>	109	189
464.h264ref	647	34.2	<u>647</u>	<u>34.2</u>	647	34.2	542	40.8	<u>540</u>	<u>41.0</u>	540	41.0
471.omnetpp	392	16.0	393	15.9	<u>392</u>	<u>16.0</u>	357	17.5	356	17.5	<u>357</u>	<u>17.5</u>
473.astar	<u>414</u>	<u>17.0</u>	414	17.0	420	16.7	355	19.8	354	19.8	<u>354</u>	<u>19.8</u>
483.xalancbmk	244	28.3	245	28.2	<u>244</u>	<u>28.3</u>	244	28.3	245	28.2	<u>244</u>	<u>28.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
C1 Enhanced Mode = Disable
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
SnoopFilter = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.08.html>

<http://www.spec.org/cpu2006/flags/FSC-SLES10-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.08.xml>

<http://www.spec.org/cpu2006/flags/FSC-SLES10-Platform.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:16:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 October 2008.