SPEC® CINT2006 Result
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Intel Corporation
Lenovo Thinkpad T60

SPECint®_rate2006 =   20.8
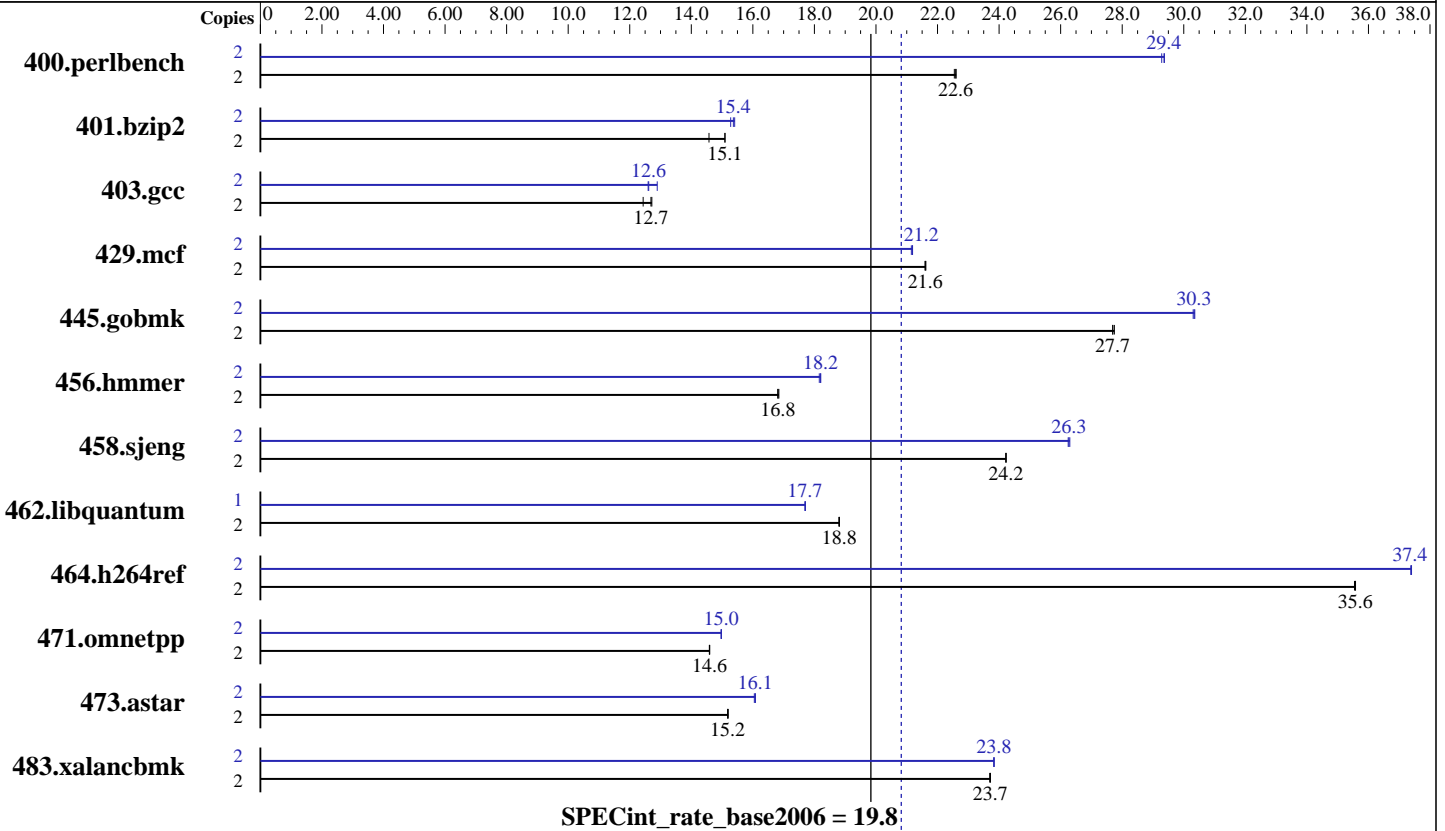
SPECint_rate_base2006 = 19.8

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2008

Hardware Availability: Sep-2006

Software Availability: Nov-2007

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400.perlbench	29.4 / 22.6
401.bzip2	15.4 / 15.1
403.gcc	12.6 / 12.7
429.mcf	21.2 / 21.6
445.gobmk	30.3 / 27.7
456.hmmer	18.2 / 16.8
458.sjeng	26.3 / 24.2
462.libquantum	17.7 / 18.8
464.h264ref	37.4 / 35.6
471.omnetpp	15.0 / 14.6
473.astar	16.1 / 15.2
483.xalancbmk	23.8 / 23.7

Hardware

CPU Name: Intel Core Duo T2700

CPU Characteristics:

CPU MHz: 2333

FPU: Integrated

CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip

CPU(s) orderable: 1 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 2 MB I+D on chip per chip

L3 Cache: None

Other Cache: None

Memory: 2 GB (2x1GB Micron PC2-5300 DDR2-667 CL5)

Disk Subsystem: Hitachi Travelstar HTS721010G9SA00 SATA 100GB 7200RPM

Other Hardware: None

Software

Operating System: Windows XP Professional SP2

Compiler: Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)

Auto Parallel: Yes

File System: NTFS

System State: Default

Base Pointers: 32-bit

Peak Pointers: 32-bit

Other Software: None

SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	867	22.5	864	22.6	**865**	**22.6**	2	**666**	**29.4**	665	29.4	667	29.3
401.bzip2	2	1324	14.6	**1279**	**15.1**	1278	15.1	2	**1256**	**15.4**	1263	15.3	1253	15.4
403.gcc	2	**1269**	**12.7**	1266	12.7	1295	12.4	2	1278	12.6	**1276**	**12.6**	1249	12.9
429.mcf	2	**844**	**21.6**	845	21.6	844	21.6	2	**861**	**21.2**	862	21.1	861	21.2
445.gobmk	2	**758**	**27.7**	758	27.7	756	27.7	2	691	30.4	**692**	**30.3**	692	30.3
456.hmmer	2	1110	16.8	**1109**	**16.8**	1108	16.8	2	**1025**	**18.2**	1025	18.2	1028	18.2
458.sjeng	2	999	24.2	**999**	**24.2**	999	24.2	2	920	26.3	**921**	**26.3**	922	26.2
462.libquantum	2	2205	18.8	2202	18.8	**2204**	**18.8**	1	1171	17.7	1170	17.7	**1171**	**17.7**
464.h264ref	2	1244	35.6	**1244**	**35.6**	1245	35.5	2	**1184**	**37.4**	1184	37.4	1183	37.4
471.omnetpp	2	**857**	**14.6**	856	14.6	857	14.6	2	835	15.0	835	15.0	**835**	**15.0**
473.astar	2	**925**	**15.2**	924	15.2	925	15.2	2	873	16.1	875	16.0	**873**	**16.1**
483.xalancbmk	2	582	23.7	582	23.7	**582**	**23.7**	2	**579**	**23.8**	579	23.8	579	23.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
-QxP -Qipo -O3 -Qprec-div- -Qvec-guard-write /F512000000
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
    -QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc8 -Qc99
```

C++ benchmarks:
```
    icl -Qvc8
```

Peak Portability Flags

```
      403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
  483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qansi-alias -Qprefetch /F512000000
           shlw32m.lib              -link /FORCE:MULTIPLE

     401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qprefetch /F512000000

      403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- /F512000000

      429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qprefetch /F512000000 shlw32m.lib
                   -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

```
 445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
            -Qprec-div- -Qansi-alias /F512000000

 456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
            -Qprec-div- -Qunroll2 -Qansi-alias
            -Qopt-multi-version-aggressive /F512000000

 458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
            -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Ob0 -Qprefetch
            -Qopt-streaming-stores:always -Qparallel
            -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
            -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
            -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block
            -Qcxx_features /F512000000 shlw32m.lib
                     -link /FORCE:MULTIPLE

 473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
            -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine
            -Qcxx_features /F512000000 shlw32m.lib
                     -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
            -Qprec-div- -Qansi-alias -Qcxx_features /F512000000
            shlw32m.lib            -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
   403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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