



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 84.0

ACTINA SOLAR 442 S3 (Intel Xeon E7420, 2.13 GHz)

SPECint_rate_base2006 = 73.1

CPU2006 license: 9008

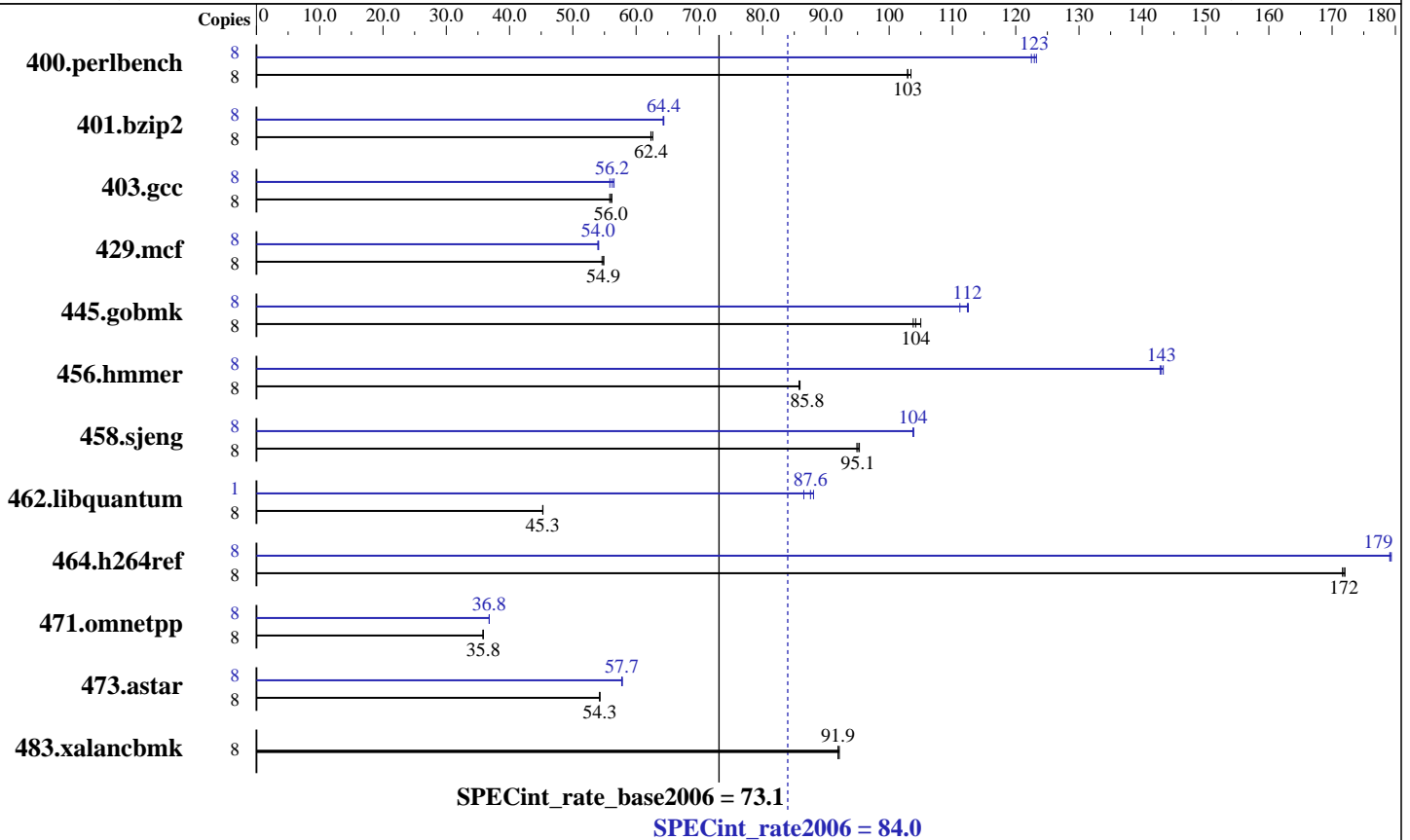
Test date: Aug-2008

Test sponsor: ACTION S.A.

Hardware Availability: Sep-2008

Tested by: ACTION S.A.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7420
 CPU Characteristics: Quad-Core, 1066 MHz bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (8x 2 GB PC2-5300 ECC FBDIMM)
 Disk Subsystem: 1x 250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	756	103	760	103	759	103	8	638	122	634	123	636	123
401.bzip2	8	1238	62.3	1238	62.4	1232	62.6	8	1200	64.4	1199	64.4	1201	64.3
403.gcc	8	1146	56.2	1150	56.0	1153	55.8	8	1145	56.2	1139	56.5	1153	55.9
429.mcf	8	1330	54.9	1336	54.6	1329	54.9	8	1349	54.1	1351	54.0	1352	54.0
445.gobmk	8	809	104	799	105	805	104	8	746	113	755	111	747	112
456.hmmmer	8	870	85.8	870	85.8	869	85.9	8	523	143	522	143	521	143
458.sjeng	8	1018	95.1	1016	95.3	1020	94.9	8	933	104	932	104	933	104
462.libquantum	8	3664	45.2	3660	45.3	3663	45.3	1	235	88.0	237	87.6	240	86.5
464.h264ref	8	1029	172	1030	172	1031	172	8	987	179	988	179	988	179
471.omnetpp	8	1396	35.8	1395	35.8	1395	35.8	8	1359	36.8	1359	36.8	1360	36.8
473.astar	8	1035	54.3	1036	54.2	1035	54.3	8	973	57.7	971	57.8	973	57.7
483.xalancbmk	8	599	92.1	601	91.9	600	91.9	8	599	92.1	601	91.9	600	91.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode.
Taskset command was used to bind processes to CPUs.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
```

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.xml>



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For other inquiries, please contact webmaster@spec.org.

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