



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX100 S5, Intel Core 2 Duo E7200, 2.53 GHz

SPECint®_rate2006 = 33.7

SPECint_rate_base2006 = 29.5

CPU2006 license: 22

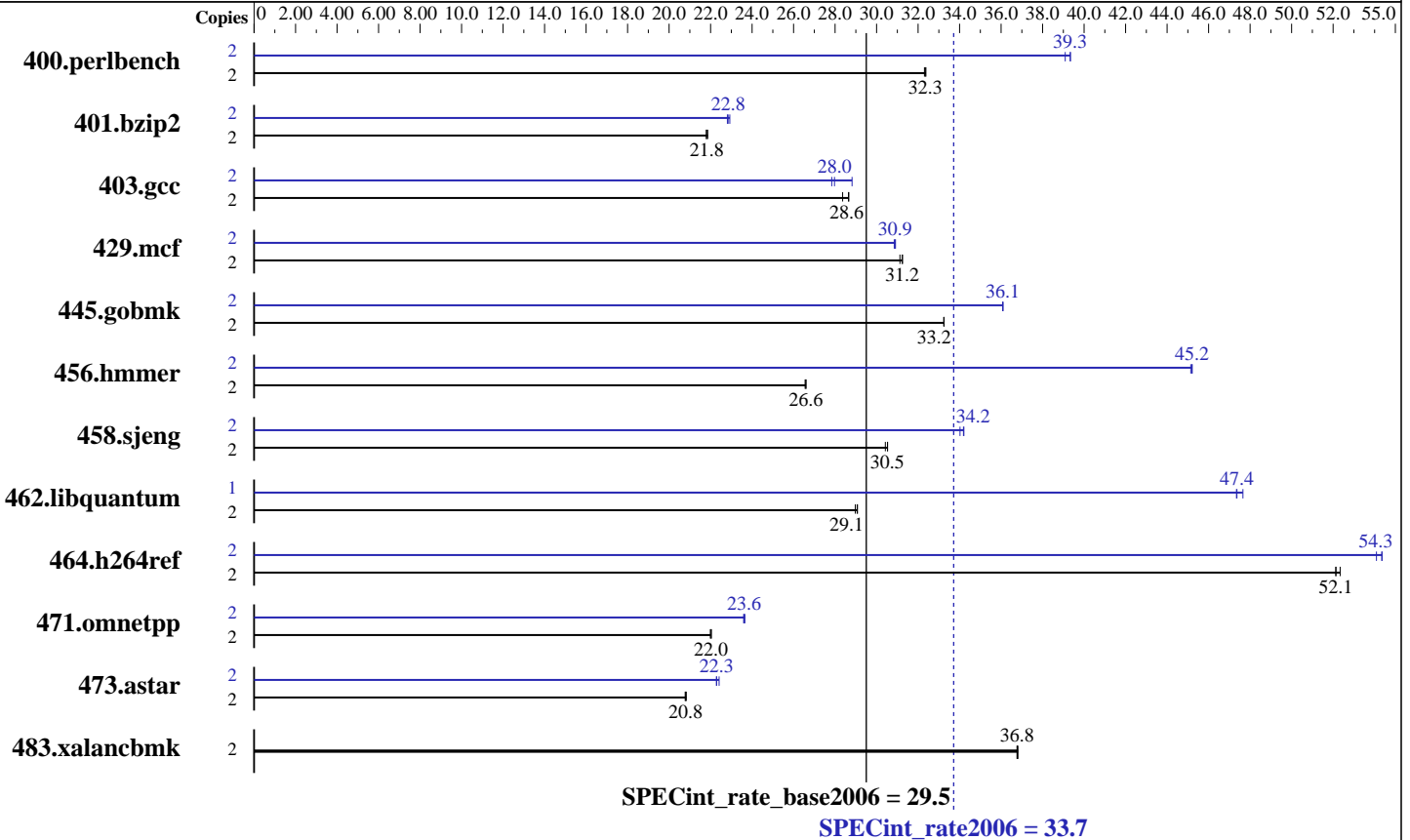
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Aug-2008

Hardware Availability: Aug-2008

Software Availability: May-2008



Hardware

CPU Name: Intel Core 2 Duo E7200
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 3 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL 6-6-6, ECC)
 Disk Subsystem: 1x SATA, 160 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap Library, Version 8.1
 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	605	32.3	604	32.3	604	32.4	2	497	39.3	500	39.1	497	39.3
401.bzip2	2	885	21.8	883	21.9	886	21.8	2	842	22.9	845	22.8	846	22.8
403.gcc	2	562	28.6	568	28.4	562	28.7	2	559	28.8	578	27.8	576	28.0
429.mcf	2	584	31.2	584	31.2	586	31.1	2	591	30.9	591	30.8	590	30.9
445.gobmk	2	631	33.2	631	33.2	631	33.2	2	581	36.1	581	36.1	582	36.1
456.hmmmer	2	702	26.6	702	26.6	703	26.6	2	413	45.1	413	45.2	413	45.2
458.sjeng	2	793	30.5	796	30.4	793	30.5	2	711	34.0	708	34.2	707	34.2
462.libquantum	2	1430	29.0	1426	29.1	1425	29.1	1	435	47.7	437	47.4	438	47.3
464.h264ref	2	846	52.3	849	52.1	849	52.1	2	814	54.3	814	54.4	818	54.1
471.omnetpp	2	568	22.0	567	22.0	568	22.0	2	528	23.7	529	23.6	530	23.6
473.astar	2	676	20.8	674	20.8	675	20.8	2	630	22.3	626	22.4	630	22.3
483.xalancbmk	2	375	36.8	375	36.8	375	36.8	2	375	36.8	375	36.8	375	36.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

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Base Compiler Invocation (Continued)

C++ benchmarks:

```
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-fast -inline-alloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

```
401.bzip2: icc
```

```
456.hmmer: icc
```

C++ benchmarks:

```
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.xml>

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For other inquiries, please contact webmaster@spec.org.

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