



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 43.4

PRIMERGY RX200 S4, Intel Xeon X5260, 3.33 GHz

### SPECint\_rate\_base2006 = 36.7

CPU2006 license: 22

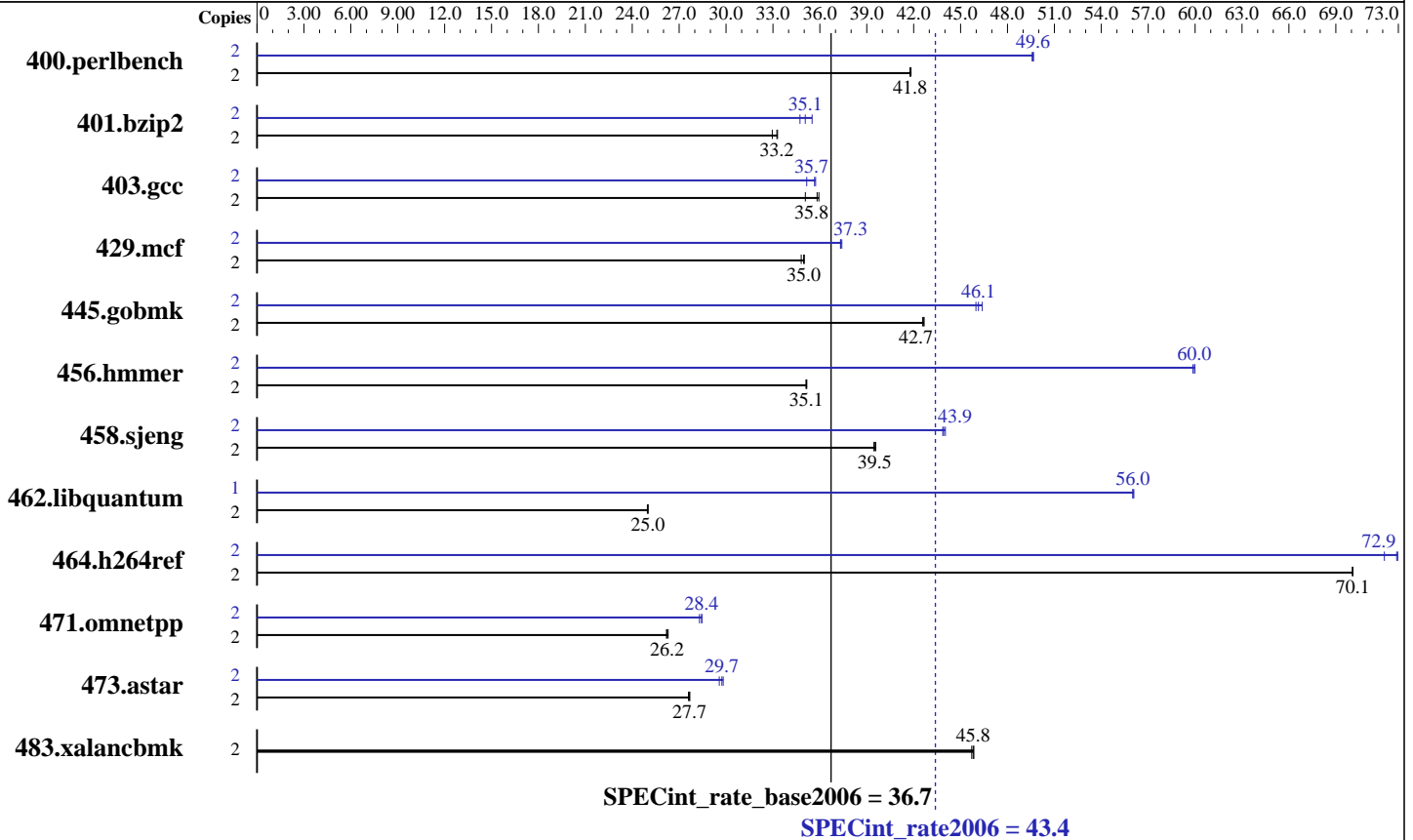
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jul-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)  
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86\_64



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>468</b>	<b>41.8</b>	467	41.8	468	41.8	2	<b>394</b>	<b>49.6</b>	394	49.6	393	49.7
401.bzip2	2	586	33.0	<b>580</b>	<b>33.2</b>	580	33.3	2	544	35.5	<b>550</b>	<b>35.1</b>	556	34.7
403.gcc	2	448	35.9	459	35.1	<b>449</b>	<b>35.8</b>	2	451	35.7	458	35.2	<b>451</b>	<b>35.7</b>
429.mcf	2	521	35.0	524	34.8	<b>522</b>	<b>35.0</b>	2	<b>489</b>	<b>37.3</b>	488	37.4	489	37.3
445.gobmk	2	493	42.6	492	42.7	<b>492</b>	<b>42.7</b>	2	<b>455</b>	<b>46.1</b>	456	46.0	452	46.4
456.hmmmer	2	531	35.1	<b>531</b>	<b>35.1</b>	531	35.2	2	311	60.0	<b>311</b>	<b>60.0</b>	312	59.9
458.sjeng	2	612	39.6	<b>613</b>	<b>39.5</b>	613	39.5	2	<b>551</b>	<b>43.9</b>	552	43.9	550	44.0
462.libquantum	2	1660	25.0	1657	25.0	<b>1658</b>	<b>25.0</b>	1	<b>370</b>	<b>56.0</b>	369	56.1	370	56.0
464.h264ref	2	632	70.1	<b>632</b>	<b>70.1</b>	632	70.1	2	607	73.0	614	72.1	<b>607</b>	<b>72.9</b>
471.omnetpp	2	<b>477</b>	<b>26.2</b>	477	26.2	475	26.3	2	<b>440</b>	<b>28.4</b>	442	28.3	439	28.5
473.astar	2	507	27.7	509	27.6	<b>507</b>	<b>27.7</b>	2	475	29.6	<b>472</b>	<b>29.7</b>	471	29.8
483.xalancbmk	2	301	45.8	302	45.7	<b>301</b>	<b>45.8</b>	2	301	45.8	302	45.7	<b>301</b>	<b>45.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores (default)

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

## General Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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