



# SPEC® CINT2006 Result

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**Sun Microsystems**

**Sun SPARC Enterprise M5000**

**SPECint\_rate2006 = 264**

**SPECint\_rate\_base2006 = 232**

**CPU2006 license:** 6

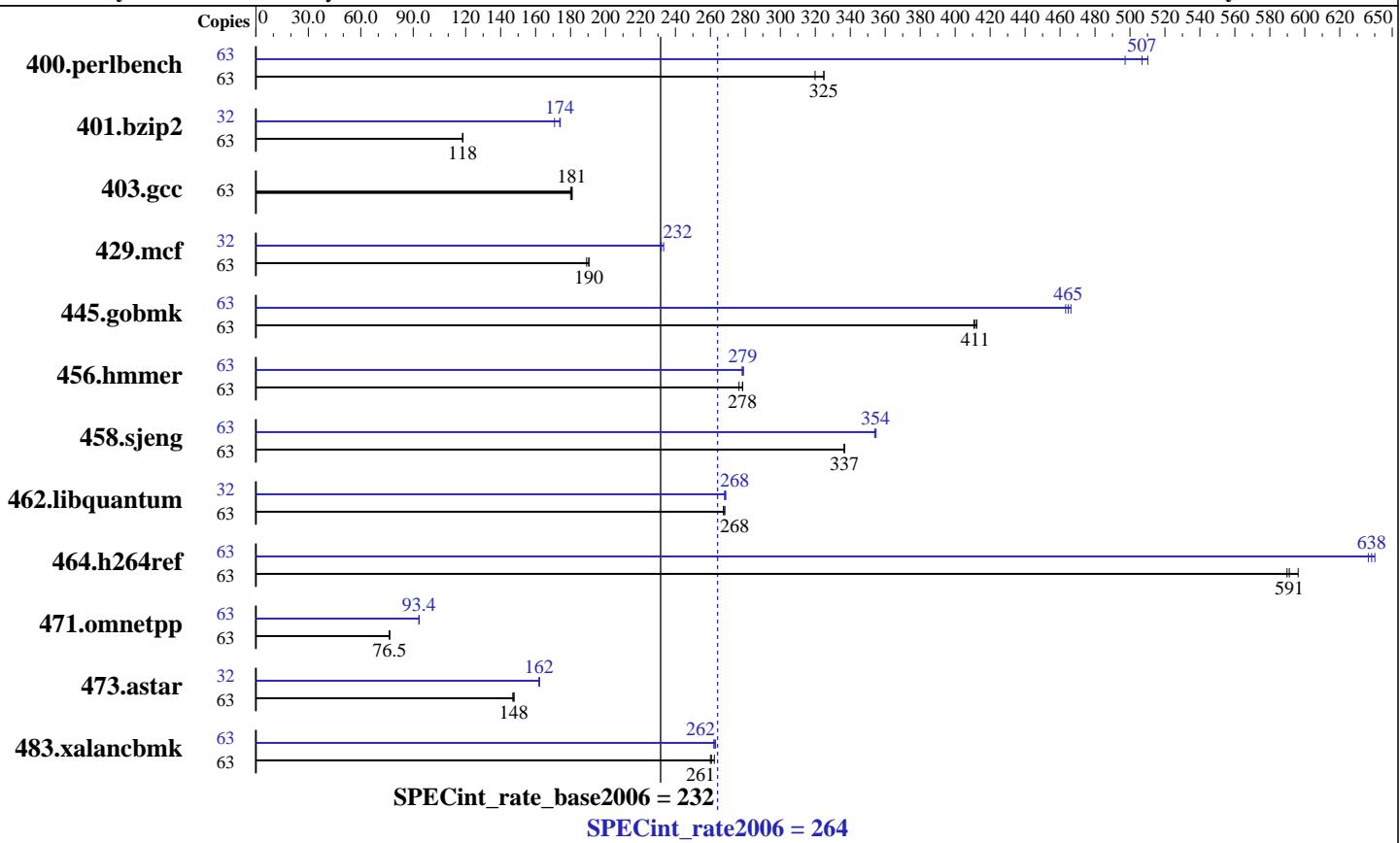
**Test sponsor:** Sun Microsystems

**Tested by:** Sun Microsystems

**Test date:** Jun-2008

**Hardware Availability:** Jul-2008

**Software Availability:** Jul-2008



## Hardware

CPU Name: SPARC64 VII  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 8 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 to 4 CMU; each CMU contains 2 CPU chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 5 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 128 GB (64 x 2 GB)  
 Disk Subsystem: 158 GB RAID 0 Solaris Volume  
 3 x Seagate 73 GB 10000 RPM  
 Stripe interlace 512 Kbytes  
 Other Hardware: None

## Software

Operating System: Solaris 10 5/08 with patch 137111-03  
 Compiler: Sun Studio 12 with patches 124867-06, 124861-07, 124863-05 (see patch information below)  
 Auto Parallel: No  
 File System: ufs  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: None



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	63	<b>1895</b>	<b>325</b>	1894	325	1924	320	63	1238	497	<b>1214</b>	<b>507</b>	1206	510
401.bzip2	63	<b>5141</b>	<b>118</b>	5131	118	5141	118	32	1808	171	1774	174	<b>1777</b>	<b>174</b>
403.gcc	63	2817	180	2803	181	<b>2807</b>	<b>181</b>	63	2817	180	2803	181	<b>2807</b>	<b>181</b>
429.mcf	63	3016	190	3037	189	<b>3017</b>	<b>190</b>	32	<b>1258</b>	<b>232</b>	1251	233	1259	232
445.gobmk	63	1602	412	<b>1606</b>	<b>411</b>	1609	411	63	1427	463	<b>1422</b>	<b>465</b>	1418	466
456.hmmer	63	<b>2112</b>	<b>278</b>	2127	276	2111	278	63	2114	278	2108	279	<b>2110</b>	<b>279</b>
458.sjeng	63	<b>2264</b>	<b>337</b>	2265	336	2264	337	63	2149	355	2154	354	<b>2151</b>	<b>354</b>
462.libquantum	63	<b>4877</b>	<b>268</b>	4880	267	4866	268	32	<b>2470</b>	<b>268</b>	2467	269	2473	268
464.h264ref	63	<b>2358</b>	<b>591</b>	2338	596	2364	590	63	<b>2184</b>	<b>638</b>	2191	636	2178	640
471.omnetpp	63	<b>5150</b>	<b>76.5</b>	5156	76.4	5143	76.6	63	4224	93.2	<b>4216</b>	<b>93.4</b>	4214	93.4
473.astar	63	3009	147	2995	148	<b>2997</b>	<b>148</b>	32	<b>1387</b>	<b>162</b>	1387	162	1384	162
483.xalancbmk	63	<b>1668</b>	<b>261</b>	1672	260	1658	262	63	1653	263	1659	262	<b>1658</b>	<b>262</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

Sun Studio compiler patches are available at  
[http://developers.sun.com/sunstudio/downloads/patches/ss12\\_patches.jsp](http://developers.sun.com/sunstudio/downloads/patches/ss12_patches.jsp)

## Submit Notes

Processes were assigned to specific processors using 'pbind' commands. The config file option 'submit' was used, along with a list of processors in the 'BIND' variable, to generate the pbind commands. (For details, please see the config file.)

## Operating System Notes

Environment Variable Settings:

ulimit -s 131072 was used to limit the space consumed  
 by the stack (making more space available for the heap)

System Tunables (/etc/system parameters):

tune\_t\_fsflushr=10  
 Controls how many seconds elapse between runs of the  
 page flush daemon, fsflush.

autoup=600  
 Causes pages older than the listed number of seconds to  
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## Operating System Notes (Continued)

be written by fsflush.  
bufhwm=3000  
Memory byte limit for caching I/O buffers  
segmap\_percent=1  
Set maximum percent memory for file system cache  
lpg\_alloc\_prefer=1  
Set lgroup page allocation to strongly prefer local pages

Other System Settings:

The webconsole service was turned off using  
svcadm disable webconsole

## Platform Notes

Memory is 8-way interleaved by filling all slots with  
the same capacity DIMMs.

This result is measured on a Sun SPARC Enterprise M5000 Server.  
Note that the Sun SPARC Enterprise M5000 and Fujitsu SPARC Enterprise  
M5000 are electrically equivalent.

## Base Compiler Invocation

C benchmarks:  
cc

C++ benchmarks:  
CC

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_SOLARIS\_SPARC  
403.gcc: -DSPEC\_CPU\_SOLARIS  
462.libquantum: -DSPEC\_CPU\_SOLARIS  
483.xalancbmk: -DSPEC\_CPU\_SOLARIS

## Base Optimization Flags

C benchmarks:  
-fast -fma=fused -xipo=2 -xpagesize=4M -xprefetch\_level=1  
-xalias\_level=std -ll2amm

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xdepend -library=stlport4 -fast -fma=fused -xipo=2 -xpagesize=4M  
-xprefetch_level=2 -xalias_level=compatible -lfast
```

## Base Other Flags

C benchmarks:

```
-xjobs=16 -V -#
```

C++ benchmarks:

```
-xjobs=16 -verbose=diags,version
```

## Peak Compiler Invocation

C benchmarks:

```
cc
```

C++ benchmarks:

```
CC
```

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_SOLARIS\_SPARC

403.gcc: -DSPEC\_CPU\_SOLARIS

462.libquantum: -DSPEC\_CPU\_SOLARIS

483.xalancbmk: -DSPEC\_CPU\_SOLARIS

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xprofile=collect:./feedback(pass 1)

-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M

-xalias\_level=std -xipo=2 -xrestrict -fma=fused -lfast

401.bzip2: -xprofile=collect:./feedback(pass 1)

-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M

-xalias\_level=strong -fma=fused

403.gcc: basepeak = yes

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## Peak Optimization Flags (Continued)

429.mcf: -fast -xpagesize=4M -xipo=2 -xprefetch=no -xrestrict  
-xalias\_level=std -lfast

445.gobmk: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M  
-xalias\_level=std -xrestrict -fma=fused -ll2amm

456.hmmr: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M  
-xipo=2 -fma=fused

458.sjeng: Same as 456.hmmr

462.libquantum: -fast -xpagesize=4M -xipo=2 -xprefetch=no -fma=fused  
-lbsdmalloc

464.h264ref: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M  
-xipo=2 -xalias\_level=std -xprefetch=no -ll2amm

C++ benchmarks:

471.omnetpp: -xdepend -library=stlport4  
-xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M  
-xalias\_level=compatible -xipo=2 -xprefetch\_level=2  
-Qoption cg -Qlp-av=0 -fma=fused -lfast

473.astar: -xdepend -library=stlport4 -fast -xpagesize=4M  
-xalias\_level=compatible -xipo=2 -xprefetch\_level=2  
-fma=fused -lfast

483.xalancbmk: -xdepend -library=stlport4  
-xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xpagesize=4M  
-xalias\_level=compatible -xipo=2 -xprefetch=no -fma=fused  
-lfast

## Peak Other Flags

C benchmarks:

-xjobs=16 -V -#

C++ benchmarks:

-xjobs=16 -verbose=diags,version



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Sun-Solaris-Studio12-and-gccfss4.2.20090713.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Sun-Solaris-Studio12-and-gccfss4.2.20090713.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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