



# SPEC® CFP2006 Result

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Dell Inc.

SPECfp®\_rate2006 = 26.3

Dell Precision M6300 (Intel T9500, 2.60 GHz)

SPECfp\_rate\_base2006 = 25.0

CPU2006 license: 55

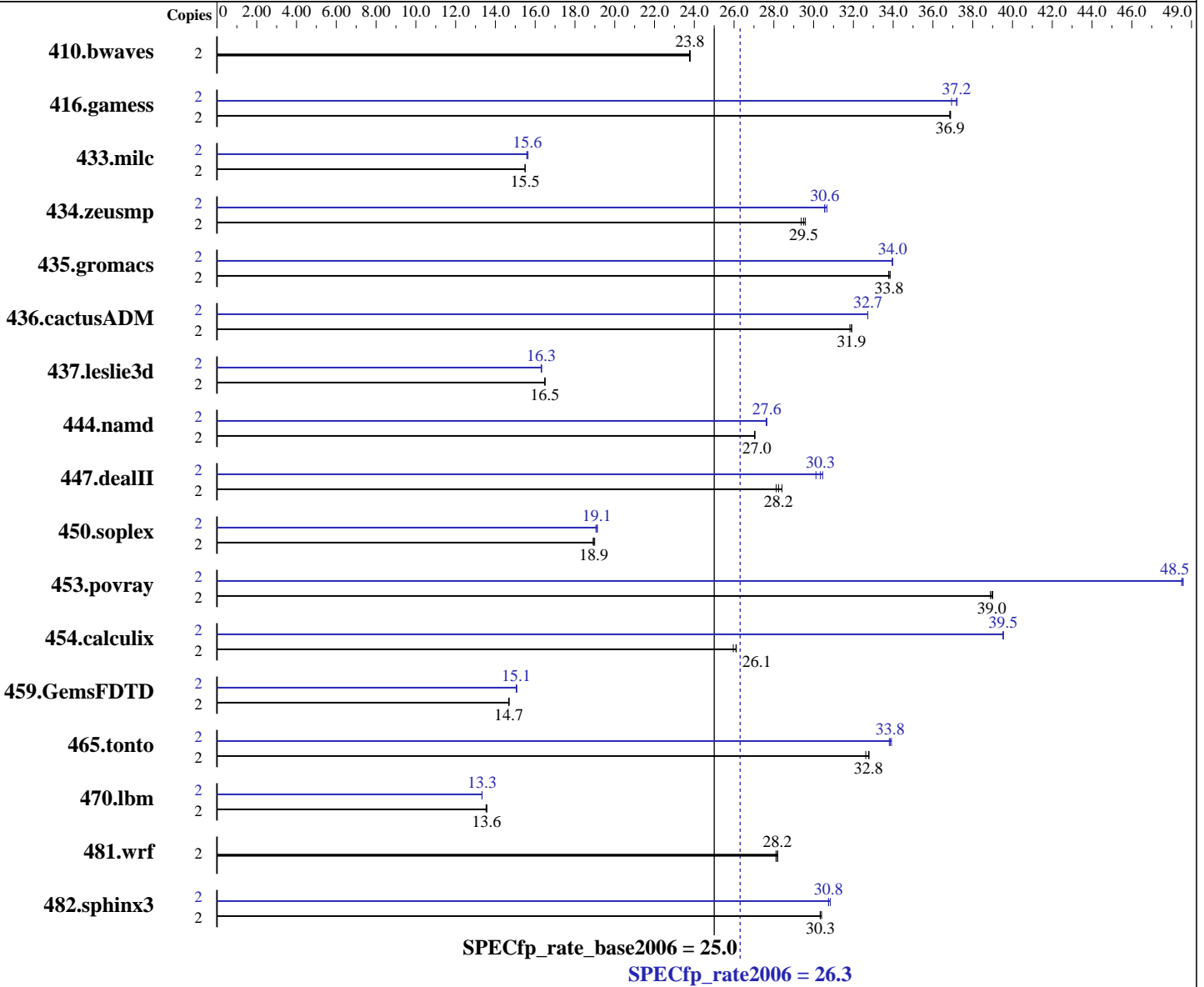
Test date: Apr-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Mar-2008



### Hardware

CPU Name: Intel Core 2 Duo T9500  
 CPU Characteristics: 800 MHz Bus Speed  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip

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### Software

Operating System: Windows Vista Ultimate (64-bit)  
 Compiler: Intel C++ Compiler for Intel 64, Version 10.1  
 Build 20080312 Package ID: w\_cc\_p\_10.1.021  
 Intel Visual Fortran Compiler for Intel 64,  
 Version 10.0  
 Build 20080312 Package ID: w\_fc\_p\_10.1.021  
 Microsoft Visual Studio 2005 SP1

Auto Parallel: No  
 File System: NTFS

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L3 Cache: None  
Other Cache: None  
Memory: 4 GB (2x2 GB 667 MHz CL5 DDR2)  
Disk Subsystem: 1 x 200GB SATA 7200 RPM  
Other Hardware: None

System State: Default  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: MicroQuill SmartHeap Library 8.1 for x64

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	2	1144	23.8	<u>1143</u>	<u>23.8</u>	1142	23.8	2	1144	23.8	<u>1143</u>	<u>23.8</u>	1142	23.8		
416.gamess	2	<u>1062</u>	<u>36.9</u>	1063	36.8	1062	36.9	2	1060	36.9	<u>1053</u>	<u>37.2</u>	1052	37.2		
433.milc	2	<u>1185</u>	<u>15.5</u>	1185	15.5	1184	15.5	2	1178	15.6	1174	15.6	<u>1177</u>	<u>15.6</u>		
434.zeusmp	2	615	29.6	<u>617</u>	<u>29.5</u>	619	29.4	2	<u>595</u>	<u>30.6</u>	593	30.7	596	30.5		
435.gromacs	2	<u>423</u>	<u>33.8</u>	423	33.8	422	33.8	2	<u>420</u>	<u>34.0</u>	421	34.0	420	34.0		
436.cactusADM	2	751	31.8	749	31.9	<u>749</u>	<u>31.9</u>	2	730	32.7	<u>730</u>	<u>32.7</u>	730	32.7		
437.leslie3d	2	<u>1140</u>	<u>16.5</u>	1140	16.5	1140	16.5	2	<u>1152</u>	<u>16.3</u>	1152	16.3	1153	16.3		
444.namd	2	<u>593</u>	<u>27.0</u>	593	27.1	593	27.0	2	581	27.6	580	27.6	<u>580</u>	<u>27.6</u>		
447.dealII	2	805	28.4	<u>810</u>	<u>28.2</u>	814	28.1	2	751	30.5	<u>754</u>	<u>30.3</u>	760	30.1		
450.soplex	2	<u>880</u>	<u>18.9</u>	882	18.9	878	19.0	2	<u>873</u>	<u>19.1</u>	875	19.1	872	19.1		
453.povray	2	<u>273</u>	<u>39.0</u>	273	39.0	274	38.9	2	219	48.5	219	48.6	<u>219</u>	<u>48.5</u>		
454.calculix	2	636	26.0	<u>632</u>	<u>26.1</u>	632	26.1	2	417	39.5	<u>417</u>	<u>39.5</u>	418	39.5		
459.GemsFDTD	2	<u>1447</u>	<u>14.7</u>	1447	14.7	1444	14.7	2	1410	15.0	<u>1410</u>	<u>15.1</u>	1407	15.1		
465.tonto	2	600	32.8	<u>601</u>	<u>32.8</u>	603	32.6	2	<u>581</u>	<u>33.8</u>	580	33.9	582	33.8		
470.lbm	2	2031	13.5	<u>2027</u>	<u>13.6</u>	2025	13.6	2	2061	13.3	<u>2062</u>	<u>13.3</u>	2062	13.3		
481.wrf	2	<u>793</u>	<u>28.2</u>	795	28.1	792	28.2	2	<u>793</u>	<u>28.2</u>	795	28.1	792	28.2		
482.sphinx3	2	1285	30.3	<u>1285</u>	<u>30.3</u>	1282	30.4	2	1268	30.7	<u>1268</u>	<u>30.8</u>	1264	30.8		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icl -Qstd=c99

C++ benchmarks:  
icl

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icl -Qstd=c99 ifort



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

```

## Base Optimization Flags

```

C benchmarks:
  -fast -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:
  -fast -Qauto-ilp32 -Qcxx_features /F1000000000 shlw64m.lib
  -link /FORCE:MULTIPLE

Fortran benchmarks:
  -fast -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
  -fast -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

```

## Peak Compiler Invocation

```

C benchmarks:
  icl -Qstd=c99

C++ benchmarks:
  icl

Fortran benchmarks:
  ifort

Benchmarks using both Fortran and C:
  icl -Qstd=c99 ifort

```



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll2 -Oa /F1000000000 -link /FORCE:MULTIPLE

470.lbm: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll2 -Qscalar-rep- -Qprefetch /F1000000000  
-link /FORCE:MULTIPLE

482.sphinx3: -fast -Qauto-ilp32 -Qunroll2 /F1000000000  
-link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Oa -Qcxx\_features /F1000000000 shlw64m.lib  
-link /FORCE:MULTIPLE

447.dealII: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll2 -Qprefetch -Qcxx\_features /F1000000000  
shlw64m.lib -link /FORCE:MULTIPLE

450.soplex: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qcxx\_features /F1000000000 shlw64m.lib  
-link /FORCE:MULTIPLE

453.povray: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll4 -Qansi-alias -Qcxx\_features /F1000000000  
shlw64m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000  
-link /FORCE:MULTIPLE

434.zeusmp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qprec-div-  
-Qunroll10 -Qscalar-rep- /F1000000000  
-link /FORCE:MULTIPLE

437.leslie3d: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qprefetch /F1000000000 -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

459.GemsFDTD: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll2 -Ob0 -Qprefetch /F1000000000  
-link /FORCE:MULTIPLE

465.tonto: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll4 -Qauto /F1000000000  
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Oa -Qprefetch /F1000000000  
-link /FORCE:MULTIPLE

436.cactusADM: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto-ilp32  
-Qunroll2 -Qprefetch /F1000000000  
-link /FORCE:MULTIPLE

454.calculix: -fast -Qauto-ilp32 -Qunroll-aggressive /F1000000000  
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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