



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant ML310 G5
(2.66 GHz, Intel Xeon X3350)

SPECint®2006 = 23.9

SPECint_base2006 = 20.0

CPU2006 license: 3

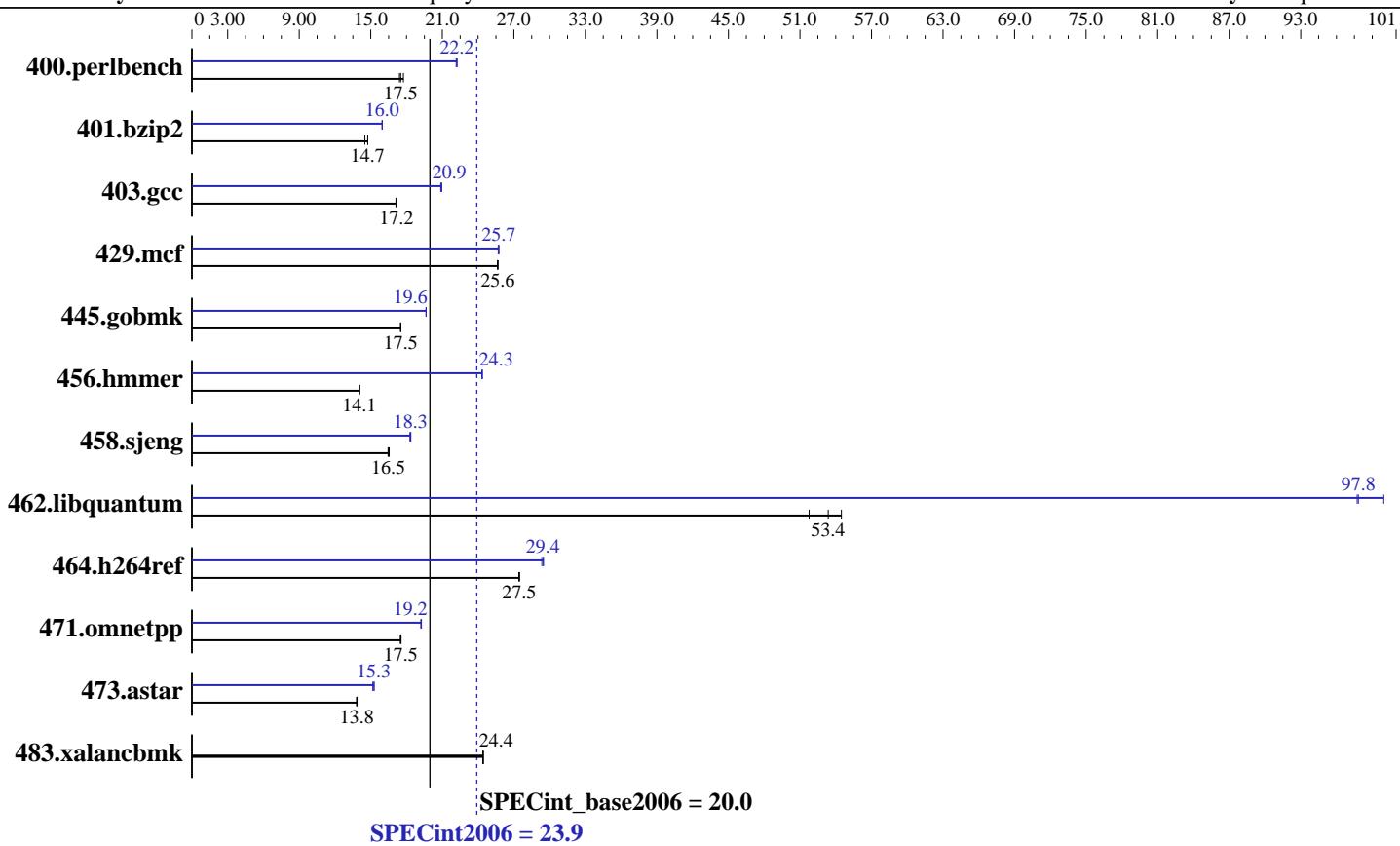
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2008

Hardware Availability: Mar-2008

Software Availability: Sep-2007



Hardware

CPU Name: Intel Xeon X3350
CPU Characteristics: 2.66 GHZ, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-6400E CL5)
Disk Subsystem: 1x250 GB 7.2 K SATA
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux
Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1
binutils-2.17.50



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	551	17.7	561	17.4	558	17.5	441	22.2	439	22.2	439	22.2
401.bzip2	665	14.5	654	14.7	654	14.7	605	15.9	604	16.0	604	16.0
403.gcc	470	17.1	469	17.2	469	17.2	384	21.0	386	20.9	385	20.9
429.mcf	356	25.6	356	25.6	355	25.7	354	25.7	355	25.7	354	25.7
445.gobmk	599	17.5	599	17.5	599	17.5	534	19.6	534	19.6	534	19.6
456.hmmer	663	14.1	663	14.1	664	14.0	383	24.3	384	24.3	383	24.4
458.sjeng	735	16.5	731	16.5	734	16.5	659	18.4	660	18.3	662	18.3
462.libquantum	400	51.8	388	53.4	380	54.5	212	97.8	212	97.7	207	100
464.h264ref	806	27.5	806	27.5	807	27.4	754	29.3	750	29.5	752	29.4
471.omnetpp	358	17.5	356	17.5	357	17.5	325	19.3	326	19.2	325	19.2
473.astar	508	13.8	507	13.8	507	13.9	463	15.2	459	15.3	460	15.3
483.xalancbmk	283	24.4	283	24.4	282	24.4	283	24.4	283	24.4	282	24.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -vec-guard-write -opt-malloc-options=3 -parallel  
-par-runtime-control
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmr: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmr: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.xml>

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