SPEC® CINT2006 Result

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Acer Incorporated

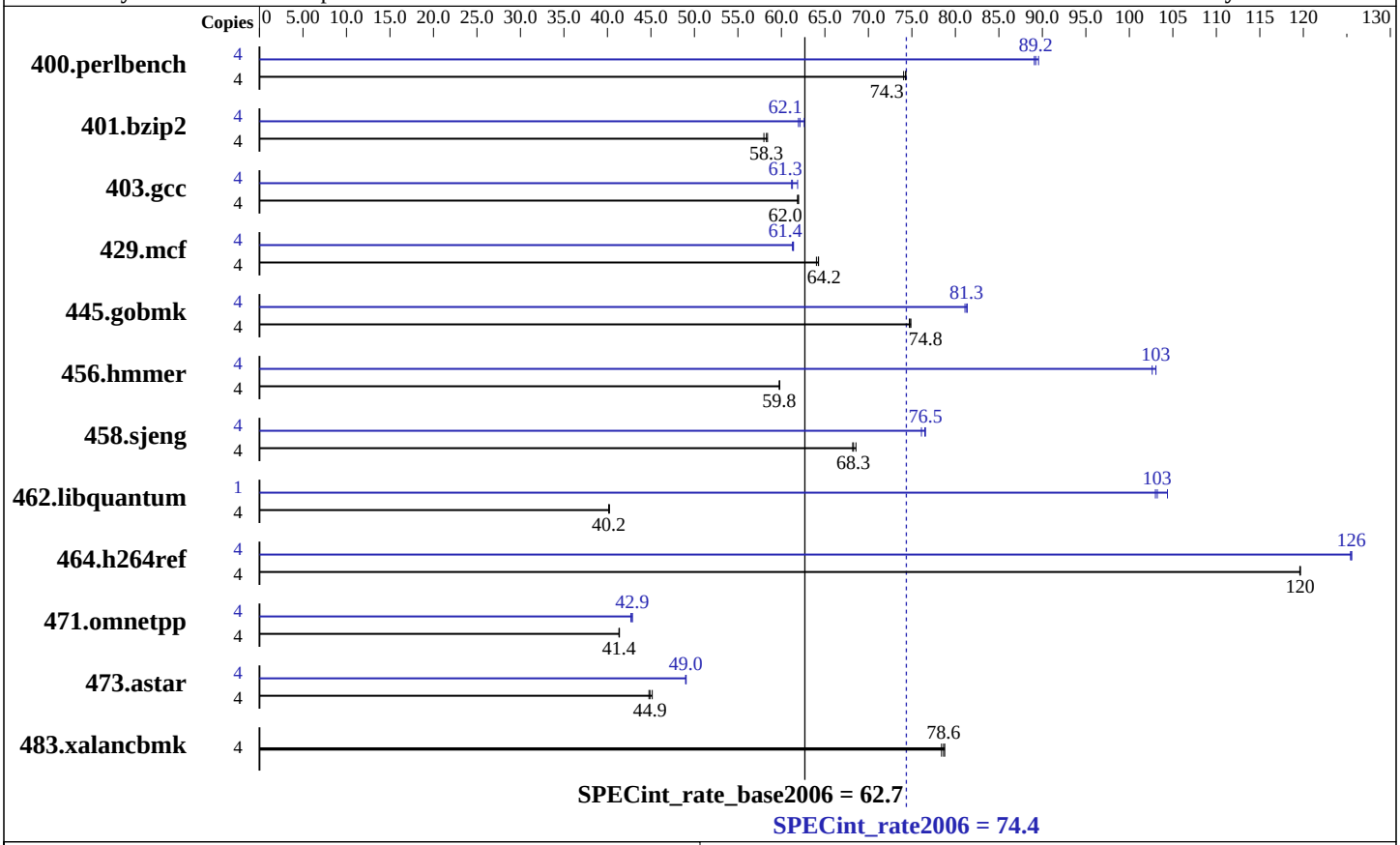
Acer Altos G330 Mk2 (Intel Xeon X3360)

SPECint®_rate2006 = 74.4

SPECint_rate_base2006 = 62.7

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X3360
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2830
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
Disk Subsystem:	1 x 160 GB 7200RPM SATA HDD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 SP1 (x86_64) Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**526**	**74.3**	528	74.1	526	74.3	4	439	89.1	**438**	**89.2**	436	89.6
401.bzip2	4	**662**	**58.3**	666	58.0	661	58.4	4	616	62.6	623	62.0	**621**	**62.1**
403.gcc	4	520	62.0	**520**	**62.0**	521	61.8	4	**526**	**61.3**	526	61.2	520	61.9
429.mcf	4	**568**	**64.2**	570	64.0	568	64.3	4	595	61.3	594	61.4	**594**	**61.4**
445.gobmk	4	562	74.7	560	74.9	**561**	**74.8**	4	**516**	**81.3**	516	81.4	517	81.1
456.hmmer	4	624	59.8	625	59.8	**624**	**59.8**	4	362	103	**362**	**103**	364	103
458.sjeng	4	**709**	**68.3**	710	68.2	706	68.6	4	632	76.6	**633**	**76.5**	636	76.1
462.libquantum	4	**2064**	**40.2**	2060	40.2	2064	40.1	1	201	103	**201**	**103**	199	104
464.h264ref	4	**740**	**120**	740	120	740	120	4	706	125	705	126	**705**	**126**
471.omnetpp	4	604	41.4	**605**	**41.4**	605	41.3	4	586	42.7	583	42.9	**583**	**42.9**
473.astar	4	**625**	**44.9**	622	45.2	627	44.8	4	573	49.0	**573**	**49.0**	572	49.1
483.xalancbmk	4	350	78.8	**351**	**78.6**	352	78.4	4	350	78.8	**351**	**78.6**	352	78.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
All results but peak 462.libquantum used taskset to bind
processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREAD set to number to cores
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

456.hmmer:
```
/opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml`

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