



SPEC[®] CINT2006 Result

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Fujitsu Siemens Computers

SPECint[®]_rate2006 = 20.5

PRIMERGY RX100 S5, Intel Pentium Dual Core E2140, 1.60 GHz

SPECint_rate_base2006 = 18.2

CPU2006 license: 22

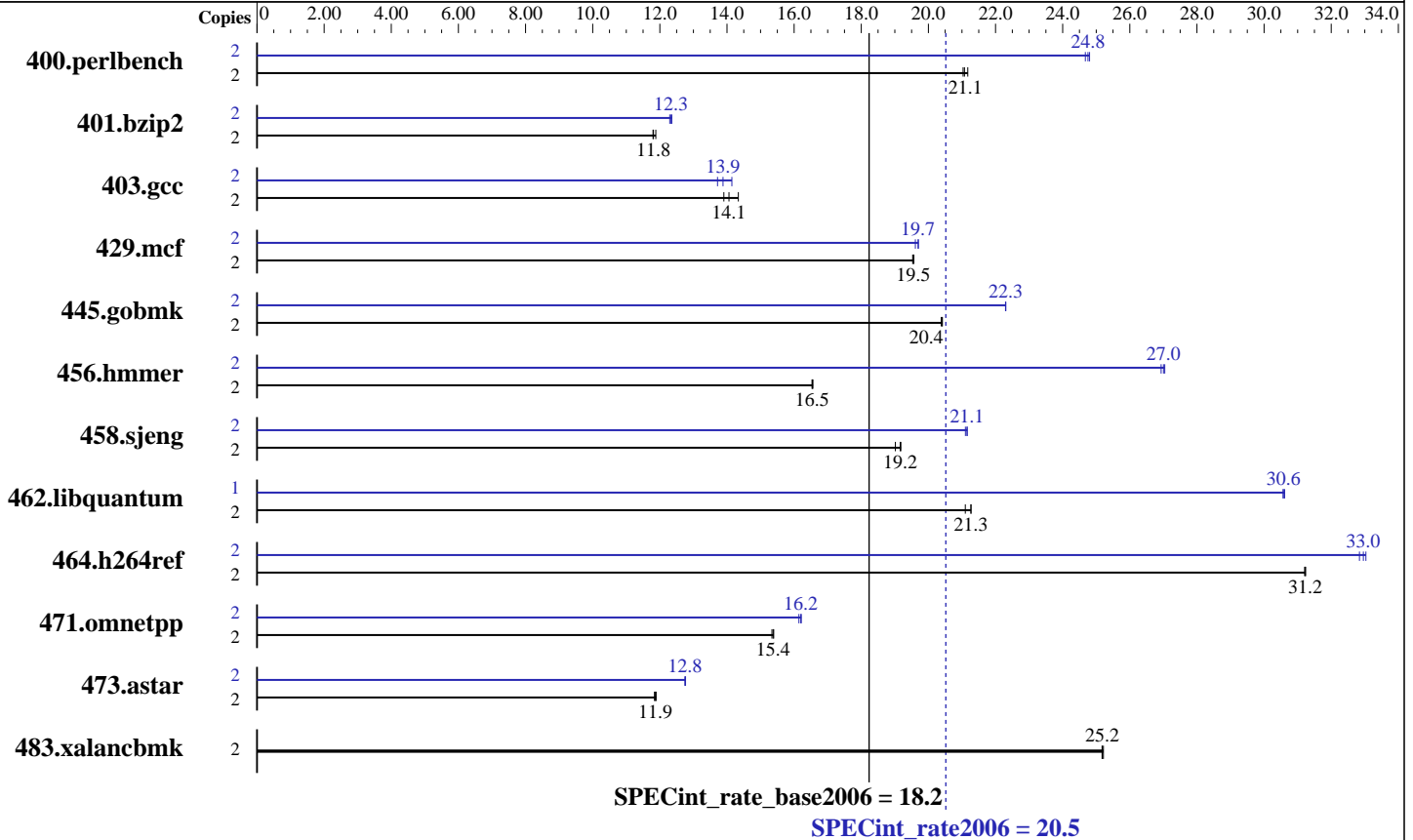
Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Jan-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Pentium Dual Core E2140
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CAS 6-6-6, with ECC)
 Disk Subsystem: Western Digital WD5000AAKS (SATA, 500GB, 7200rpm)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	923	21.2	929	21.0	<u>927</u>	<u>21.1</u>	2	792	24.7	<u>789</u>	<u>24.8</u>	788	24.8
401.bzip2	2	<u>1634</u>	<u>11.8</u>	1636	11.8	1625	11.9	2	<u>1565</u>	<u>12.3</u>	1562	12.4	1570	12.3
403.gcc	2	<u>1145</u>	<u>14.1</u>	1158	13.9	1123	14.3	2	1174	13.7	1138	14.1	<u>1160</u>	<u>13.9</u>
429.mcf	2	934	19.5	932	19.6	<u>933</u>	<u>19.5</u>	2	<u>926</u>	<u>19.7</u>	930	19.6	926	19.7
445.gobmk	2	1028	20.4	<u>1028</u>	<u>20.4</u>	1029	20.4	2	940	22.3	941	22.3	<u>941</u>	<u>22.3</u>
456.hmmmer	2	1127	16.6	<u>1128</u>	<u>16.5</u>	1129	16.5	2	693	26.9	690	27.0	<u>691</u>	<u>27.0</u>
458.sjeng	2	1273	19.0	<u>1263</u>	<u>19.2</u>	1262	19.2	2	1144	21.2	<u>1144</u>	<u>21.1</u>	1147	21.1
462.libquantum	2	1964	21.1	<u>1948</u>	<u>21.3</u>	1948	21.3	1	<u>678</u>	<u>30.6</u>	678	30.6	677	30.6
464.h264ref	2	<u>1417</u>	<u>31.2</u>	1418	31.2	1417	31.2	2	1340	33.0	1348	32.8	<u>1343</u>	<u>33.0</u>
471.omnetpp	2	812	15.4	<u>813</u>	<u>15.4</u>	815	15.3	2	<u>772</u>	<u>16.2</u>	771	16.2	774	16.1
473.astar	2	1181	11.9	<u>1183</u>	<u>11.9</u>	1186	11.8	2	1101	12.7	<u>1101</u>	<u>12.8</u>	1100	12.8
483.xalancbmk	2	548	25.2	547	25.2	<u>548</u>	<u>25.2</u>	2	548	25.2	547	25.2	<u>548</u>	<u>25.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'OMP_NUM_THREADS' set to number of cores (default)

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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