



SPEC[®] CINT2006 Result

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Itautec

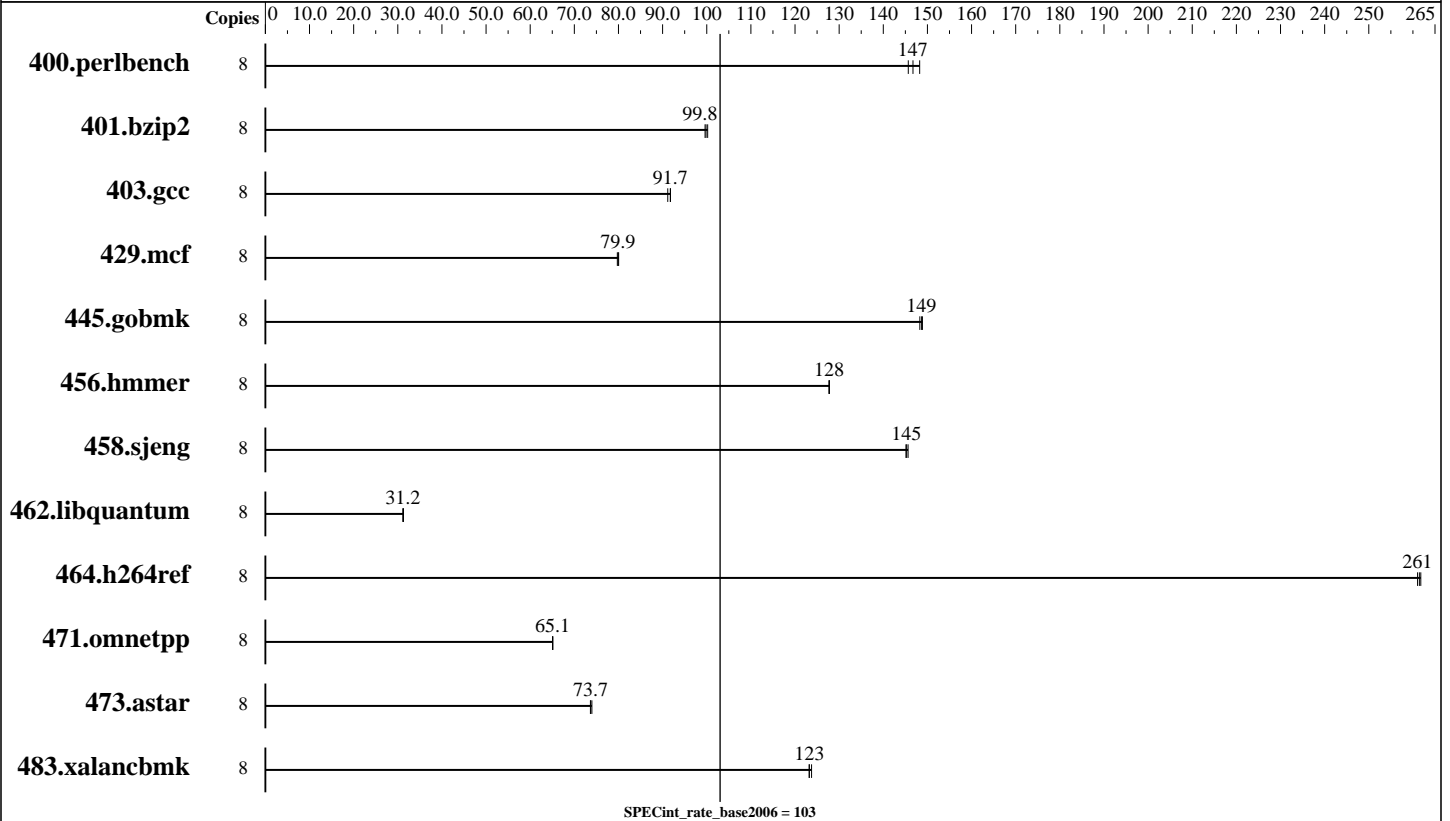
SPECint[®]_rate2006 = Not Run

Servidor Itautec LX201 (Intel Xeon X5460)

SPECint_rate_base2006 = 103

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics:
 CPU MHz: 3160
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 * 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SATA-2, 160GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: No
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	537	146	527	148	<u>533</u>	<u>147</u>							
401.bzip2	8	775	99.6	770	100	<u>773</u>	<u>99.8</u>							
403.gcc	8	702	91.8	706	91.2	<u>702</u>	<u>91.7</u>							
429.mcf	8	912	80.0	<u>913</u>	<u>79.9</u>	915	79.7							
445.gobmk	8	566	148	<u>564</u>	<u>149</u>	564	149							
456.hammer	8	584	128	584	128	<u>584</u>	<u>128</u>							
458.sjeng	8	<u>666</u>	<u>145</u>	667	145	665	146							
462.libquantum	8	5318	31.2	<u>5312</u>	<u>31.2</u>	5298	31.3							
464.h264ref	8	676	262	678	261	<u>677</u>	<u>261</u>							
471.omnetpp	8	768	65.1	<u>768</u>	<u>65.1</u>	768	65.1							
473.astar	8	763	73.6	<u>762</u>	<u>73.7</u>	759	74.0							
483.xalancbmk	8	446	124	<u>448</u>	<u>123</u>	448	123							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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