



SPEC® CINT2006 Result

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Itaotec

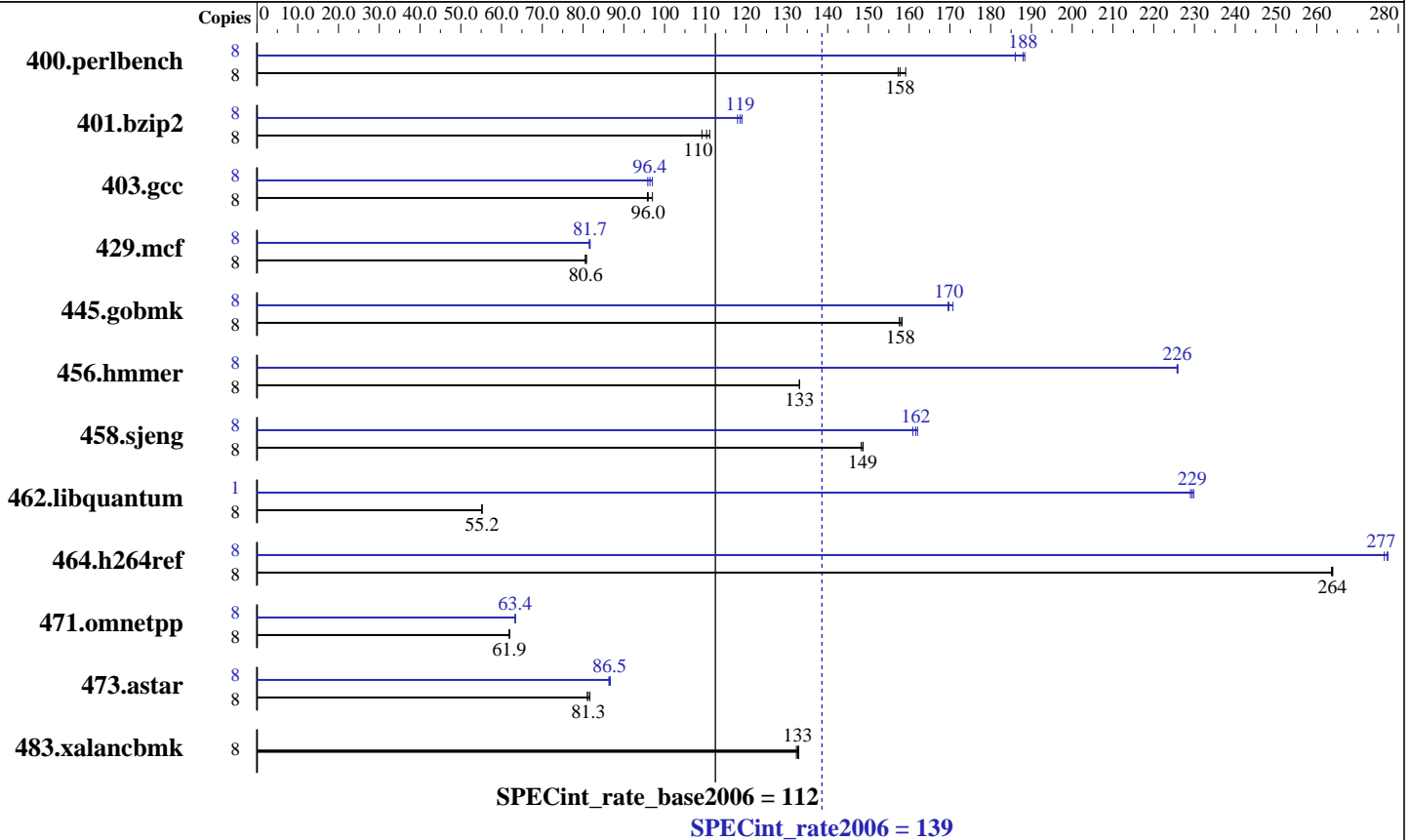
SPECint®_rate2006 = 139

Servidor Itaotec MX201 (Intel Xeon X5460)

SPECint_rate_base2006 = 112

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics:
 CPU MHz: 3160
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|-------------|------------|-------------|------------|-------------|--------|-------------|------------|------------|------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 491 | 159 | 495 | 158 | 497 | 157 | 8 | 416 | 188 | 415 | 188 | 420 | 186 |
| 401.bzip2 | 8 | 700 | 110 | 695 | 111 | 707 | 109 | 8 | 655 | 118 | 651 | 119 | 649 | 119 |
| 403.gcc | 8 | 671 | 96.0 | 664 | 97.0 | 672 | 95.8 | 8 | 672 | 95.9 | 664 | 97.0 | 668 | 96.4 |
| 429.mcf | 8 | 902 | 80.8 | 906 | 80.6 | 905 | 80.6 | 8 | 895 | 81.5 | 893 | 81.7 | 893 | 81.7 |
| 445.gobmk | 8 | 533 | 158 | 531 | 158 | 530 | 158 | 8 | 491 | 171 | 494 | 170 | 495 | 170 |
| 456.hammer | 8 | 561 | 133 | 561 | 133 | 561 | 133 | 8 | 331 | 226 | 331 | 226 | 330 | 226 |
| 458.sjeng | 8 | 651 | 149 | 651 | 149 | 653 | 148 | 8 | 597 | 162 | 599 | 162 | 602 | 161 |
| 462.libquantum | 8 | 3002 | 55.2 | 3004 | 55.2 | 3002 | 55.2 | 1 | 90.3 | 229 | 90.5 | 229 | 90.2 | 230 |
| 464.h264ref | 8 | 671 | 264 | 672 | 264 | 671 | 264 | 8 | 638 | 277 | 640 | 277 | 638 | 277 |
| 471.omnetpp | 8 | 807 | 61.9 | 808 | 61.9 | 807 | 61.9 | 8 | 789 | 63.4 | 789 | 63.3 | 789 | 63.4 |
| 473.astar | 8 | 693 | 81.0 | 691 | 81.3 | 688 | 81.7 | 8 | 648 | 86.7 | 651 | 86.3 | 649 | 86.5 |
| 483.xalancbmk | 8 | 415 | 133 | 417 | 132 | 416 | 133 | 8 | 415 | 133 | 417 | 132 | 416 | 133 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Enabled

General Notes

This result was measured on the Servidor Itaotec MX201.
The Servidor Itaotec MX201 and the Servidor Itaotec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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