



SPEC® CINT2006 Result

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Itaotec

SPECint®_rate2006 = Not Run

Servidor Itaotec LX211 (Intel Xeon E5440)

SPECint_rate_base2006 = 57.5

CPU2006 license: 9001

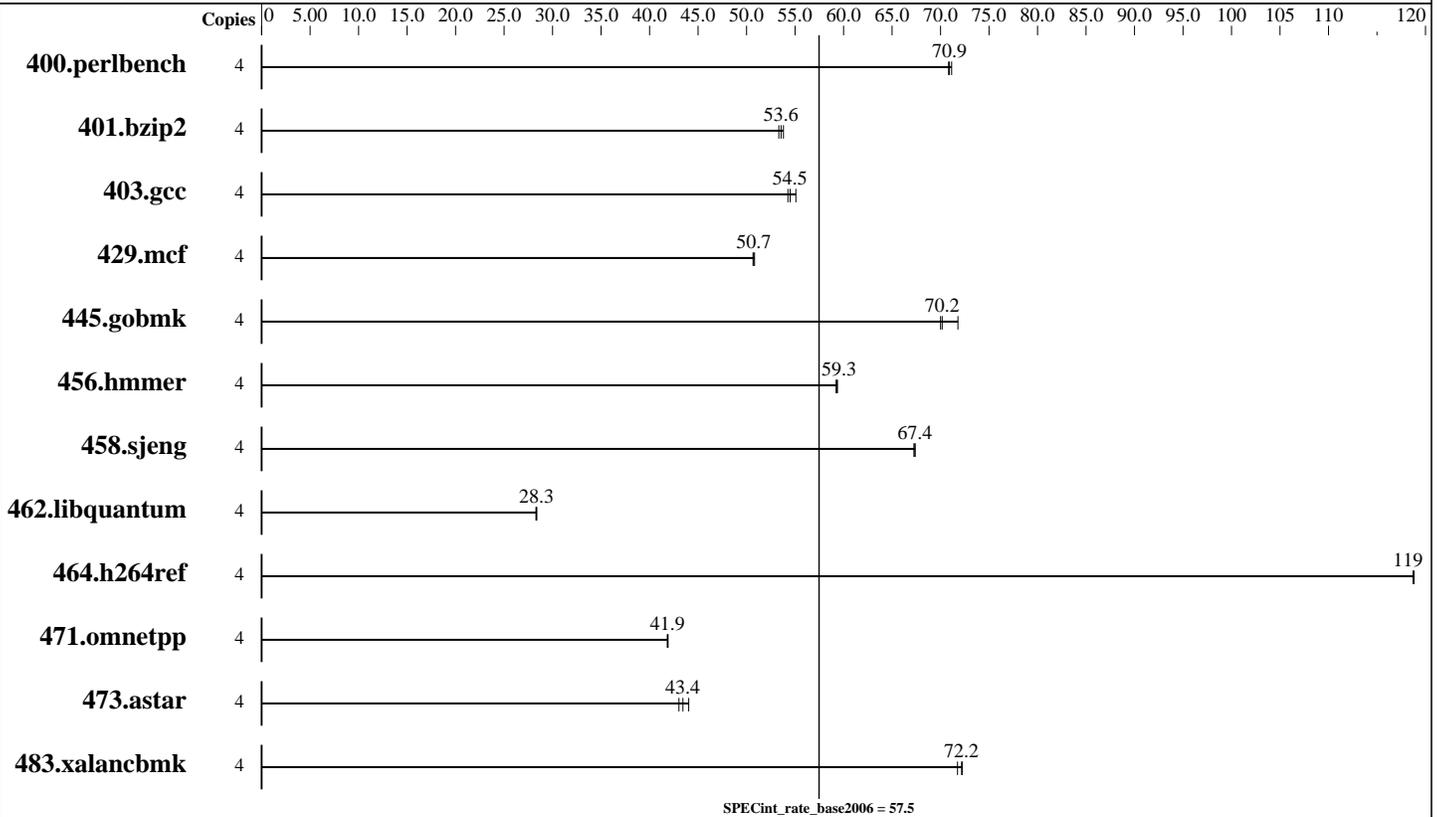
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics:
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SATA, 200GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	549	71.1	552	70.8	<u>551</u>	<u>70.9</u>							
401.bzip2	4	717	53.8	724	53.3	<u>721</u>	<u>53.6</u>							
403.gcc	4	593	54.3	<u>591</u>	<u>54.5</u>	585	55.1							
429.mcf	4	718	50.8	720	50.7	<u>719</u>	<u>50.7</u>							
445.gobmk	4	584	71.8	599	70.0	<u>598</u>	<u>70.2</u>							
456.hammer	4	<u>629</u>	<u>59.3</u>	630	59.2	629	59.4							
458.sjeng	4	720	67.3	718	67.4	<u>719</u>	<u>67.4</u>							
462.libquantum	4	2922	28.4	<u>2924</u>	<u>28.3</u>	2930	28.3							
464.h264ref	4	746	119	745	119	<u>746</u>	<u>119</u>							
471.omnetpp	4	597	41.9	598	41.8	<u>597</u>	<u>41.9</u>							
473.astar	4	638	44.0	<u>646</u>	<u>43.4</u>	653	43.0							
483.xalancbmk	4	382	72.2	385	71.8	<u>382</u>	<u>72.2</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Itautec

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Test date: Mar-2008
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Base Optimization Flags

C benchmarks:
-fast -inline-alloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs -L/CPU2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090713.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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