SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

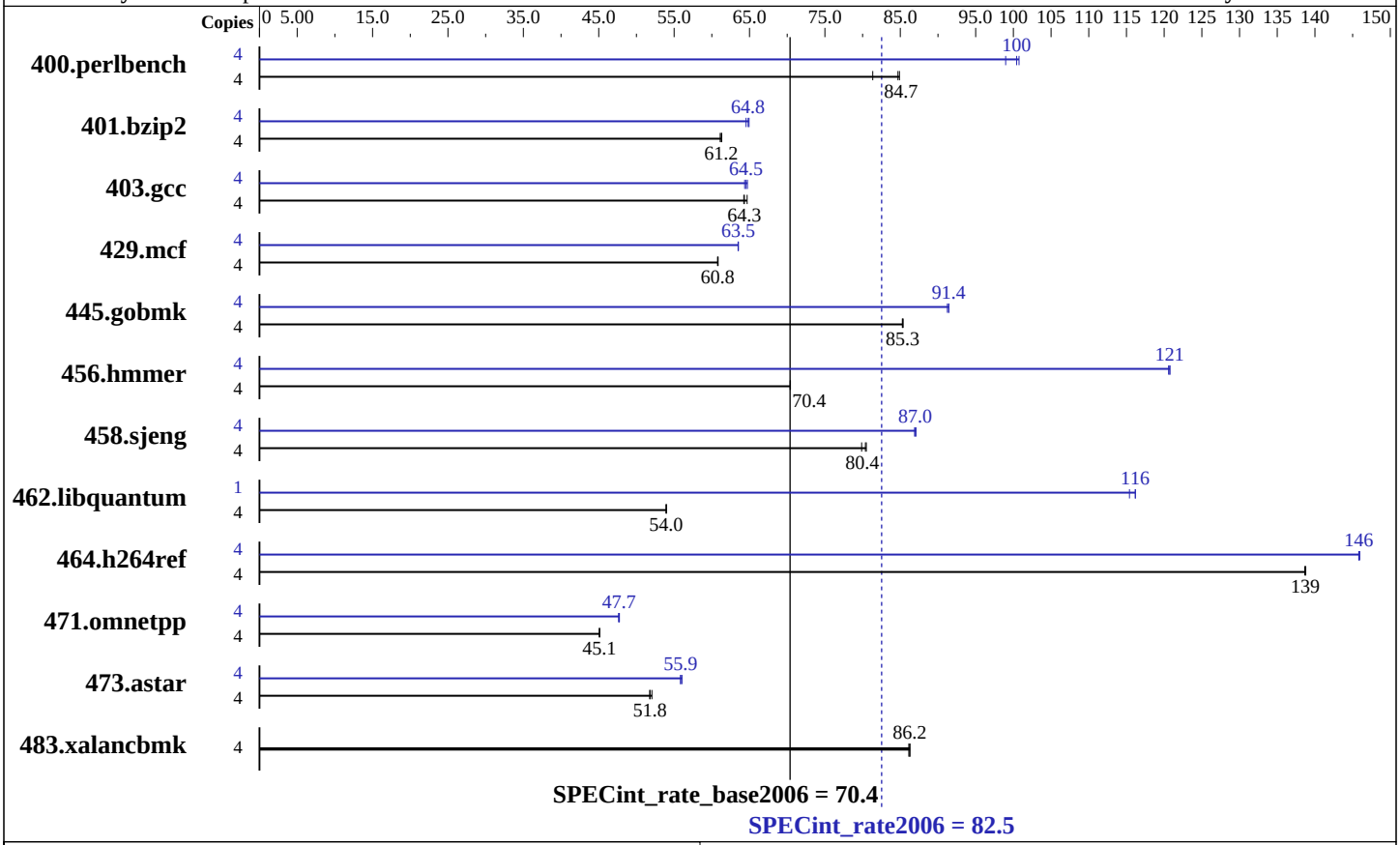
Supermicro
Motherboard X7DBR-3

SPECint®_rate2006 = 82.5

SPECint_rate_base2006 = 70.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	Dual Core, 3.33GHz
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	Western DigitalD WD1600YS-01SHB1 160GB SATA II, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-default
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DBR-3

SPECint_rate2006 = 82.5

SPECint_rate_base2006 = 70.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	480	81.3	460	84.9	**461**	**84.7**	4	**389**	**100**	388	101	395	99.0
401.bzip2	4	**630**	**61.2**	632	61.1	630	61.3	4	**596**	**64.8**	594	64.9	598	64.5
403.gcc	4	501	64.2	**501**	**64.3**	498	64.7	4	500	64.4	498	64.7	**499**	**64.5**
429.mcf	4	600	60.8	600	60.8	**600**	**60.8**	4	**574**	**63.5**	574	63.5	575	63.5
445.gobmk	4	492	85.4	**492**	**85.3**	492	85.3	4	460	91.2	**459**	**91.4**	459	91.4
456.hmmer	4	530	70.4	530	70.4	**530**	**70.4**	4	**309**	**121**	309	121	309	121
458.sjeng	4	**602**	**80.4**	606	79.9	601	80.5	4	**556**	**87.0**	557	86.9	556	87.1
462.libquantum	4	**1536**	**54.0**	1537	53.9	1536	54.0	1	**178**	**116**	180	115	178	116
464.h264ref	4	638	139	638	139	**638**	**139**	4	607	146	**607**	**146**	607	146
471.omnetpp	4	**554**	**45.1**	554	45.1	554	45.1	4	**524**	**47.7**	524	47.7	525	47.6
473.astar	4	542	51.8	**542**	**51.8**	539	52.1	4	**503**	**55.9**	501	56.0	503	55.9
483.xalancbmk	4	320	86.1	320	86.3	**320**	**86.2**	4	320	86.1	320	86.3	**320**	**86.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-815TQ-700V case,
To ensure system stability, a 500W (minimum) ATX power supply
[4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBR-3.cfm
The system bus runs at 1333 MHz
The "taskset" command was used to bind benchmark copies to cores
except for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

456.hmmer:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 17:02:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.