SPEC® CFP2006 Result

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Bull SAS

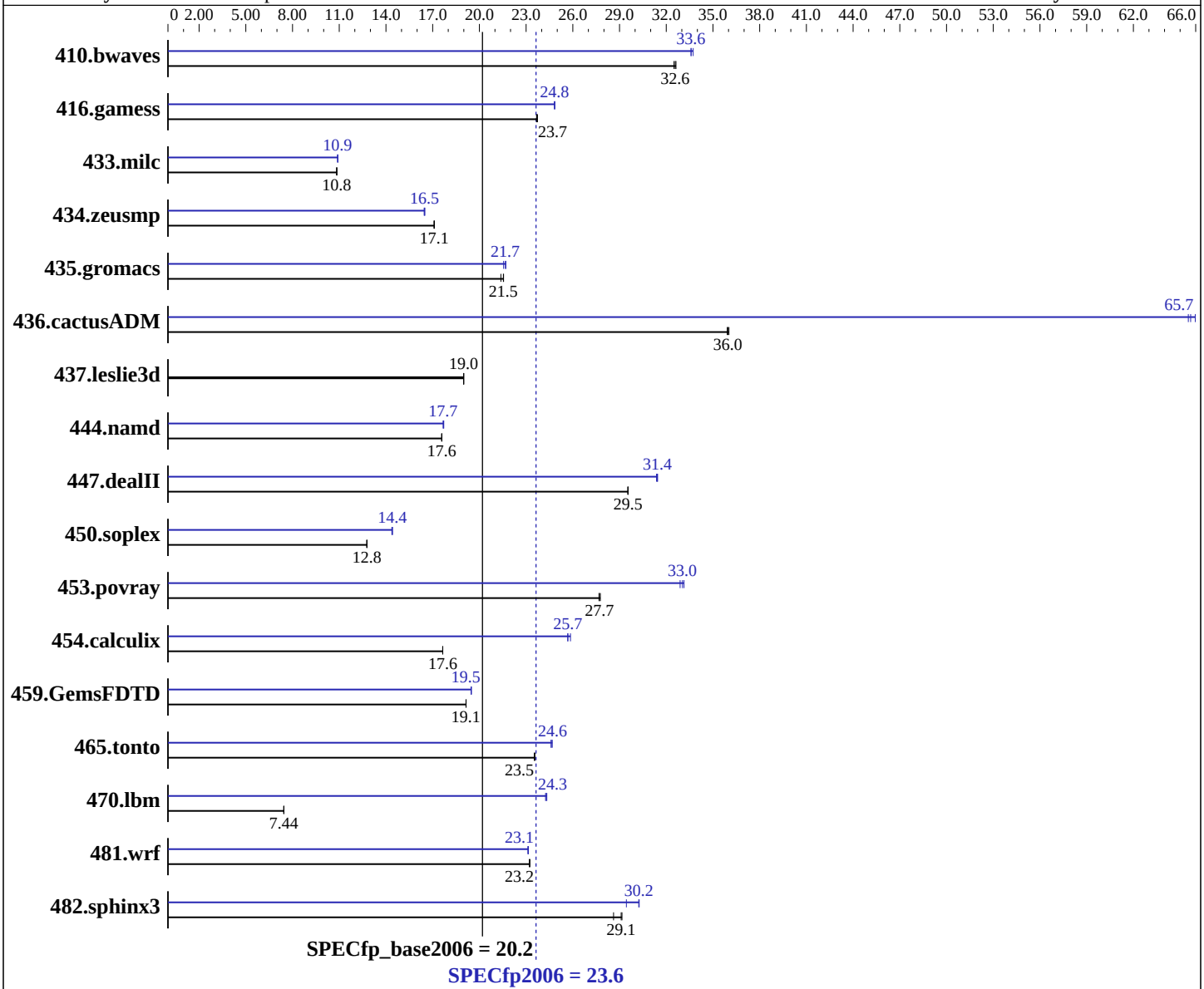
NovaScale T860 E1
(Intel Xeon X5260,3.33GHz)

SPECfp®2006 = 23.6

SPECfp_base2006 = 20.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	3.33 GHz, 6 MB L2, 1333 MHz bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2

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L3 Cache: None
Other Cache: None
Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: binutils-2.17.tar.gz, Version 2.17

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	417	32.6	418	32.5	**417**	**32.6**	**405**	**33.6**	403	33.7	405	33.6
416.gamess	825	23.7	**826**	**23.7**	827	23.7	**788**	**24.8**	789	24.8	788	24.8
433.milc	846	10.8	**847**	**10.8**	847	10.8	**842**	**10.9**	842	10.9	843	10.9
434.zeusmp	532	17.1	532	17.1	**532**	**17.1**	552	16.5	553	16.5	**552**	**16.5**
435.gromacs	**332**	**21.5**	334	21.4	331	21.6	331	21.6	329	21.7	**329**	**21.7**
436.cactusADM	**332**	**36.0**	333	35.9	332	36.0	181	66.0	**182**	**65.7**	182	65.5
437.leslie3d	495	19.0	**495**	**19.0**	495	19.0	495	19.0	**495**	**19.0**	495	19.0
444.namd	456	17.6	**456**	**17.6**	456	17.6	454	17.7	453	17.7	**454**	**17.7**
447.dealII	**387**	**29.5**	387	29.5	387	29.5	365	31.4	364	31.4	**364**	**31.4**
450.soplex	653	12.8	**653**	**12.8**	653	12.8	579	14.4	**579**	**14.4**	580	14.4
453.povray	**192**	**27.7**	192	27.7	192	27.8	162	32.9	**161**	**33.0**	161	33.1
454.calculix	**467**	**17.6**	467	17.6	468	17.6	321	25.7	319	25.8	**321**	**25.7**
459.GemsFDTD	554	19.1	554	19.1	**554**	**19.1**	545	19.5	545	19.5	**545**	**19.5**
465.tonto	**418**	**23.5**	418	23.6	418	23.5	400	24.6	399	24.7	**400**	**24.6**
470.lbm	1844	7.45	1848	7.44	**1846**	**7.44**	565	24.3	**566**	**24.3**	567	24.2
481.wrf	481	23.2	481	23.2	**481**	**23.2**	**483**	**23.1**	483	23.1	483	23.1
482.sphinx3	**670**	**29.1**	681	28.6	668	29.2	644	30.3	**645**	**30.2**	662	29.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Intel SpeedStep Technology: Disabled
```

General Notes

```
All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode
```

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General Notes (Continued)

```
The NEC Express5800/120Lj(Intel Xeon X5260) and
the Bull NovaScale T860 E1(Intel Xeon X5260,3.33GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/120Lj(Intel Xeon X5260) model.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-fast -parallel
```

C++ benchmarks:
```
-fast -parallel
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-fast -parallel`

Benchmarks using both Fortran and C:
`-fast -parallel`

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

482.sphinx3: `-fast -unroll2`

C++ benchmarks:

444.namd: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

447.dealII: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias -scalar-rep-`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-fast -prefetch -parallel`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -prefetch -parallel`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -parallel -auto-ilp32`

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

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Peak Optimization Flags (Continued)

481.wrf: `-fast -parallel -prefetch -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.xml`

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