



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint®_rate2006 = 74.8

PRIMERGY RX600 S4, Intel Xeon E7220, 2.93 GHz

SPECint_rate_base2006 = 62.0

CPU2006 license: 22

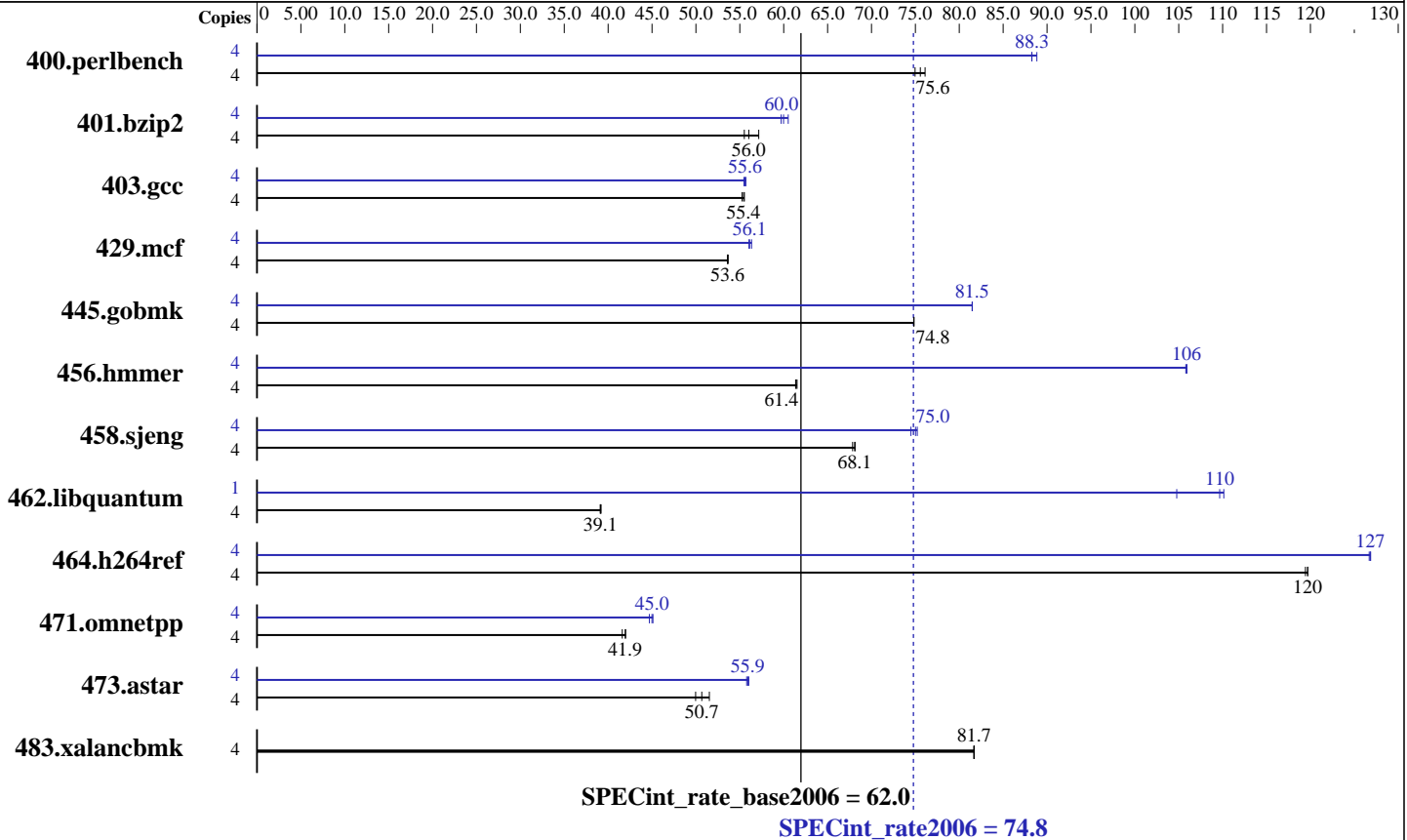
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7220
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per core
 L3 Cache: None
 Other Cache: None
 Memory: 64 GB (16x4 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.8

PRIMERGY RX600 S4, Intel Xeon E7220, 2.93 GHz

SPECint_rate_base2006 = 62.0

CPU2006 license: 22

Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	521	75.0	<u>517</u>	<u>75.6</u>	514	76.1	4	443	88.3	440	88.8	<u>443</u>	<u>88.3</u>
401.bzip2	4	675	57.2	695	55.5	<u>689</u>	<u>56.0</u>	4	638	60.5	<u>644</u>	<u>60.0</u>	646	59.7
403.gcc	4	583	55.3	<u>581</u>	<u>55.4</u>	580	55.5	4	580	55.5	578	55.7	<u>579</u>	<u>55.6</u>
429.mcf	4	<u>680</u>	<u>53.6</u>	680	53.7	681	53.6	4	651	56.0	<u>650</u>	<u>56.1</u>	647	56.3
445.gobmk	4	561	74.8	561	74.8	<u>561</u>	<u>74.8</u>	4	<u>515</u>	<u>81.5</u>	515	81.5	515	81.5
456.hmmmer	4	<u>608</u>	<u>61.4</u>	607	61.5	608	61.4	4	352	106	353	106	<u>352</u>	<u>106</u>
458.sjeng	4	710	68.1	713	67.8	<u>711</u>	<u>68.1</u>	4	<u>645</u>	<u>75.0</u>	650	74.5	643	75.2
462.libquantum	4	<u>2118</u>	<u>39.1</u>	2120	39.1	2114	39.2	1	188	110	<u>189</u>	<u>110</u>	198	105
464.h264ref	4	740	120	741	119	<u>740</u>	<u>120</u>	4	699	127	<u>698</u>	<u>127</u>	698	127
471.omnetpp	4	<u>596</u>	<u>41.9</u>	601	41.6	595	42.0	4	<u>556</u>	<u>45.0</u>	554	45.1	559	44.7
473.astar	4	<u>554</u>	<u>50.7</u>	545	51.5	562	50.0	4	501	56.0	<u>502</u>	<u>55.9</u>	503	55.8
483.xalancbmk	4	338	81.7	<u>338</u>	<u>81.7</u>	338	81.6	4	338	81.7	<u>338</u>	<u>81.7</u>	338	81.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'OMP_NUM_THREADS' set to number of cores (default)

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.8

PRIMERGY RX600 S4, Intel Xeon E7220, 2.93 GHz

SPECint_rate_base2006 = 62.0

CPU2006 license: 22

Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.8

PRIMERGY RX600 S4, Intel Xeon E7220, 2.93 GHz

SPECint_rate_base2006 = 62.0

CPU2006 license: 22

Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.8

PRIMERGY RX600 S4, Intel Xeon E7220, 2.93 GHz

SPECint_rate_base2006 = 62.0

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.04.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:46:36 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 March 2008.