



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3550 (Intel Xeon E5440)

## SPECint\_rate\_base2006 = 107

CPU2006 license: 11

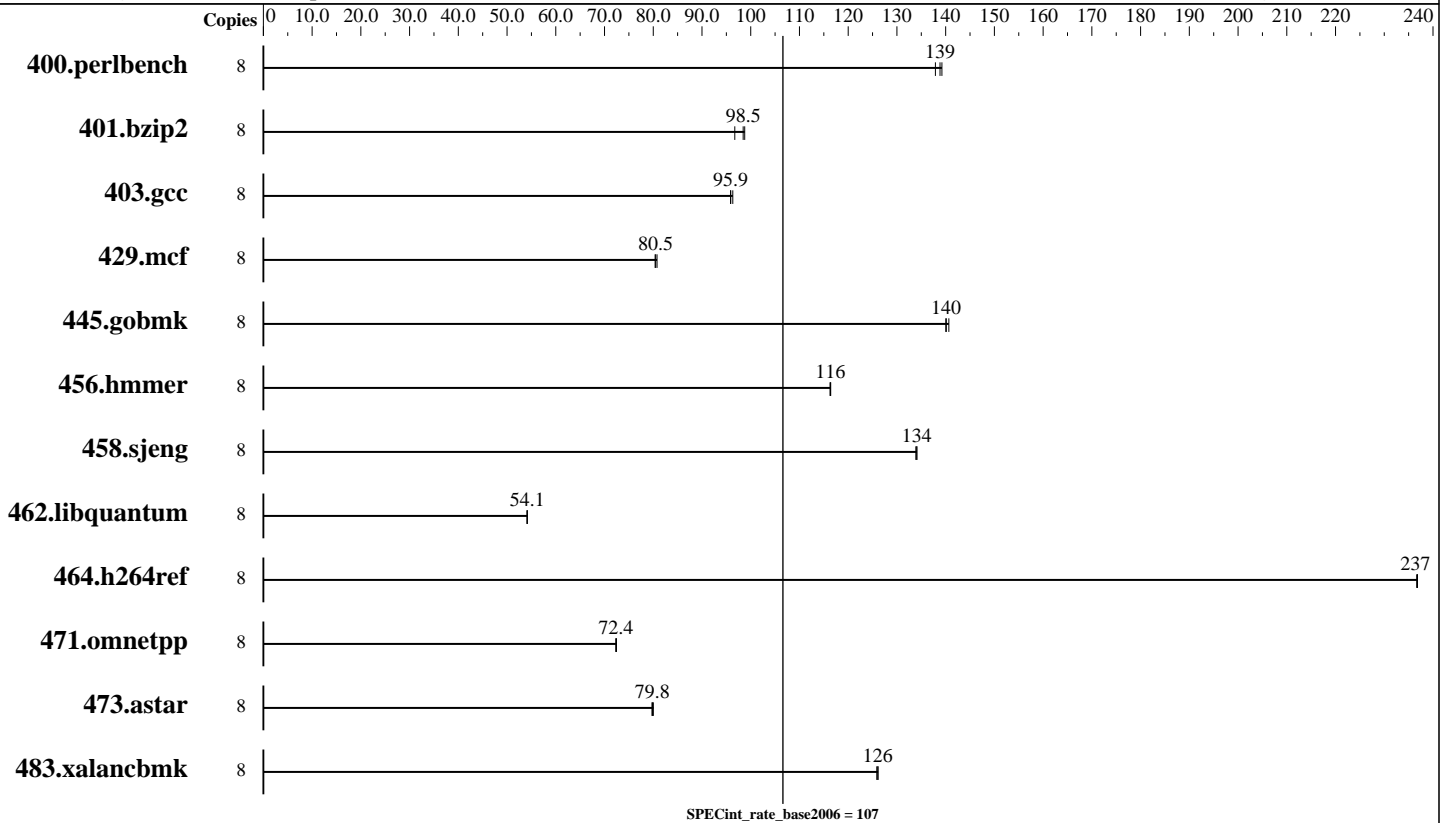
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5440  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2833  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	567	138	561	139	<u>563</u>	<u>139</u>							
401.bzip2	8	782	98.8	798	96.7	<u>784</u>	<u>98.5</u>							
403.gcc	8	669	96.3	672	95.9	<u>672</u>	<u>95.9</u>							
429.mcf	8	903	80.8	<u>906</u>	<u>80.5</u>	908	80.4							
445.gobmk	8	597	141	599	140	<u>599</u>	<u>140</u>							
456.hammer	8	<u>641</u>	<u>116</u>	642	116	641	116							
458.sjeng	8	722	134	<u>722</u>	<u>134</u>	723	134							
462.libquantum	8	3061	54.1	3063	54.1	<u>3062</u>	<u>54.1</u>							
464.h264ref	8	748	237	748	237	<u>748</u>	<u>237</u>							
471.omnetpp	8	<u>691</u>	<u>72.4</u>	691	72.3	690	72.5							
473.astar	8	704	79.7	<u>703</u>	<u>79.8</u>	702	80.0							
483.xalancbmk	8	439	126	<u>438</u>	<u>126</u>	438	126							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode  
 Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
 Powersaved dameon was disabled in OS  
 taskset utility used to bind CPU(s) to processes

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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