SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

$SPECint^{®}\_rate2006 =$ 28.8

Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

$SPECint\_rate\_base2006 =$ 26.8
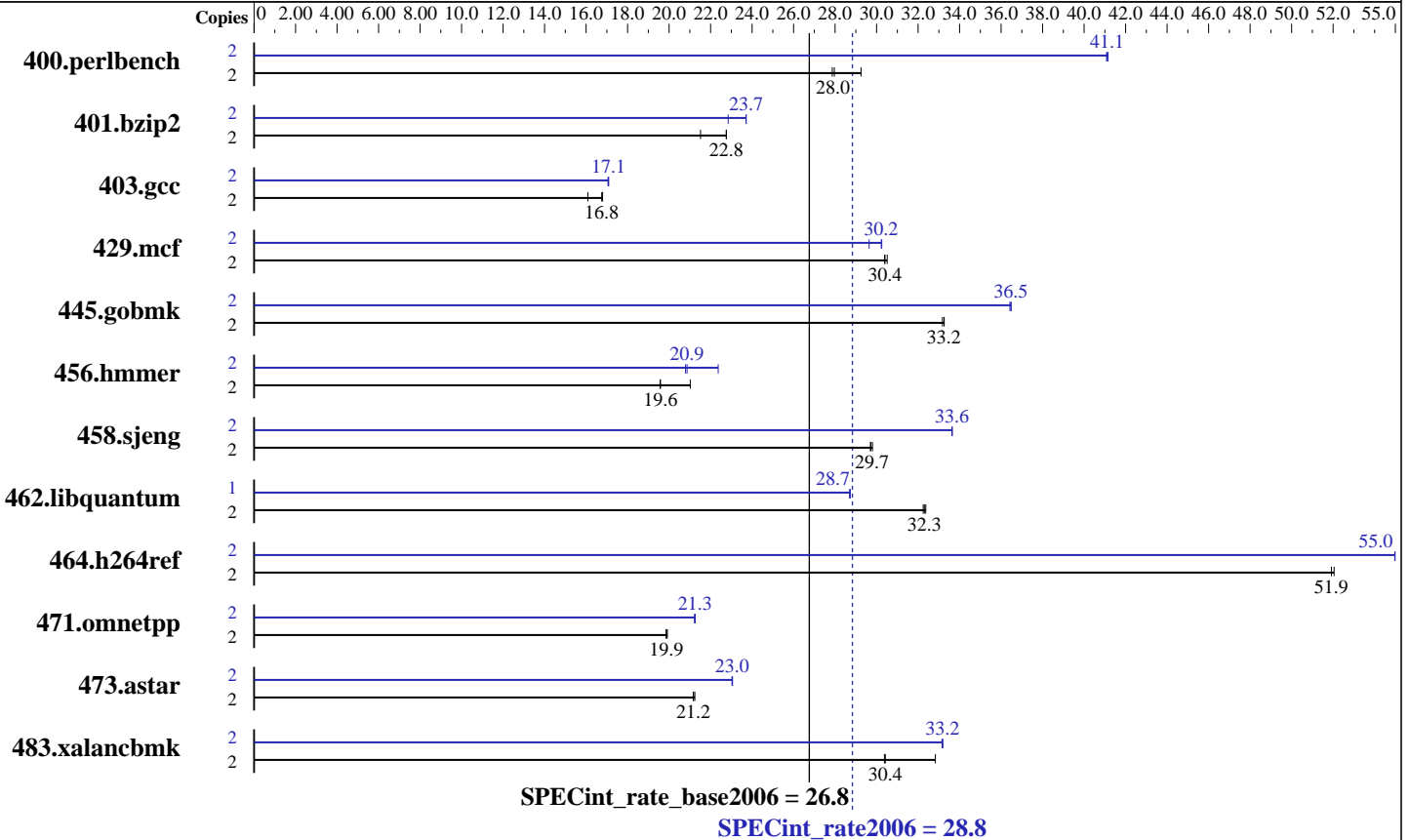
CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Benchmark	Copies	Value
400.perlbench	2 / 2	41.1 / 28.0
401.bzip2	2 / 2	23.7 / 22.8
403.gcc	2 / 2	17.1 / 16.8
429.mcf	2 / 2	30.2 / 30.4
445.gobmk	2 / 2	36.5 / 33.2
456.hmmer	2 / 2	20.9 / 19.6
458.sjeng	2 / 2	33.6 / 29.7
462.libquantum	1 / 2	28.7 / 32.3
464.h264ref	2 / 2	55.0 / 51.9
471.omnetpp	2 / 2	21.3 / 19.9
473.astar	2 / 2	23.0 / 21.2
483.xalancbmk	2 / 2	33.2 / 30.4

SPECint_rate_base2006 = 26.8

SPECint_rate2006 = 28.8

Hardware

CPU Name: Intel Core 2 Duo T7800
CPU Characteristics:
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem: Hitachi 100 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Windows Vista Ultimate (32-bit)
Compiler: Intel C++ Compiler for IA32 version 10.1
Build 20070913 Package ID: w_cc_p_10.1.011
Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None
SmartHeap Library Version 8.1 from
http://www.microquill.com/

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

SPECint_rate2006 = 28.8

SPECint_rate_base2006 = 26.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	668	29.3	701	27.9	**699**	**28.0**	2	475	41.2	476	41.1	**475**	**41.1**
401.bzip2	2	897	21.5	848	22.8	**848**	**22.8**	2	814	23.7	**814**	**23.7**	845	22.8
403.gcc	2	959	16.8	**961**	**16.8**	1001	16.1	2	**943**	**17.1**	944	17.1	942	17.1
429.mcf	2	598	30.5	**600**	**30.4**	600	30.4	2	603	30.2	**603**	**30.2**	615	29.6
445.gobmk	2	**631**	**33.2**	631	33.3	632	33.2	2	**575**	**36.5**	576	36.4	575	36.5
456.hmmer	2	888	21.0	954	19.6	**953**	**19.6**	2	**894**	**20.9**	834	22.4	897	20.8
458.sjeng	2	812	29.8	815	29.7	**814**	**29.7**	2	719	33.6	**719**	**33.6**	720	33.6
462.libquantum	2	1280	32.4	**1283**	**32.3**	1285	32.2	1	**722**	**28.7**	722	28.7	721	28.7
464.h264ref	2	850	52.0	852	51.9	**852**	**51.9**	2	**805**	**55.0**	805	55.0	805	55.0
471.omnetpp	2	628	19.9	**629**	**19.9**	630	19.9	2	589	21.2	**588**	**21.3**	588	21.3
473.astar	2	661	21.2	663	21.2	**663**	**21.2**	2	**609**	**23.0**	610	23.0	609	23.1
483.xalancbmk	2	420	32.8	454	30.4	**454**	**30.4**	2	**416**	**33.2**	416	33.2	416	33.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
    The system bus runs at 800 MHz
    Binaries were built on Windows Vista (32-bit)
    The following VS 2005 SP1 updates were applied: KB926601 and KB932232
    OMP_NUM_THREADS set to number of cores
    KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qvc8 -Qc99
```

C++ benchmarks:
```
    icl -Qvc8
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
    483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

SPEC CINT2006 Result

Intel Corporation

Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

SPECint_rate2006 =   28.8

SPECint_rate_base2006 = 26.8

CPU2006 license: 13		**Test date:**	Dec-2007
Test sponsor: Intel Corporation		**Hardware Availability:**	Jan-2008
Tested by: Intel Corporation		**Software Availability:**	Nov-2007

Base Optimization Flags

C benchmarks:
```
-fast -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
        -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Peak Portability Flags

```
     403.gcc: -DSPEC_CPU_WIN32
  464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
        -Qprefetch /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

    401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
        /F512000000

     403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

      429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
        /F512000000 shlw32m.lib              -link /FORCE:MULTIPLE
```

Continued on next page

Intel Corporation

Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

SPECint_rate2006 = 28.8

SPECint_rate_base2006 = 26.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Peak Optimization Flags (Continued)

445.gobmk: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo`
`-Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2`
`-Qansi-alias -Qopt-multi-version-aggressive /F512000000`

458.sjeng: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4`
`/F512000000`

462.libquantum: `-fast -Qunroll4 -Ob0 -Qprefetch`
`-Qopt-streaming-stores:always -Qparallel`
`-Qpar-runtime-control /F512000000`

464.h264ref: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2`
`-Qansi-alias /F512000000`

C++ benchmarks:

471.omnetpp: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias`
`-Qopt-ra-region-strategy=block -Qcxx_features /F512000000`
`shlw32m.lib          -link /FORCE:MULTIPLE`

473.astar: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias`
`-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000`
`shlw32m.lib          -link /FORCE:MULTIPLE`

483.xalancbmk: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias`
`-Qcxx_features /F512000000 shlw32m.lib`
`-link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

SPECint_rate2006 = 28.8

SPECint_rate_base2006 = 26.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:04:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.