



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 37.0

PowerEdge R200 (Intel Xeon 3075, 2.66 GHz)

SPECint\_rate\_base2006 = 32.0

CPU2006 license: 55

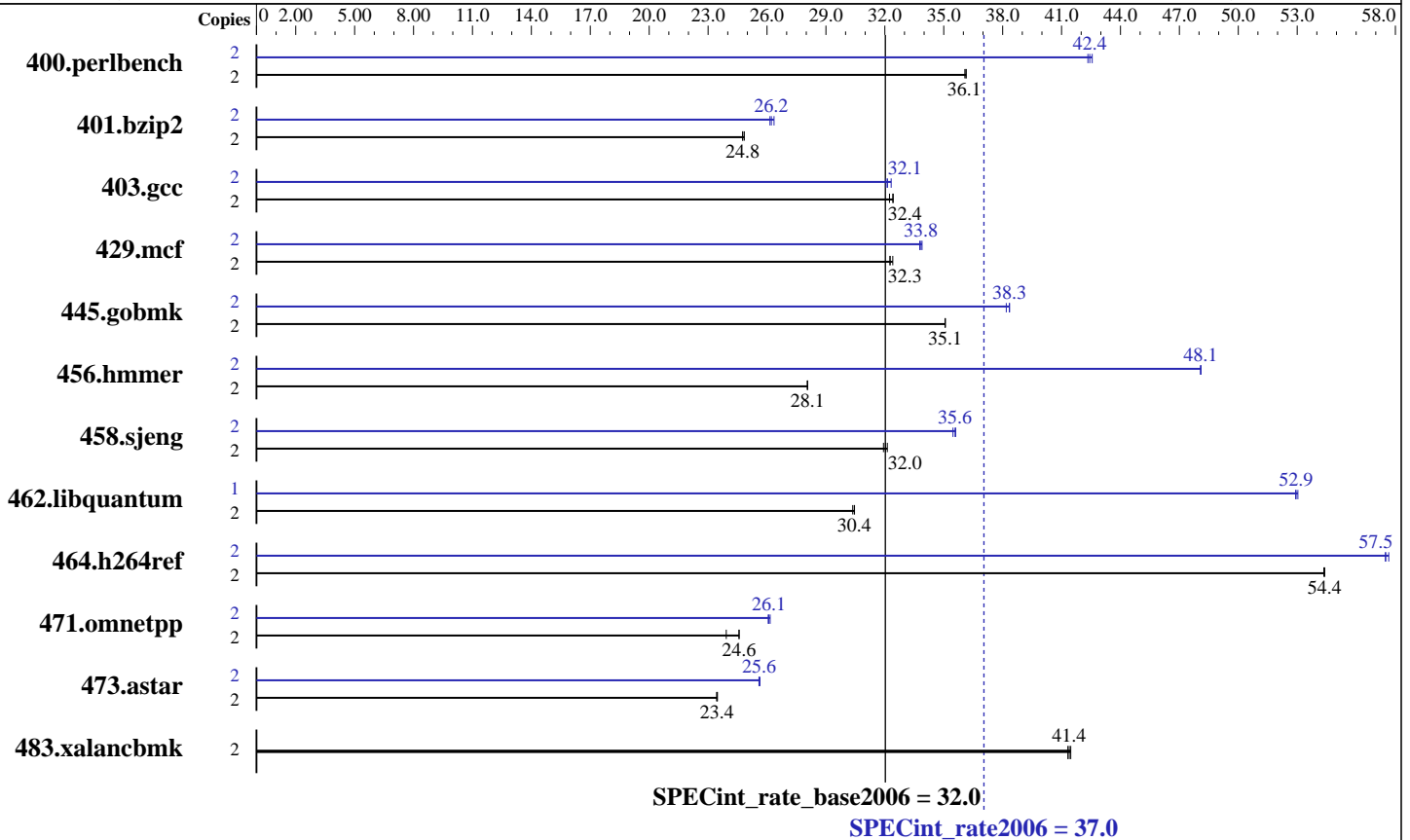
Test date: Nov-2007

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



## Hardware

CPU Name: Intel Xeon 3075  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2GB 800MHz DDR2)  
 Disk Subsystem: 73GB SAS, 10K RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1,  
 Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64  
 version 10.1  
 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>541</b>	<b>36.1</b>	541	36.1	542	36.1	2	<b>460</b>	<b>42.4</b>	459	42.6	461	42.3
401.bzip2	2	779	24.8	777	24.8	<b>779</b>	<b>24.8</b>	2	732	26.4	<b>736</b>	<b>26.2</b>	738	26.2
403.gcc	2	499	32.2	497	32.4	<b>497</b>	<b>32.4</b>	2	498	32.3	502	32.1	<b>501</b>	<b>32.1</b>
429.mcf	2	563	32.4	<b>565</b>	<b>32.3</b>	565	32.3	2	540	33.8	538	33.9	<b>539</b>	<b>33.8</b>
445.gobmk	2	598	35.1	<b>598</b>	<b>35.1</b>	598	35.1	2	549	38.2	547	38.4	<b>547</b>	<b>38.3</b>
456.hammer	2	<b>665</b>	<b>28.1</b>	665	28.1	665	28.1	2	388	48.1	388	48.1	<b>388</b>	<b>48.1</b>
458.sjeng	2	758	31.9	<b>755</b>	<b>32.0</b>	753	32.1	2	<b>680</b>	<b>35.6</b>	680	35.6	682	35.5
462.libquantum	2	1365	30.4	1361	30.5	<b>1361</b>	<b>30.4</b>	1	391	53.0	392	52.9	<b>391</b>	<b>52.9</b>
464.h264ref	2	814	54.4	<b>814</b>	<b>54.4</b>	814	54.4	2	<b>770</b>	<b>57.5</b>	770	57.5	768	57.7
471.omnetpp	2	508	24.6	<b>509</b>	<b>24.6</b>	522	23.9	2	480	26.1	478	26.2	<b>479</b>	<b>26.1</b>
473.astar	2	599	23.4	598	23.5	<b>599</b>	<b>23.4</b>	2	549	25.6	<b>548</b>	<b>25.6</b>	548	25.6
483.xalancbmk	2	333	41.5	<b>333</b>	<b>41.4</b>	334	41.3	2	333	41.5	<b>333</b>	<b>41.4</b>	334	41.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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