SPEC® CINT2006 Result

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Dell Inc.

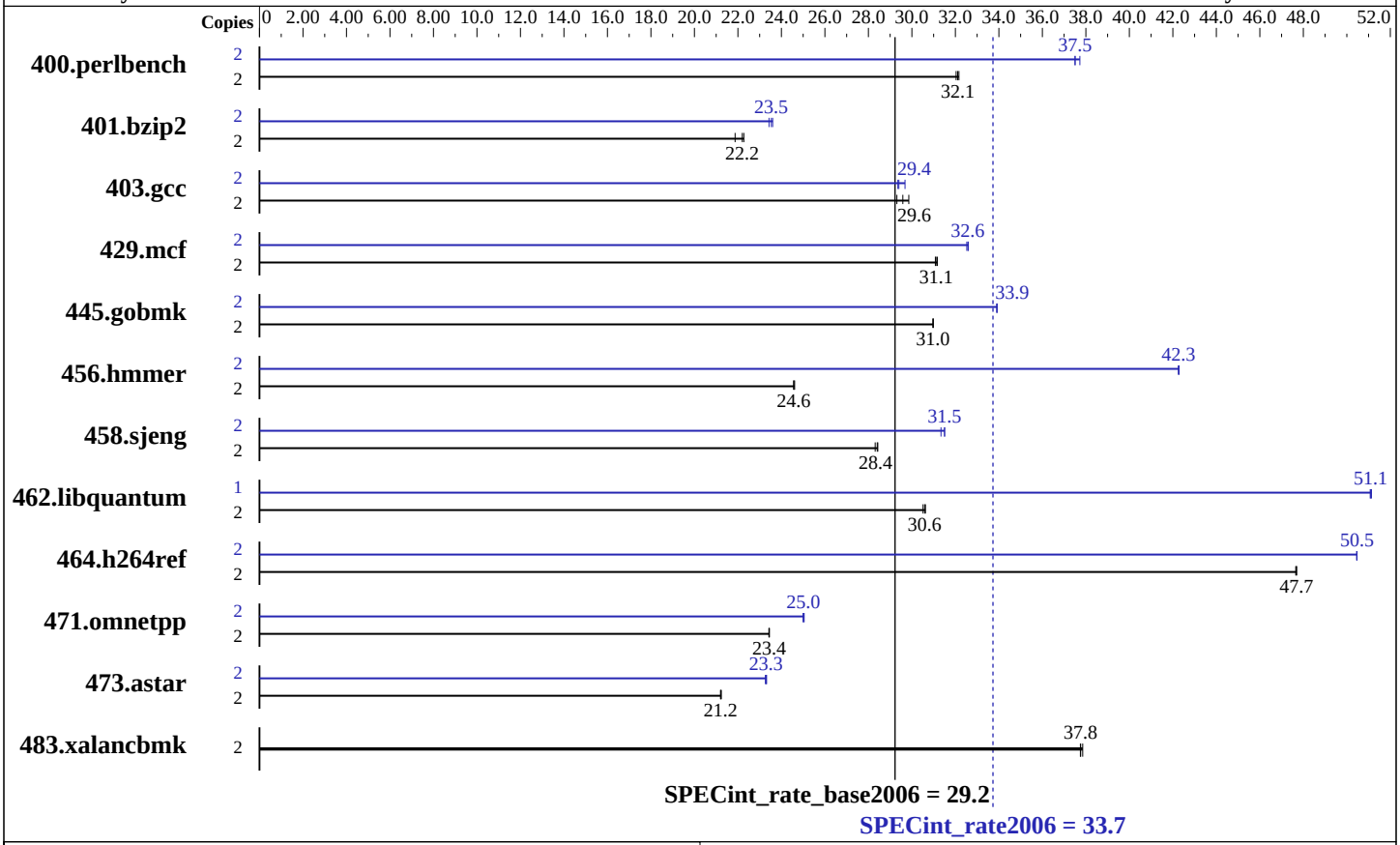
PowerEdge R200 (Intel Xeon 3065, 2.33 GHz)

SPECint®_rate2006 = 33.7

SPECint_rate_base2006 = 29.2

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon 3065
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB 800MHz DDR2)
Disk Subsystem:	73GB SAS, 10K RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	610	32.0	608	32.2	**609**	**32.1**	2	518	37.7	**521**	**37.5**	521	37.5
401.bzip2	2	882	21.9	**869**	**22.2**	867	22.3	2	818	23.6	**820**	**23.5**	824	23.4
403.gcc	2	**544**	**29.6**	549	29.3	539	29.9	2	**548**	**29.4**	548	29.4	542	29.7
429.mcf	2	585	31.2	**586**	**31.1**	587	31.1	2	**560**	**32.6**	561	32.5	560	32.6
445.gobmk	2	677	31.0	**677**	**31.0**	677	31.0	2	619	33.9	619	33.9	**619**	**33.9**
456.hmmer	2	**760**	**24.6**	758	24.6	760	24.6	2	442	42.3	**442**	**42.3**	441	42.3
458.sjeng	2	851	28.4	**852**	**28.4**	855	28.3	2	**768**	**31.5**	772	31.3	768	31.5
462.libquantum	2	1358	30.5	1353	30.6	**1355**	**30.6**	1	405	51.1	**405**	**51.1**	406	51.1
464.h264ref	2	**928**	**47.7**	929	47.7	928	47.7	2	**877**	**50.5**	877	50.5	877	50.4
471.omnetpp	2	534	23.4	533	23.4	**533**	**23.4**	2	499	25.0	500	25.0	**500**	**25.0**
473.astar	2	**662**	**21.2**	662	21.2	661	21.2	2	604	23.3	**603**	**23.3**	602	23.3
483.xalancbmk	2	365	37.9	**365**	**37.8**	366	37.7	2	365	37.9	**365**	**37.8**	366	37.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:03:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 December 2007.