



# SPEC<sup>®</sup> CINT2006 Result

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## IBM Corporation

## SPECint<sup>®</sup>\_rate2006 = Not Run

## IBM System x3650 (Intel Xeon E5405)

## SPECint\_rate\_base2006 = 86.1

CPU2006 license: 11

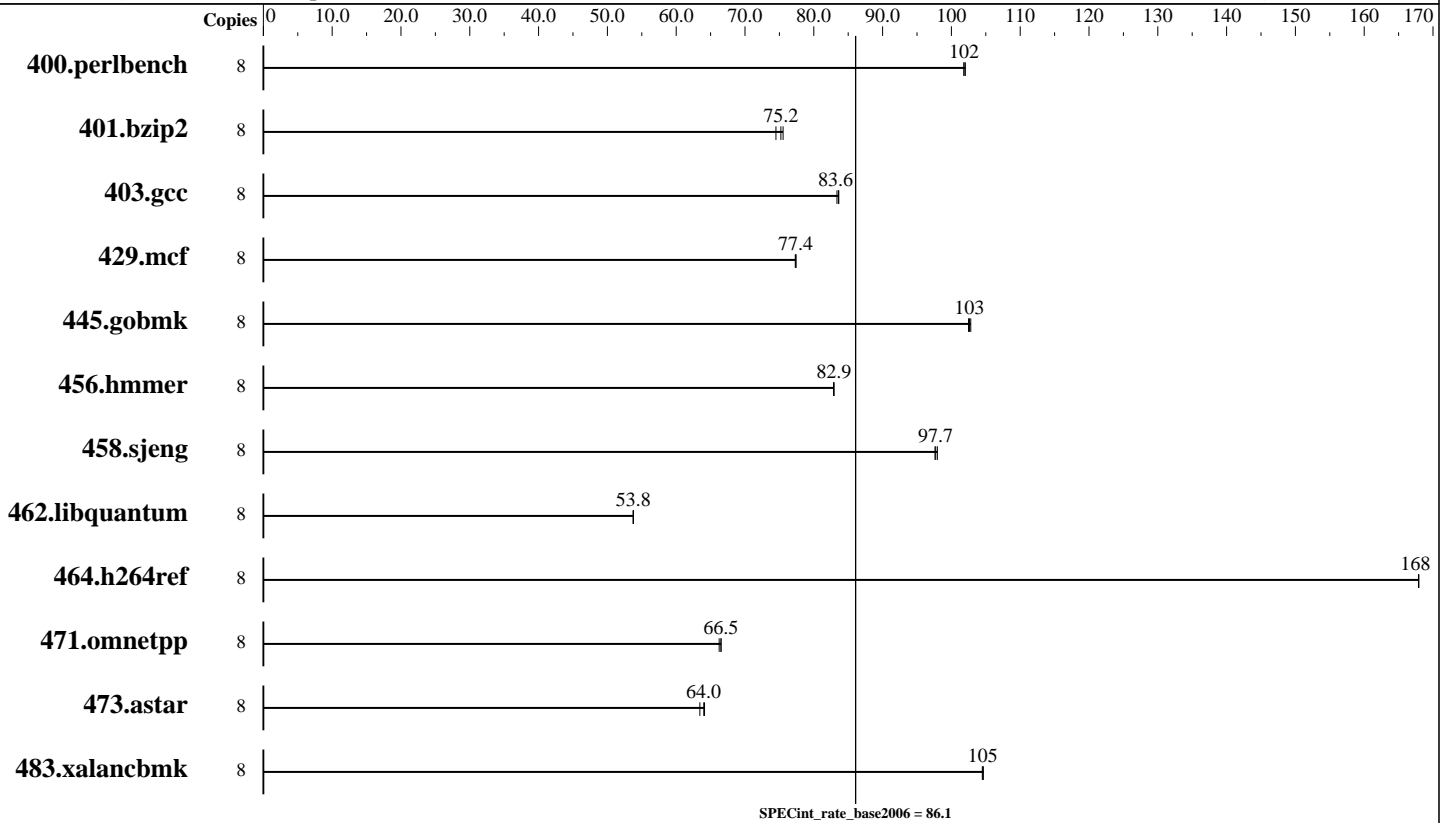
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Nov-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 1995  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64), kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	766	102	<b><u>767</u></b>	<b><u>102</u></b>	768	102							
401.bzip2	8	1036	74.5	1022	75.5	<b><u>1026</u></b>	<b><u>75.2</u></b>							
403.gcc	8	<b><u>771</u></b>	<b><u>83.6</u></b>	772	83.4	770	83.6							
429.mcf	8	<b><u>943</u></b>	<b><u>77.4</u></b>	944	77.3	942	77.4							
445.gobmk	8	<b><u>818</u></b>	<b><u>103</u></b>	819	102	816	103							
456.hammer	8	<b><u>900</u></b>	<b><u>82.9</u></b>	901	82.9	900	82.9							
458.sjeng	8	<b><u>991</u></b>	<b><u>97.7</u></b>	988	98.0	992	97.6							
462.libquantum	8	3084	53.8	3082	53.8	<b><u>3084</u></b>	<b><u>53.8</u></b>							
464.h264ref	8	<b><u>1054</u></b>	<b><u>168</u></b>	1054	168	1055	168							
471.omnetpp	8	752	66.5	<b><u>752</u></b>	<b><u>66.5</u></b>	755	66.2							
473.astar	8	885	63.4	876	64.1	<b><u>877</u></b>	<b><u>64.0</u></b>							
483.xalancbmk	8	528	105	<b><u>528</u></b>	<b><u>105</u></b>	528	104							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode  
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 64M  
Powersaved dameon was disabled in OS  
taskset utility used to bind CPU(s) to processes

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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