SPEC® CFP2006 Result

Supermicro

(Test Sponsor: Intel Corporation)

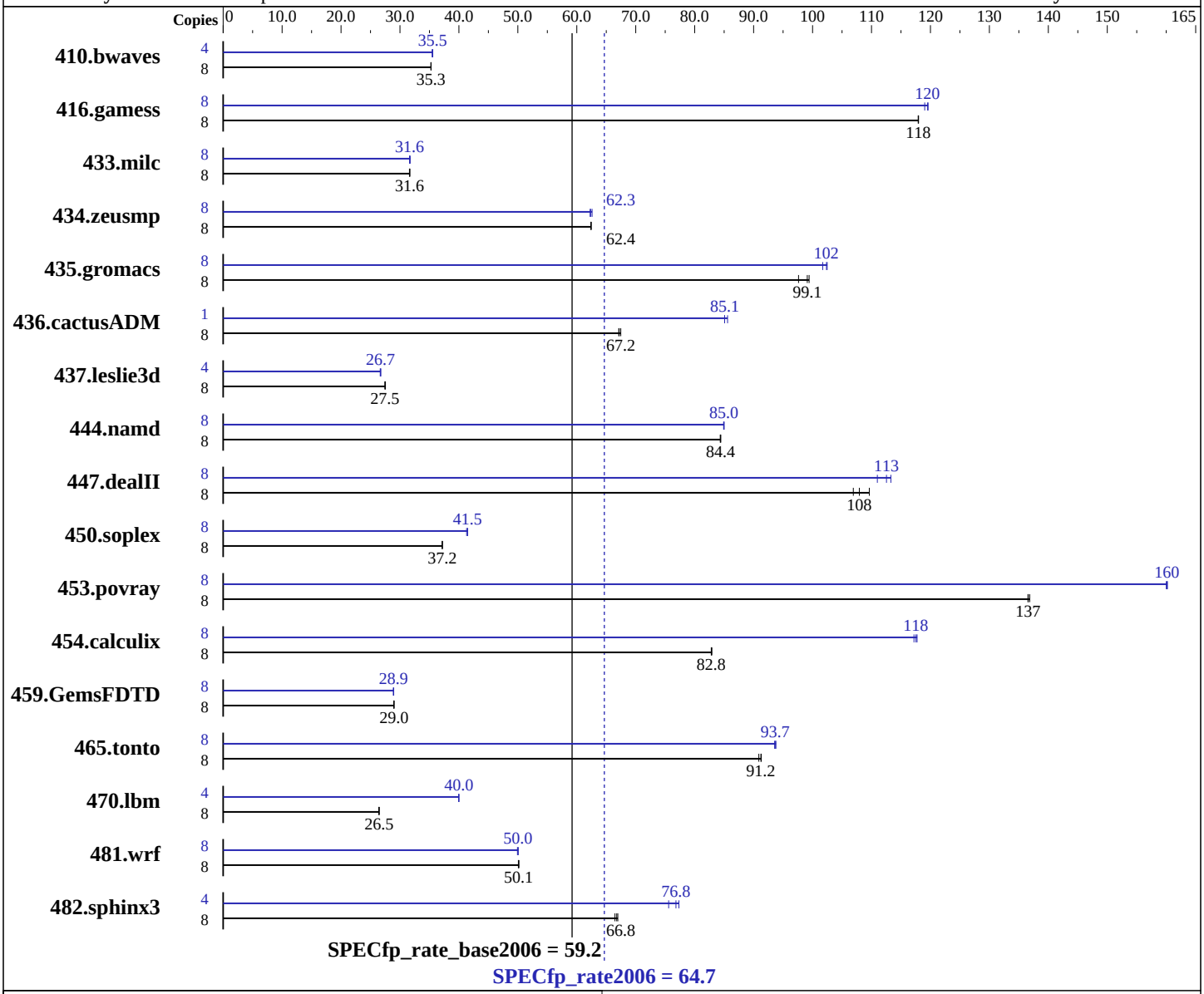
Supermicro X7DB8+ (Intel Xeon processor E5405, 2.00 GHz)

SPECfp®_rate2006 = 64.7

SPECfp_rate_base2006 = 59.2

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	Quad Core, 2.00 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel linux-cbgm 2.6.16.43-0.5-smp for x86_64
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 * 2GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73GB Seagate ST37330LC SCSI 10K RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3084	35.3	**3083**	**35.3**	3082	35.3	4	1533	35.5	**1530**	**35.5**	1528	35.6
416.gamess	8	**1328**	**118**	1328	118	1328	118	8	**1311**	**120**	1310	120	1316	119
433.milc	8	2318	31.7	2324	31.6	**2321**	**31.6**	8	2322	31.6	**2321**	**31.6**	2315	31.7
434.zeusmp	8	1166	62.4	1167	62.4	**1167**	**62.4**	8	**1168**	**62.3**	1163	62.6	1169	62.3
435.gromacs	8	575	99.4	**576**	**99.1**	585	97.6	8	562	102	557	102	**558**	**102**
436.cactusADM	8	1424	67.1	**1423**	**67.2**	1417	67.5	1	**140**	**85.1**	140	85.6	140	85.1
437.leslie3d	8	2740	27.4	**2738**	**27.5**	2731	27.5	4	1405	26.8	**1408**	**26.7**	1411	26.6
444.namd	8	**760**	**84.4**	761	84.3	760	84.4	8	**755**	**85.0**	755	85.0	756	84.9
447.dealII	8	**848**	**108**	856	107	835	110	8	**813**	**113**	808	113	825	111
450.soplex	8	1794	37.2	**1795**	**37.2**	1795	37.2	8	1609	41.5	**1609**	**41.5**	1615	41.3
453.povray	8	312	137	**312**	**137**	311	137	8	266	160	**266**	**160**	266	160
454.calculix	8	**797**	**82.8**	797	82.8	796	82.9	8	561	118	**562**	**118**	563	117
459.GemsFDTD	8	2933	28.9	2927	29.0	**2928**	**29.0**	8	**2935**	**28.9**	2940	28.9	2933	28.9
465.tonto	8	862	91.3	**863**	**91.2**	866	90.9	8	839	93.8	841	93.6	**840**	**93.7**
470.lbm	8	**4153**	**26.5**	4152	26.5	4164	26.4	4	1374	40.0	1375	40.0	**1374**	**40.0**
481.wrf	8	**1783**	**50.1**	1784	50.1	1781	50.2	8	1790	49.9	**1786**	**50.0**	1785	50.1
482.sphinx3	8	2329	67.0	**2335**	**66.8**	2345	66.5	4	**1015**	**76.8**	1008	77.3	1031	75.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Disabled
Adjacent Sector Prefetch: Disabled
 All benchmarks compiled in 64-bit mode except 437.leslie3d, 450.soplex
 470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
The taskset utility was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icc
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib -I/opt/intel/fc/10.1.008/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: `-fast -unroll2`

C++ benchmarks:

444.namd: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

447.dealII: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias -scalar-rep-`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-fast -prefetch`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -opt-malloc-options=3`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -prefetch`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -parallel -auto-ilp32`

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

481.wrf: `-fast -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.06.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.06.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:40:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 December 2007.