



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor E5430, 2.66 GHz)

SPECint®2006 = 24.0

SPECint_base2006 = 20.9

CPU2006 license: 13

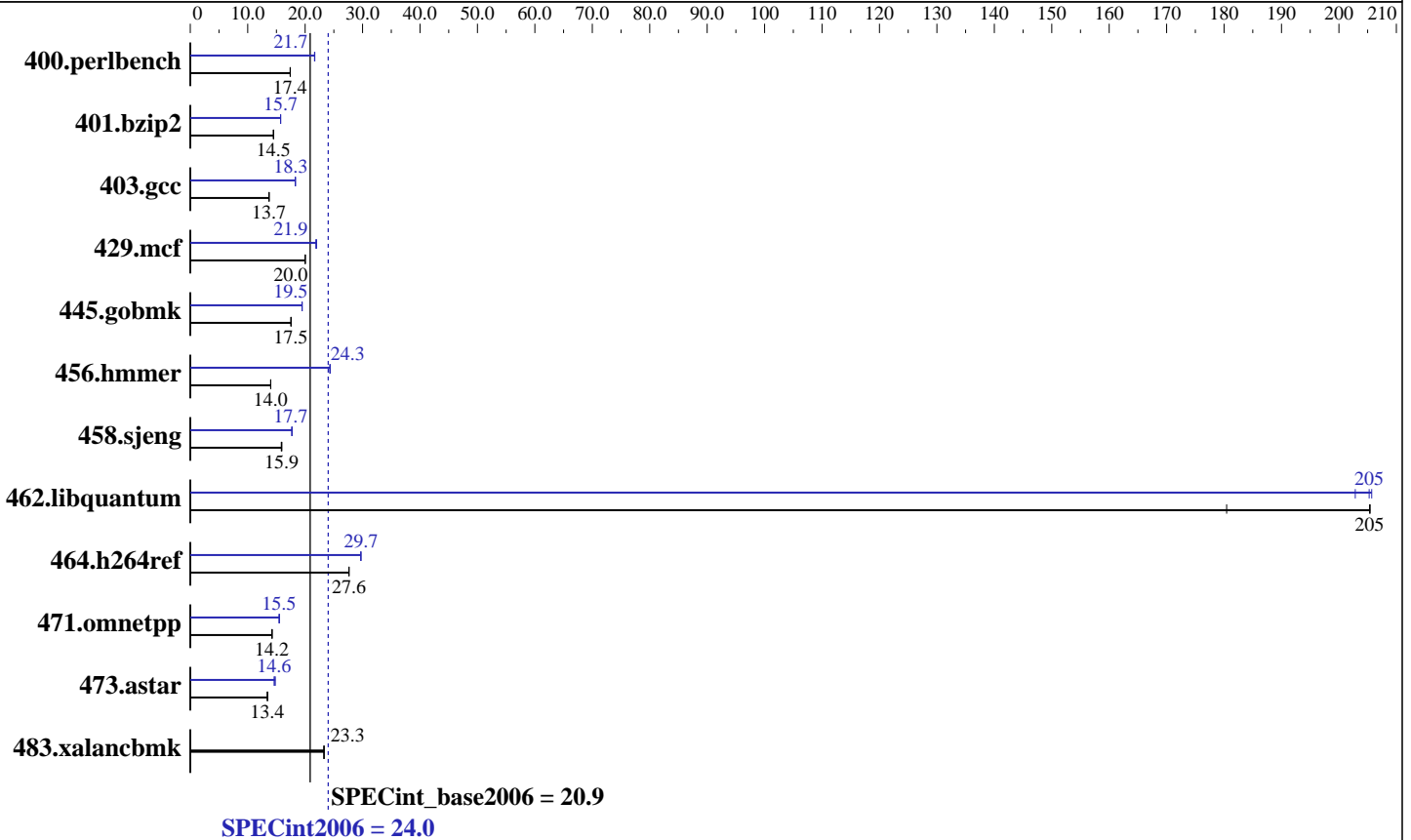
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: Quad Core, 2.66 GHz
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73GB Seagate ST37330LC SCSI 10K RPM
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel
 linux-cbgn 2.6.16.43-0.5-smp for x86_64
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1
 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>561</u>	<u>17.4</u>	561	17.4	562	17.4	451	21.7	451	21.6	<u>451</u>	<u>21.7</u>
401.bzip2	665	14.5	668	14.4	<u>665</u>	<u>14.5</u>	<u>614</u>	<u>15.7</u>	613	15.7	614	15.7
403.gcc	588	13.7	587	13.7	<u>587</u>	<u>13.7</u>	440	18.3	<u>440</u>	<u>18.3</u>	440	18.3
429.mcf	456	20.0	<u>456</u>	<u>20.0</u>	456	20.0	<u>416</u>	<u>21.9</u>	415	22.0	417	21.9
445.gobmk	598	17.5	<u>598</u>	<u>17.5</u>	598	17.5	539	19.5	<u>539</u>	<u>19.5</u>	539	19.5
456.hmmer	<u>666</u>	<u>14.0</u>	666	14.0	666	14.0	<u>383</u>	<u>24.3</u>	383	24.4	384	24.3
458.sjeng	764	15.8	<u>761</u>	<u>15.9</u>	759	15.9	685	17.7	<u>683</u>	<u>17.7</u>	683	17.7
462.libquantum	115	180	101	205	<u>101</u>	<u>205</u>	<u>101</u>	<u>205</u>	101	206	102	203
464.h264ref	<u>801</u>	<u>27.6</u>	801	27.6	801	27.6	745	29.7	745	29.7	<u>745</u>	<u>29.7</u>
471.omnetpp	<u>439</u>	<u>14.2</u>	439	14.2	439	14.2	403	15.5	404	15.5	<u>404</u>	<u>15.5</u>
473.astar	521	13.5	<u>523</u>	<u>13.4</u>	524	13.4	480	14.6	474	14.8	<u>480</u>	<u>14.6</u>
483.xalanbmk	<u>296</u>	<u>23.3</u>	296	23.3	297	23.2	<u>296</u>	<u>23.3</u>	296	23.3	297	23.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Enabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-fast -vec-guard-write -parallel -par-runtime-control`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include`

`456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:

`icpc`

Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.36.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.36.xml>

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For other inquiries, please contact webmaster@spec.org.

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